

# VS-10TQ035SPbF, VS-10TQ045SPbF

# Vishay Semiconductors

ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CO	VALUES	UNITS	
		10 A	T <sub>.1</sub> = 25 °C	0.57	
Maximum forward voltage drop	V <sub>FM</sub> <sup>(1)</sup>	20 A	1J=25 C	0.67	V
See fig. 1		10 A	T <sub>.1</sub> = 125 °C	0.49	
		20 A	1J = 125 C	0.61	
Maximum reverse leakage current See fig. 2	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	V <sub>R</sub> = Rated V <sub>R</sub>	2	- mA
		T <sub>J</sub> = 125 °C	v <sub>R</sub> = nateu v <sub>R</sub>	15	
Maximum junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 5 V <sub>DC</sub> (test signal ran	ge 100 kHz to 1 MHz), 25 °C	900	pF
Typical series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body		8.0	nH
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>		10 000	V/µs

#### Note

 $<sup>^{(1)}\,</sup>$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL TEST CONDITIONS		VALUES	UNITS
Maximum junction and storage temperature range		T <sub>J</sub> , T <sub>Stg</sub>		-55 to +175	°C
Maximum thermal resistance, junction to case		$R_{thJC}$	DC operation 2.0		°C/W
Typical thermal resistance, case to heatsink		R <sub>thCS</sub>	Mounting surface, smooth and greased	0.50	C/VV
Approximate weight				2	g
Approximate weight				0.07	OZ.
Mounting torque	minimum			6 (5)	kgf · cm
	maximum			12 (10)	(lbf · in)
Marking device			Case style D <sup>2</sup> PAK	10TQ	045S



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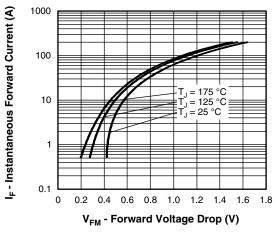


Fig. 1 - Maximum Forward Voltage Drop Characteristics

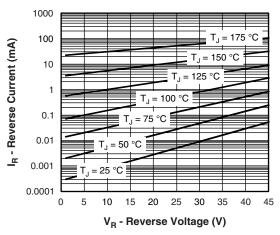


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

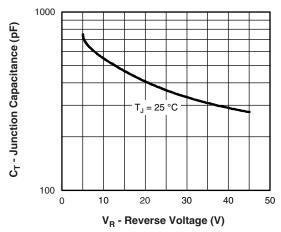


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

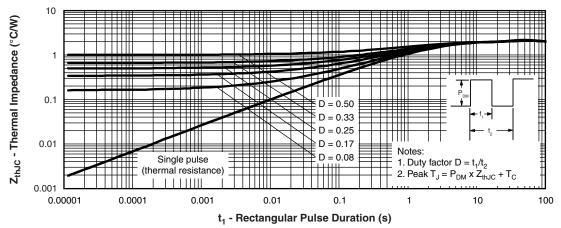


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics





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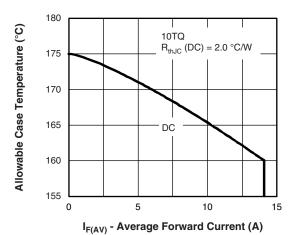


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

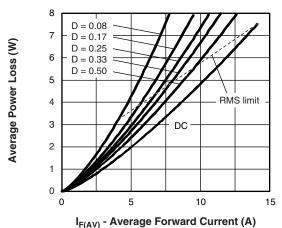


Fig. 6 - Forward Power Loss Characteristics

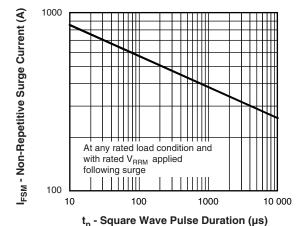


Fig. 7 - Maximum Non-Repetitive Surge Current

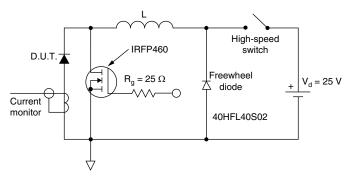


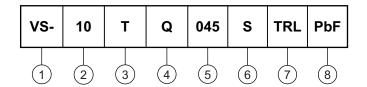
Fig. 8 - Unclamped Inductive Test Circuit

## VS-10TQ035SPbF, VS-10TQ045SPbF

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#### **ORDERING INFORMATION TABLE**

**Device code** 



1 - Vishay Semiconductors product

2 - Current rating (10 A)

Circuit configuration: T = D<sup>2</sup>PAK

4 - Schottky "Q" series

- Voltage ratings - 035 = 35 V 045 = 45 V

6 - S = D<sup>2</sup>PAK

7 - • None = tube (50 pieces)

• TRL = tape and reel (left oriented)

• TRR = tape and reel (right oriented)

PbF = lead (Pb)-free

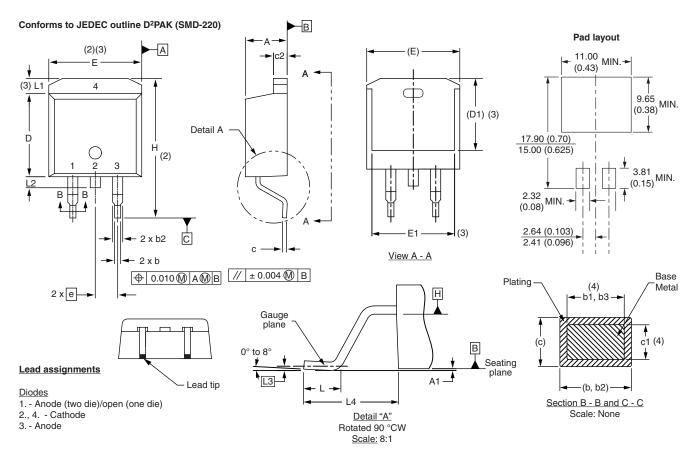
LINKS TO RELAT	ED DOCUMENTS
Dimensions	www.vishay.com/doc?95014
Part marking information	www.vishay.com/doc?95008
Packaging information	www.vishay.com/doc?95032



## Vishay Semiconductors

# **D<sup>2</sup>PAK, TO-262**

#### **DIMENSIONS - D<sup>2</sup>PAK** in millimeters and inches



	1		1		
SYMBOL	MILLIMETERS		INCHES		NOTES
	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.06	4.83	0.160	0.190	
A1	0.00	0.254	0.000	0.010	
b	0.51	0.99	0.020	0.039	
b1	0.51	0.89	0.020	0.035	4
b2	1.14	1.78	0.045	0.070	
b3	1.14	1.73	0.045	0.068	4
С	0.38	0.74	0.015	0.029	
c1	0.38	0.58	0.015	0.023	4
c2	1.14	1.65	0.045	0.065	
D	8.51	9.65	0.335	0.380	2

SYMBOL	MILLIMETERS		INC	NOTES	
	MIN.	MAX.	MIN.	MAX.	NOTES
D1	6.86	8.00	0.270	0.315	3
E	9.65	10.67	0.380	0.420	2, 3
E1	7.90	8.80	0.311	0.346	3
е	2.54 BSC		0.100 BSC		
Н	14.61	15.88	0.575	0.625	
L	1.78	2.79	0.070	0.110	
L1	-	1.65	-	0.066	3
L2	1.27	1.78	0.050	0.070	
L3	0.25 BSC		0.010 BSC		
L4	4.78	5.28	0.188	0.208	

#### Notes

- (1) Dimensioning and tolerancing per ASME Y14.5 M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- $^{(3)}\,$  Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Datum A and B to be determined at datum plane H
- (6) Controlling dimension: inch

(7) Outline conforms to JEDEC outline TO-263AB

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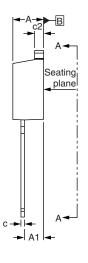
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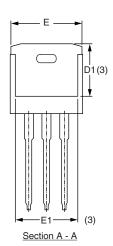
D<sup>2</sup>PAK, TO-262



#### **DIMENSIONS - TO-262** in millimeters and inches

# 





**⊕** 0.010 **M** A **M** B

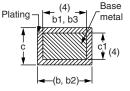
#### Lead assignments



#### **Diodes**

1. - Anode (two die)/open (one die) 2., 4. - Cathode

3. - Anode



Section B - B and C - C Scale: None

SYMBOL	MILLIM	IETERS	INC	NOTES	
	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.06	4.83	0.160	0.190	
A1	2.03	3.02	0.080	0.119	
b	0.51	0.99	0.020	0.039	
b1	0.51	0.89	0.020	0.035	4
b2	1.14	1.78	0.045	0.070	
b3	1.14	1.73	0.045	0.068	4
С	0.38	0.74	0.015	0.029	
c1	0.38	0.58	0.015	0.023	4
c2	1.14	1.65	0.045	0.065	
D	8.51	9.65	0.335	0.380	2
D1	6.86	8.00	0.270	0.315	3
E	9.65	10.67	0.380	0.420	2, 3
E1	7.90	8.80	0.311	0.346	3
е	2.54 BSC		0.100 BSC		
L	13.46	14.10	0.530	0.555	
L1	=	1.65	-	0.065	3
L2	3.56	3.71	0.140	0.146	

#### Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- (3) Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Controlling dimension: inches

(6) Outline conform to JEDEC TO-262 except A1 (maximum), b (minimum) and D1 (minimum) where dimensions derived the actual package outline

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