

**Maximum Ratings** @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V <sub>CBO</sub>	-15	V
Collector-Emitter Voltage	V <sub>CEO</sub>	-12	V
Emitter-Base Voltage	V <sub>EBO</sub>	-6	V
Peak Pulse Current	I <sub>CM</sub>	-4	A
Continuous Collector Current	I <sub>C</sub>	-2	A

**Thermal Characteristics** @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 4)	P <sub>D</sub>	0.9	W
Thermal Resistance, Junction to Ambient Air (Note 4)	R <sub>θJA</sub>	139	°C/W
Power Dissipation (Note 5)	P <sub>D</sub>	2	W
Thermal Resistance, Junction to Ambient Air (Note 5)	R <sub>θJA</sub>	62.5	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

**Electrical Characteristics** @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Min	Typ	Max	Unit	Conditions
<b>OFF CHARACTERISTICS</b>						
Collector-Base Breakdown Voltage	V <sub>(BR)CBO</sub>	-15	—	—	V	I <sub>C</sub> = -100μA, I <sub>E</sub> = 0
Collector-Emitter Breakdown Voltage (Note 6)	V <sub>(BR)CEO</sub>	-12	—	—	V	I <sub>C</sub> = -10mA, I <sub>B</sub> = 0
Emitter-Base Breakdown Voltage	V <sub>(BR)EBO</sub>	-6	—	—	V	I <sub>E</sub> = -100μA, I <sub>C</sub> = 0
Collector Cut-Off Current	I <sub>CBO</sub>	—	—	-0.1	μA	V <sub>CB</sub> = -15V, I <sub>E</sub> = 0
Emitter Cut-Off Current	I <sub>EBO</sub>	—	—	-0.1	μA	V <sub>EB</sub> = -6V, I <sub>C</sub> = 0
<b>ON CHARACTERISTICS (Note 6)</b>						
Collector-Emitter Saturation Voltage	V <sub>CE(SAT)</sub>	—	-65	-180	mV	I <sub>C</sub> = -1A, I <sub>B</sub> = -50mA
DC Current Gain	h <sub>FE</sub>	270	—	680	—	V <sub>CE</sub> = -2V, I <sub>C</sub> = -200mA
<b>SMALL SIGNAL CHARACTERISTICS</b>						
Output Capacitance	C <sub>obo</sub>	—	40	—	pF	V <sub>CB</sub> = -10V, I <sub>E</sub> = 0, f = 1MHz
Current Gain-Bandwidth Product	f <sub>T</sub>	—	140	—	MHz	V <sub>CE</sub> = -2V, I <sub>C</sub> = -100mA, f = 100MHz

- Notes: 4. Device mounted on FR-4 PCB with minimum recommended pad layout.  
 5. Device mounted on FR-4 PCB with 1 inch<sup>2</sup> copper pad layout.  
 6. Measured under pulsed conditions. Pulse width = 300μs. Duty cycle ≤2%.

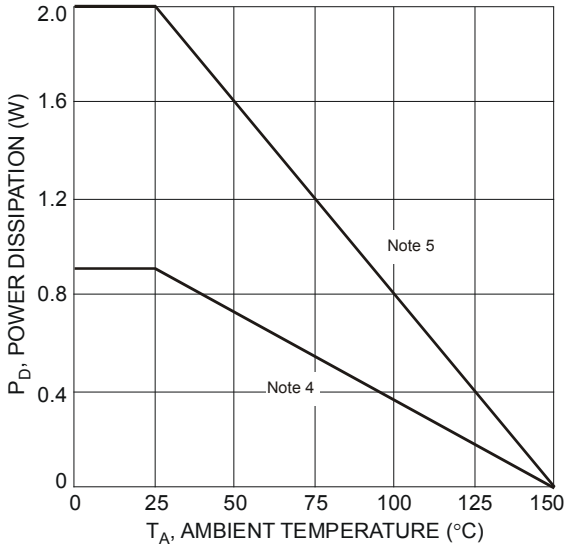


Fig. 1 Power Dissipation vs. Ambient Temperature

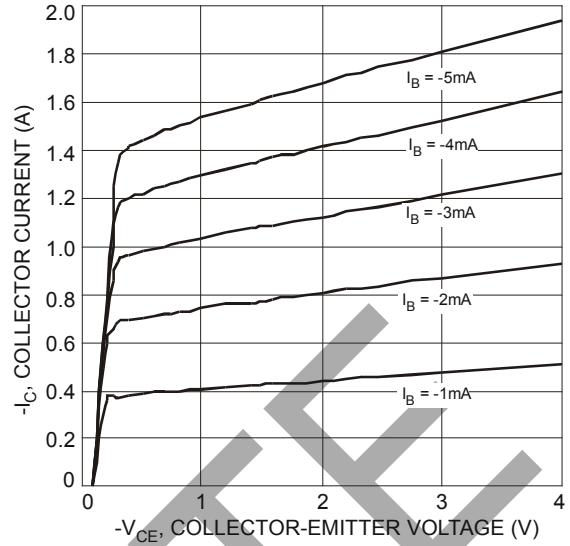


Fig. 2 Typical Collector Current vs. Collector-Emitter Voltage

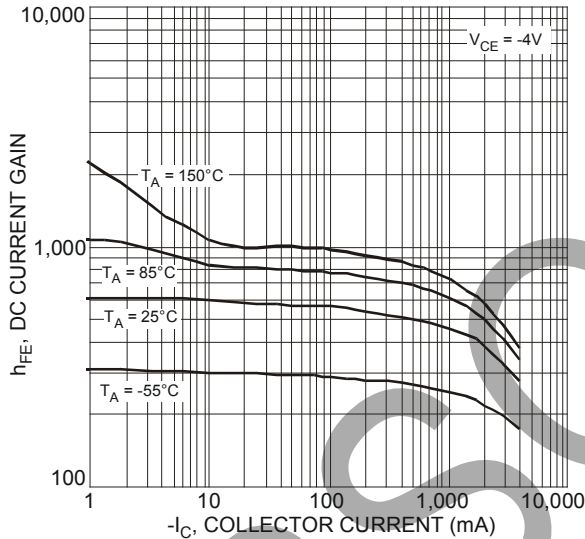


Fig. 3 Typical DC Current Gain vs. Collector Current

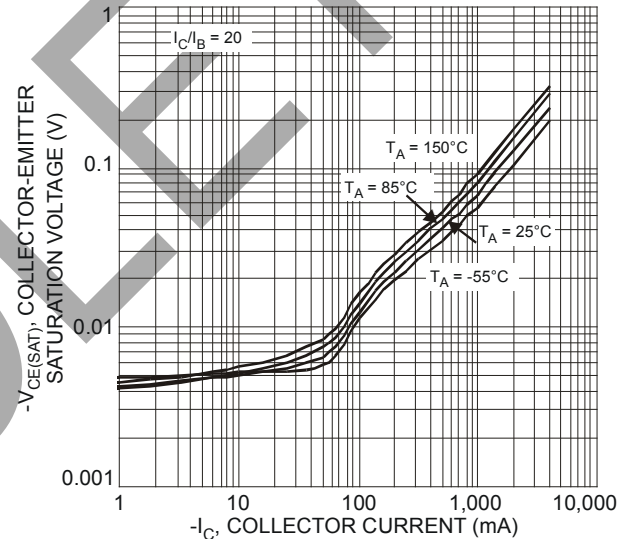


Fig. 4 Typical Collector-Emitter Saturation Voltage vs. Collector Current

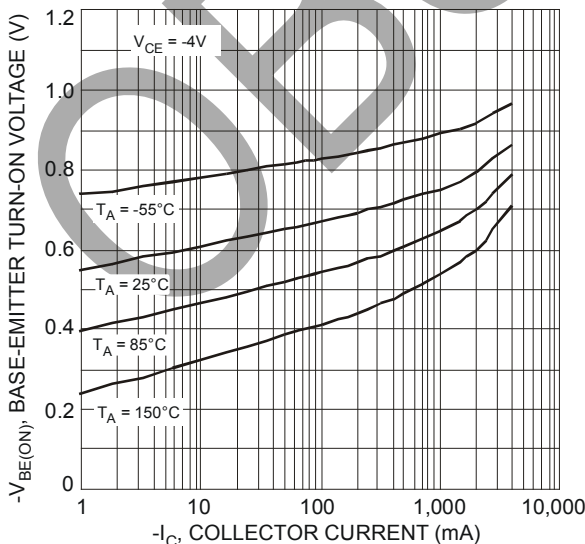


Fig. 5 Typical Base-Emitter Turn-On Voltage vs. Collector Current

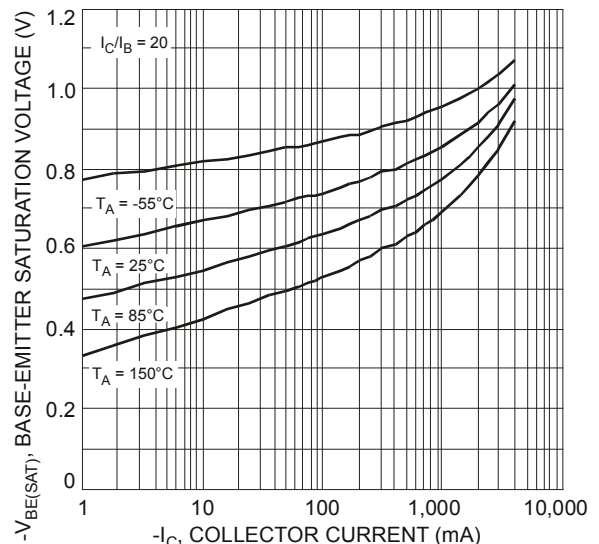


Fig. 6 Typical Base-Emitter Saturation Voltage vs. Collector Current

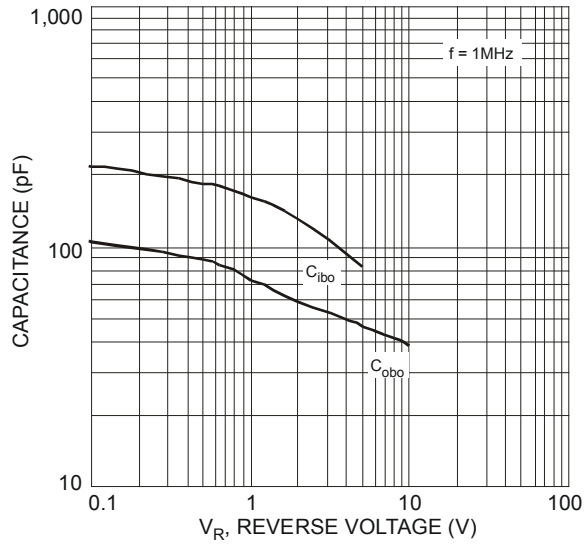


Fig. 7 Typical Capacitance Characteristics

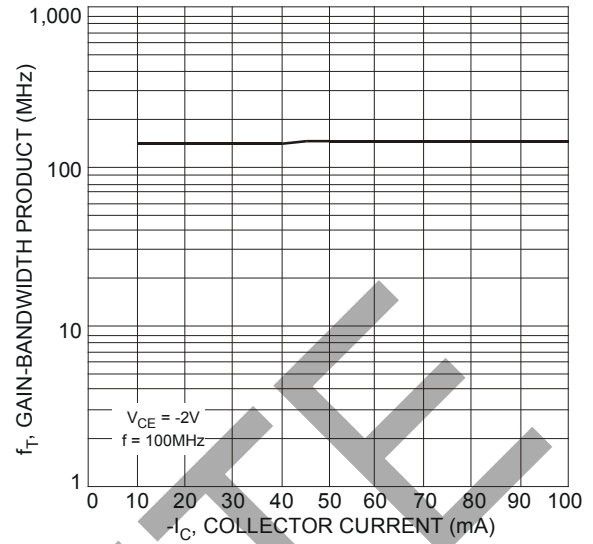
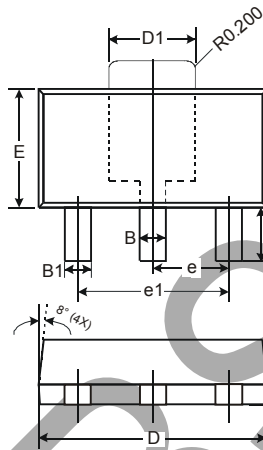


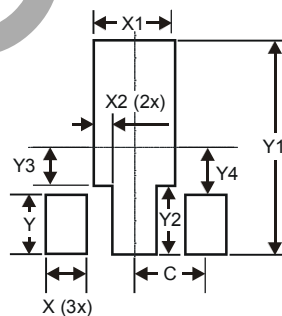
Fig. 8 Typical Gain-Bandwidth Product vs. Collector Current

## Package Outline Dimensions



SOT89		
Dim	Min	Max
A	1.40	1.60
B	0.44	0.62
B1	0.35	0.54
C	0.35	0.43
D	4.40	4.60
D1	1.52	1.83
E	2.29	2.60
e	1.50 Typ	
e1	3.00 Typ	
H	3.94	4.25
L	0.89	1.20
All Dimensions in mm		

## Suggested Pad Layout



Dimensions	Value (in mm)
X	0.900
X1	1.733
X2	0.416
Y	1.300
Y1	4.600
Y2	1.475
Y3	0.950
Y4	1.125
C	1.500

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