Vishay High Power Products

Schottky Rectifier, 2 A



ELECTRICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS		
Maximum forward voltage drop	V _{FM} ⁽¹⁾	2 A	T _J = 25 °C	0.470	V		
		4 A		0.550			
		2 A	T _J = 125 °C	0.370			
		4 A		0.470			
Maximum reverse leakage current	I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	0.5	- mA		
		T _J = 125 °C		15			
Maximum junction capacitance	C _T	V_R = 5 V_{DC} , (test signal range 100 kHz to 1 MHz) 25 °C		200	pF		
Typical series inductance	L _S	Measured lead to lead 5 mm from package body		2.0	nΗ		
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs		

Note

 $^{^{(1)}}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Maximum junction and storage temperature range	T _J ⁽¹⁾ , T _{Stg}		- 55 to 150	°C		
Maximum thermal resistance, junction to lead	R _{thJL} ⁽²⁾	DC operation	25	25 °C/W		
Maximum thermal resistance, junction to ambient	R _{thJA}		80	C/VV		
Approximate weight			0.10	g		
			0.003	OZ.		
Marking device		Case style SMB (similar DO-214AA)	V2E			

Notes

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For technical questions, contact: diodes-tech@vishay.com

⁽¹⁾ $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink

⁽²⁾ Mounted 1" square PCB



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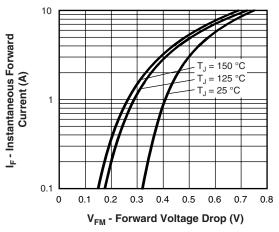


Fig. 1 - Maximum Forward Voltage Drop Characteristics

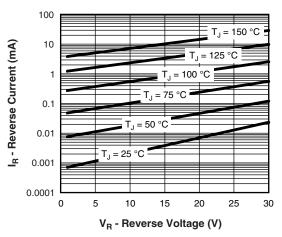


Fig. 2 - Typical Peak Reverse Current vs. Reverse Voltage

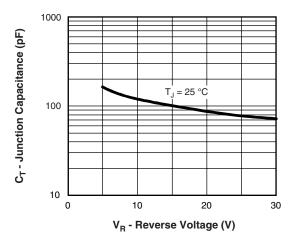


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

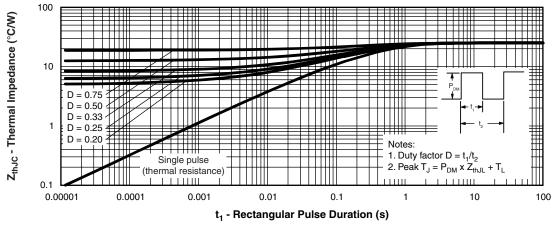


Fig. 4 - Maximum Thermal Impedance Z_{thJL} Characteristics

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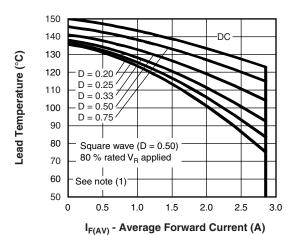


Fig. 5 - Maximum Average Forward Current vs. Allowable Lead Temperature

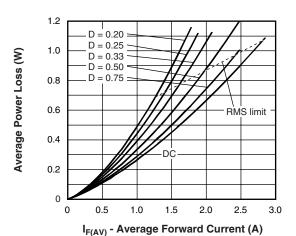


Fig. 6 - Maximum Average Forward Dissipation vs.
Average Forward Current

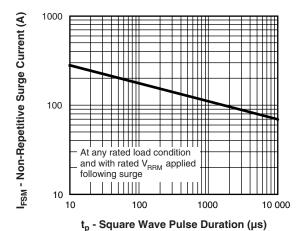


Fig. 7 - Maximum Peak Surge Forward Current vs. Pulse Duration

Note

 $\begin{array}{l} \text{(1)} \ \ \text{Formula used:} \ T_L = T_J - (Pd + Pd_{REV}) \ x \ R_{th,JL}; \\ Pd = \text{Forward power loss} = I_{F(AV)} \ x \ V_{FM} \ \text{at} \ (I_{F(AV)}/D) \ \text{(see fig. 6)}; \\ Pd_{REV} = \text{Inverse power loss} = V_{R1} \ x \ I_R \ (1 - D); \ I_R \ \text{at} \ V_{R1} = 80 \ \% \ \text{rated} \ V_R \\ \end{array}$

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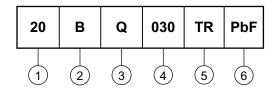
For technical questions, contact: diodes-tech@vishay.com



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ORDERING INFORMATION TABLE

Device code



1 - Current rating

B = Single lead diode

Gailing - Q = Schottky "Q" series

Voltage rating (030 = 30 V)

5 - • None = Box (1000 pieces)

• TR = Tape and reel (3000 pieces)

• None = Standard production

• PbF = Lead (Pb)-free

LINKS TO RELATED DOCUMENTS				
Dimensions	http://www.vishay.com/doc?95017			
Part marking information	http://www.vishay.com/doc?95029			
Packaging information	http://www.vishay.com/doc?95034			
SPICE model	http://www.vishay.com/doc?95284			



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