# **MAXIMUM RATINGS\*** (T<sub>J</sub> = 25°C unless otherwise noted)

Rating	Symbol	Value	Unit
Peak Repetitive Off-State Voltage (Note 1)	$V_{DRM,}$		V
$(T_J = -40 \text{ to } 125^{\circ}\text{C}, \text{ Sine Wave } 50 \text{ to } 60 \text{ Hz; Gate Open)}$	$V_{RRM}$		
2N6400		50	
2N6401		100	
2N6402		200	
2N6403		400	
2N6404		600	
2N6405		800	
On-State Current RMS (180° Conduction Angles; T <sub>C</sub> = 100°C)	I <sub>T(RMS)</sub>	16	Α
Average On-State Current (180° Conduction Angles; T <sub>C</sub> = 100°C)	I <sub>T(AV)</sub>	10	Α
Peak Non-repetitive Surge Current (1/2 Cycle, Sine Wave 60 Hz, T <sub>J</sub> = 90°C)	I <sub>TSM</sub>	160	Α
Circuit Fusing Considerations (t = 8.3 ms)	l <sup>2</sup> t	145	A <sup>2</sup> s
Forward Peak Gate Power (Pulse Width $\leq$ 1.0 $\mu$ s, T <sub>C</sub> = 100°C)	P <sub>GM</sub>	20	W
Forward Average Gate Power (t = 8.3 ms, T <sub>C</sub> = 100°C)	$P_{G(AV)}$	0.5	W
Forward Peak Gate Current (Pulse Width ≤ 1.0 μs, T <sub>C</sub> = 100°C)	I <sub>GM</sub>	2.0	Α
Operating Junction Temperature Range	T <sub>J</sub>	-40 to +125	°C
Storage Temperature Range	T <sub>stg</sub>	-40 to +150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

## THERMAL CHARACTERISTICS

Characteristic		Max	Unit
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	1.5	°C/W
Maximum Lead Temperature for Soldering Purposes 1/8 in from Case for 10 Seconds		260	°C

## **ELECTRICAL CHARACTERISTICS** (T<sub>C</sub> = 25°C unless otherwise noted.)

Critical Rate-of-Rise of Off-State Voltage (V<sub>D</sub> = Rated V<sub>DRM</sub>, Exponential Waveform)

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS					
*Peak Repetitive Forward or Reverse Blocking Current ( $V_{AK}$ = Rated $V_{DRM}$ or $V_{RRM}$ , Gate Open) $T_J$ = 25°C $T_J$ = 125°C	I <sub>DRM</sub> , I <sub>RRM</sub>	- -	- -	10 2.0	μA mA
ON CHARACTERISTICS					
*Peak Forward On-State Voltage (I $_{\text{TM}}$ = 32 A Peak, Pulse Width $\leq$ 1 ms, Duty Cycle $\leq$ 2%)	$V_{TM}$	-	-	1.7	V
*Gate Trigger Current (Continuous dc) $T_C = 25^{\circ}C$ $(V_D = 12 \text{ Vdc}, R_L = 100 \Omega)$ $T_C = -40^{\circ}C$	I <sub>GT</sub>	- -	9.0	30 60	mA
*Gate Trigger Voltage (Continuous dc) $ (V_D = 12 \text{ Vdc},  R_L = 100  \Omega) \qquad \qquad T_C = 25^{\circ}\text{C} \\ T_C = -40^{\circ}\text{C} $	V <sub>GT</sub>	- -	0.7	1.5 2.5	V
Gate Non-Trigger Voltage ( $V_D$ = 12 Vdc, $R_L$ = 100 $\Omega$ ), $T_C$ = +125 $^{\circ}$ C	$V_{GD}$	0.2	-	-	V
*Holding Current $T_C = 25^{\circ}C$ (V <sub>D</sub> = 12 Vdc, Initiating Current = 200 mA, Gate Open) $^{*}T_C = -40^{\circ}C$	lн	-	18	40 60	mA
Turn-On Time ( $I_{TM} = 16 \text{ A}$ , $I_{GT} = 40 \text{ mAdc}$ , $V_D = \text{Rated } V_{DRM}$ )	t <sub>gt</sub>	-	1.0	-	μS
Turn-Off Time ( $I_{TM}$ = 16 A, $I_R$ = 16 A, $V_D$ = Rated $V_{DRM}$ ) $T_C = 25^{\circ}C$ $T_{,1} = +125^{\circ}C$		-	15 35		μS

<sup>\*</sup>Indicates JEDEC Registered Data.

 $T_J = +125^{\circ}C$ 

dv/dt

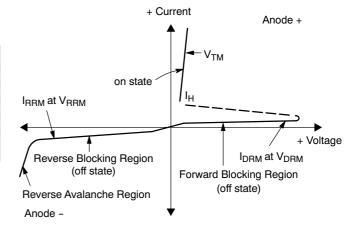
50

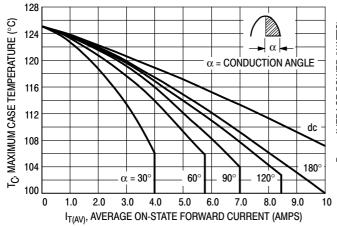
V/μs

V<sub>DRM</sub> and V<sub>RRM</sub> for all types can be applied on a continuous basis. Ratings apply for zero or negative gate voltage; however, positive gate voltage shall not be applied concurrent with negative potential on the anode. Blocking voltages shall not be tested with a constant current source such that the voltage ratings of the devices are exceeded.

# **Voltage Current Characteristic of SCR**

Symbol	Parameter
V <sub>DRM</sub>	Peak Repetitive Off State Forward Voltage
I <sub>DRM</sub>	Peak Forward Blocking Current
V <sub>RRM</sub>	Peak Repetitive Off State Reverse Voltage
I <sub>RRM</sub>	Peak Reverse Blocking Current
$V_{TM}$	Peak On State Voltage
I <sub>H</sub>	Holding Current





n, average on-state Forward current (am

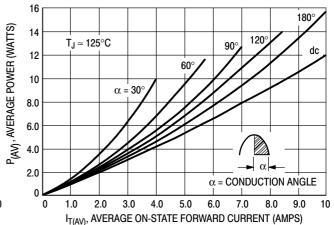


Figure 2. Maximum On-State Power Dissipation

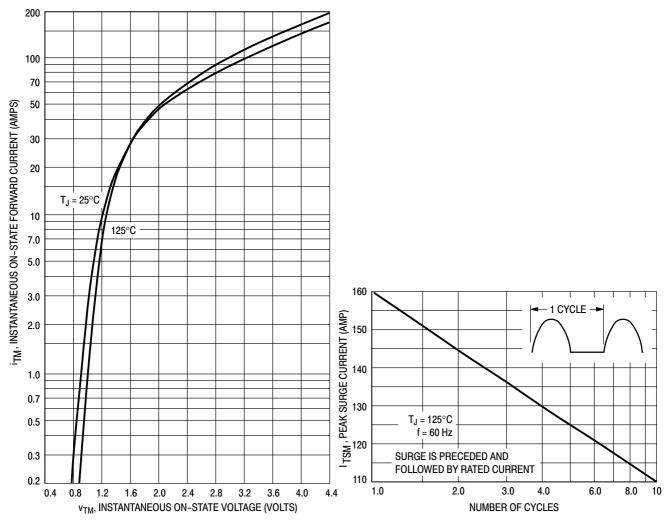


Figure 3. On-State Characteristics

Figure 4. Maximum Non-Repetitive Surge Current

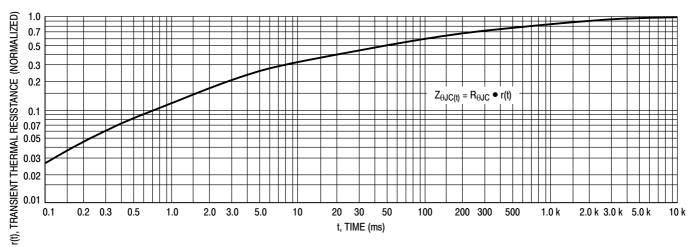


Figure 5. Thermal Response

# **TYPICAL CHARACTERISTICS**

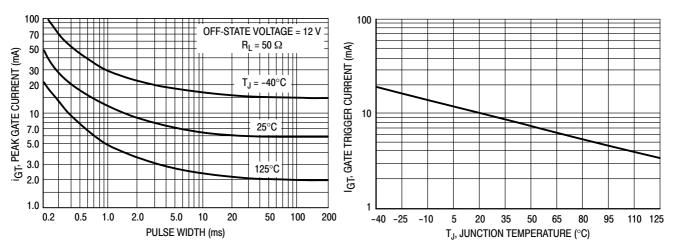


Figure 6. Typical Gate Trigger Current versus Pulse Width

Figure 7. Typical Gate Trigger Current versus Junction Temperature

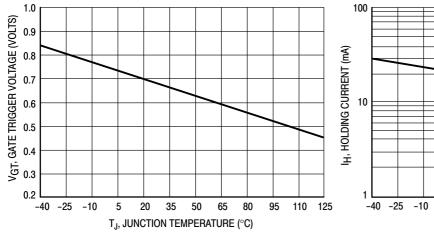


Figure 8. Typical Gate Trigger Voltage versus Junction Temperature

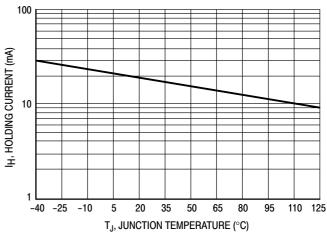


Figure 9. Typical Holding Current versus Junction Temperature

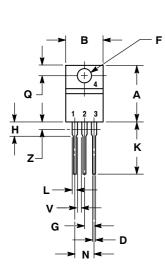
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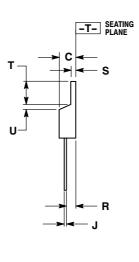
Device	Package	Shipping <sup>†</sup>		
2N6400	TO-220AB			
2N6400G	TO-220AB (Pb-Free)			
2N6401	TO-220AB			
2N6401G	TO-220AB (Pb-Free)	500 Helle / Dece		
2N6402	TO-220AB	500 Units / Box		
2N6402G	TO-220AB (Pb-Free)			
2N6403	TO-220AB			
2N6403G	TO-220AB (Pb-Free)			
2N6403TG	TO-220AB (Pb-Free)	50 Units / Rail		
2N6404	TO-220AB			
2N6404G	TO-220AB (Pb-Free)	500 Units / Box		
2N6405	TO-220AB			
2N6405G	TO-220AB (Pb-Free)			

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

### PACKAGE DIMENSIONS

## TO-220AB CASE 221A-07 ISSUE AA





#### NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ANSI Y14 5M 1982
- 114.3M, 1902.

  2. CONTROLLING DIMENSION: INCH.

  3. DIMENSION Z DEFINES A ZONE WHERE ALL BODY AND LEAD IRREGULARITIES ARE ALLOWED.

	INCHES		MILLIMETERS	
DIM	MIN	MAX	MIN	MAX
Α	0.570	0.620	14.48	15.75
В	0.380	0.405	9.66	10.28
С	0.160	0.190	4.07	4.82
D	0.025	0.035	0.64	0.88
F	0.142	0.147	3.61	3.73
G	0.095	0.105	2.42	2.66
Н	0.110	0.155	2.80	3.93
J	0.014	0.022	0.36	0.55
K	0.500	0.562	12.70	14.27
L	0.045	0.060	1.15	1.52
N	0.190	0.210	4.83	5.33
Q	0.100	0.120	2.54	3.04
R	0.080	0.110	2.04	2.79
S	0.045	0.055	1.15	1.39
Т	0.235	0.255	5.97	6.47
U	0.000	0.050	0.00	1.27
٧	0.045		1.15	
Z		0.080		2.04

STYLE 3:

PIN 1. CATHODE

2. ANODE

3. GATE ANODE

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