20CJQ100

Vishay High Power Products Schottky Rectifier, 2 x 1 A



ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS	
Maximum forward voltage drop per leg See fig. 1	V _{FM} ⁽¹⁾	1 A	T _J = 25 °C	0.79	v	
		2 A		0.89		
		1 A	- T _J = 125 °C	0.67		
		2 A		0.76		
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	$V_R = Rated V_R$	0.1	mA	
See fig. 2		T _J = 125 °C		10		
Typical junction capacitance per leg	CT	$V_{\rm R}$ = 5 $V_{\rm DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		45	pF	
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body		6	nH	
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs	

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Maximum junction and storage temperature range	T _J ⁽¹⁾ , T _{Stg}		- 55 to 175	°C		
Maximum thermal resistance, junction to ambient	R _{thJL}		25	°C/W		
Maximum thermal resistance, junction to lead	R _{thJA}	DC operation	65			
Approximate weight			0.13	g		
			0.0045	oz.		
Marking device		Case style SOT-223	2CJQJ			

Note

(1) $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink



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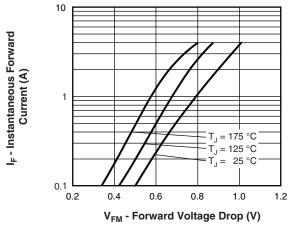


Fig. 1 - Maximum Forward Voltage Drop Characteristics

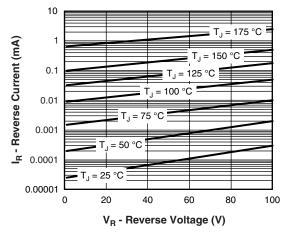


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

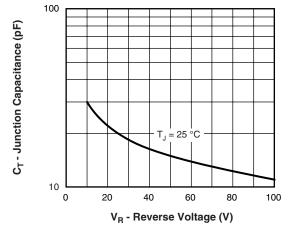


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

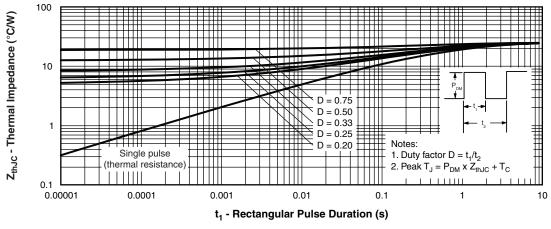
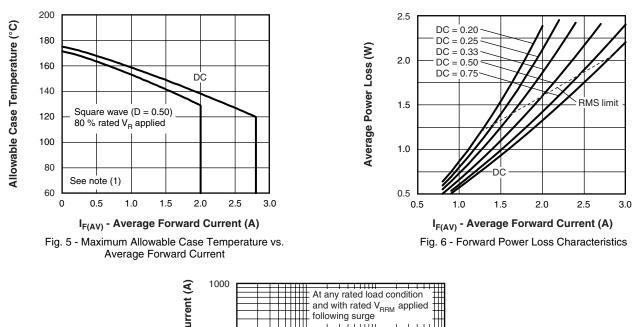


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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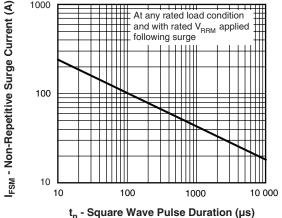


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

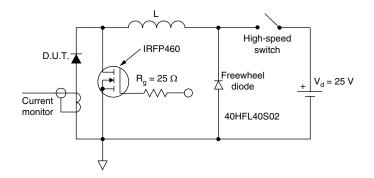


Fig. 8 - Unclamped Inductive Test Circuit

Note

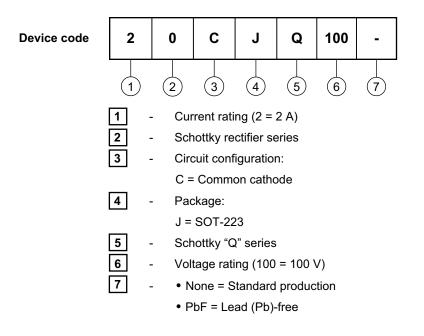
⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;

 $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \times \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see fig. 6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \times \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{80} \ \% \ \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$



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ORDERING INFORMATION TABLE



LINKS TO RELATED DOCUMENTS				
Dimensions	http://www.vishay.com/doc?95022			
Part marking information	http://www.vishay.com/doc?95031			
Packaging information	http://www.vishay.com/doc?95035			



Vishay

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