Vishay High Power Products Schottky Rectifier, 2 x 1 A



ELECTRICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS		
Maximum forward voltage drop per leg See fig. 1	V _{FM} ⁽¹⁾	1 A	T _J = 25 °C	0.59	V		
		2 A		0.75			
		1 A	T _J = 125 °C	0.56			
		2 A		0.67			
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	- V _R = Rated V _R	0.1	- mA		
See fig. 2		T _J = 125 °C		5.0			
Typical junction capacitance per leg	C _T	V_R = 5 V_{DC} (test signal range 100 kHz to 1 MHz) 25 °C		60	pF		
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body		6	nH		
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs		

Note

 $^{^{(1)}\,}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range	T _J ⁽¹⁾ , T _{Stg}		- 55 to 150	°C	
Maximum thermal resistance, junction to ambient	R _{thJL}	DC eneration	25	°C/W	
Maximum thermal resistance, junction to lead	R _{thJA}	DC operation	65		
Approximate weight			0.13	g	
			0.0045	OZ.	
Marking device		Case style SOT-223	2CJ	IQH	

Note

(1)
$$\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$$
 thermal runaway condition for a diode on its own heatsink



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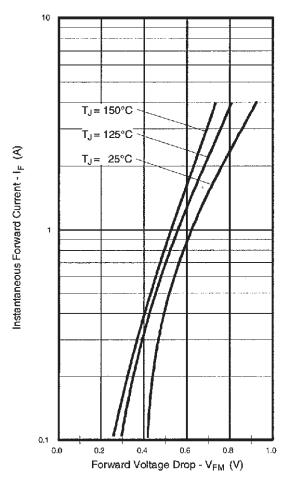


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

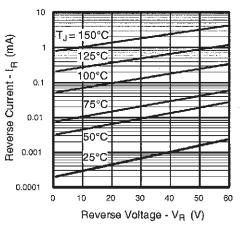


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

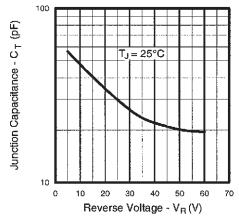


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

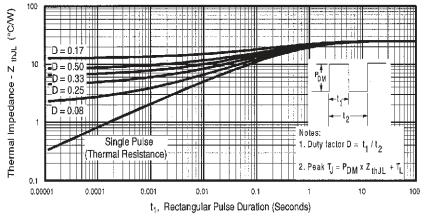


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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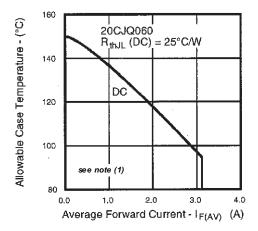


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

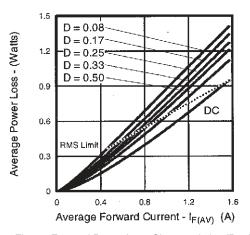


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

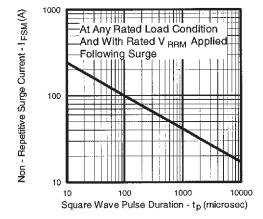


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

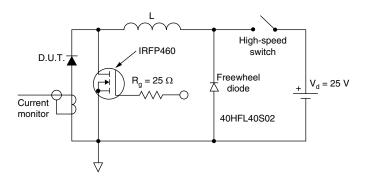


Fig. 8 - Unclamped Inductive Test Circuit

Note

Formula used: T_C = T_J - (Pd + Pd_{REV}) x R_{th,JC}; Pd = Forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 6); Pd_{REV} = Inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = 80 % rated V_R

For technical questions, contact: diodes-tech@vishay.com

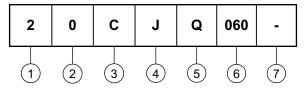
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ORDERING INFORMATION TABLE

Device code



- 1 Current rating (2 = 2 A)
- 2 Schottky rectifier series
- Circuit configuration:
 - C = Common cathode
- 4 Package:
 - J = SOT-223
- 5 Schottky "Q" series
- 6 Voltage rating (060 = 60 V)
- 7 • None = Standard production
 - PbF = Lead (Pb)-free

LINKS TO RELATED DOCUMENTS				
Dimensions	http://www.vishay.com/doc?95022			
Part marking information	http://www.vishay.com/doc?95031			
Packaging information	http://www.vishay.com/doc?95035			

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