## 2SC4617G, S2SC4617G

## **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> = 25°C)

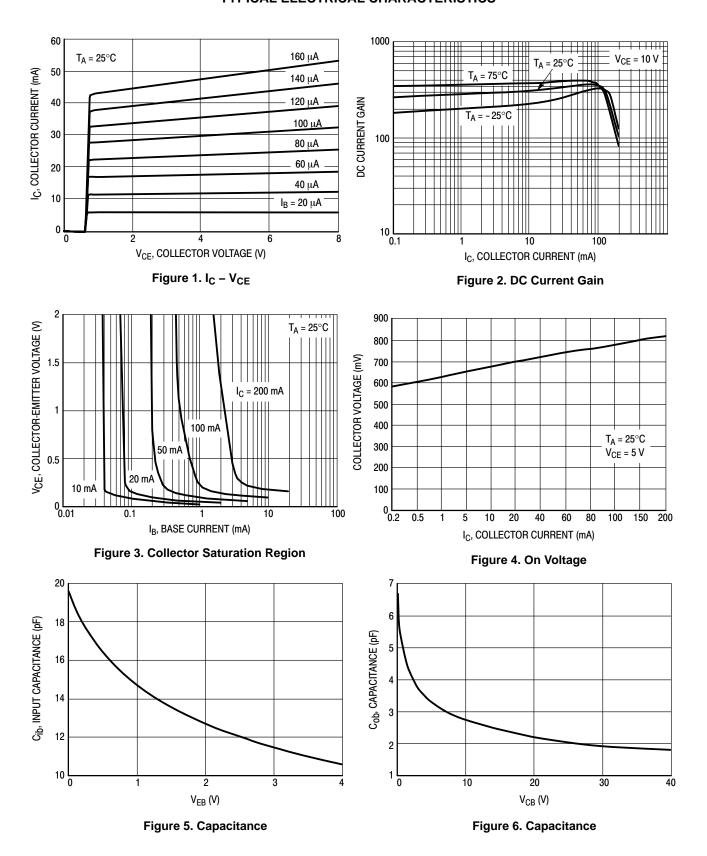
Characteristic	Symbol	Min	Тур	Max	Unit
Collector-Base Breakdown Voltage (I <sub>C</sub> = 50 μAdc, I <sub>E</sub> = 0)	V <sub>(BR)CBO</sub>	50	-	-	Vdc
Collector-Emitter Breakdown Voltage (I <sub>C</sub> = 1.0 mAdc, I <sub>B</sub> = 0)	V <sub>(BR)CEO</sub>	50	-	-	Vdc
Emitter-Base Breakdown Voltage (I <sub>E</sub> = 50 μAdc, I <sub>E</sub> = 0)	V <sub>(BR)EBO</sub>	5.0	_	-	Vdc
Collector-Base Cutoff Current (V <sub>CB</sub> = 30 Vdc, I <sub>E</sub> = 0)	I <sub>CBO</sub>	_	-	0.5	μΑ
Emitter-Base Cutoff Current (V <sub>EB</sub> = 4.0 Vdc, I <sub>B</sub> = 0)	I <sub>EBO</sub>	_	-	0.5	μΑ
Collector-Emitter Saturation Voltage (Note 2) (I <sub>C</sub> = 60 mAdc, I <sub>B</sub> = 5.0 mAdc)	V <sub>CE(sat)</sub>	_	-	0.4	Vdc
DC Current Gain (Note 2) $(V_{CE} = 6.0 \text{ Vdc}, I_{C} = 1.0 \text{ mAdc})$	h <sub>FE</sub>	120	-	560	-
Transition Frequency (V <sub>CE</sub> = 12 Vdc, I <sub>C</sub> = 2.0 mAdc, f = 30 MHz)	f <sub>T</sub>	_	180	-	MHz
Output Capacitance (V <sub>CB</sub> = 12 Vdc, I <sub>C</sub> = 0 Adc, f = 1 MHz)	C <sub>OB</sub>	_	2.0	-	pF

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

<sup>2.</sup> Pulse Test: Pulse Width  $\leq$  300  $\mu$ s, D.C.  $\leq$  2%.

## 2SC4617G, S2SC4617G

## TYPICAL ELECTRICAL CHARACTERISTICS

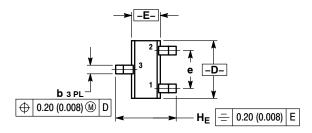


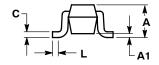


SC-75/SOT-416 CASE 463-01 ISSUE G

**DATE 07 AUG 2015** 

# SCALE 4:1





STYLE 1: PIN 1. BASE 2. EMITTER

STYLE 4:

3. COLLECTOR

PIN 1. CATHODE 2. CATHODE 3. ANODE

STYLE 2: PIN 1. ANODE 2. N/C 3. CATHODE STYLE 5: PIN 1. GATE 2. SOURCE

3. DRAIN

STYLE 3: PIN 1. ANODE 2. ANODE

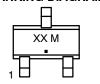
3. CATHODE

#### NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. 2. CONTROLLING DIMENSION: MILLIMETER.

	MILLIMETERS			INCHES			
DIM	MIN	NOM	MAX	MIN	NOM	MAX	
Α	0.70	0.80	0.90	0.027	0.031	0.035	
A1	0.00	0.05	0.10	0.000	0.002	0.004	
b	0.15	0.20	0.30	0.006	0.008	0.012	
С	0.10	0.15	0.25	0.004	0.006	0.010	
D	1.55	1.60	1.65	0.061	0.063	0.065	
E	0.70	0.80	0.90	0.027	0.031	0.035	
е	1.00 BSC			0.04 BSC			
L	0.10	0.15	0.20	0.004	0.006	0.008	
HE	1.50	1.60	1.70	0.060	0.063	0.067	

### **GENERIC MARKING DIAGRAM\***

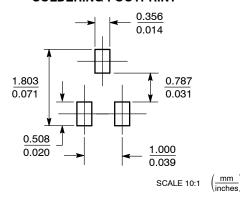


XX= Specific Device Code

Μ = Date Code

= Pb-Free Package

### **SOLDERING FOOTPRINT\***



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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