

15ETL06PbF, 15ETL06FPPbF



Vishay High Power Products Ultralow V_F Hyperfast Rectifier for
Discontinuous Mode PFC, 15 A FRED Pt™

DYNAMIC RECOVERY CHARACTERISTICS ($T_C = 25\text{ }^{\circ}\text{C}$ unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Reverse recovery time	t_{rr}	$I_F = 1\text{ A}$, $dI_F/dt = 100\text{ A}/\mu\text{s}$, $V_R = 30\text{ V}$	-	60	120	ns
		$I_F = 15\text{ A}$, $dI_F/dt = 100\text{ A}/\mu\text{s}$, $V_R = 30\text{ V}$	-	190	270	
		$T_J = 25\text{ }^{\circ}\text{C}$	-	220	-	
		$T_J = 125\text{ }^{\circ}\text{C}$	-	320	-	
Peak recovery current	I_{RRM}	$T_J = 25\text{ }^{\circ}\text{C}$	-	19	-	A
		$T_J = 125\text{ }^{\circ}\text{C}$	-	26	-	
Reverse recovery charge	Q_{rr}	$T_J = 25\text{ }^{\circ}\text{C}$	-	2.2	-	μC
		$T_J = 125\text{ }^{\circ}\text{C}$	-	4.3	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T_J , T_{Stg}		- 65	-	175	$^{\circ}\text{C}$
Thermal resistance, junction to case (FULL-PAK)	R_{thJC}		-	1.0	1.3	$^{\circ}\text{C}/\text{W}$
			-	3.0	3.5	
Thermal resistance, junction to ambient per leg	R_{thJA}	Typical socket mount	-	-	70	
Thermal resistance, case to heatsink	R_{thCS}	Mounting surface, flat, smooth and greased	-	0.5	-	
Weight			-	2.0	-	g
			-	0.07	-	oz.
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)
Marking device		Case style TO-220AC	15ETL06			
		Case style TO-220AC FULL-PAK	15ETL06FP			



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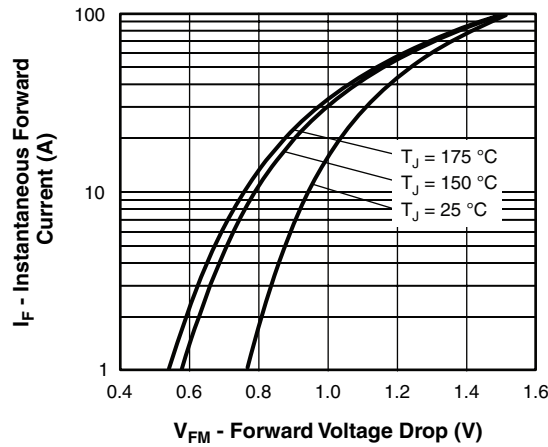


Fig. 1 - Maximum Forward Voltage Drop Characteristics

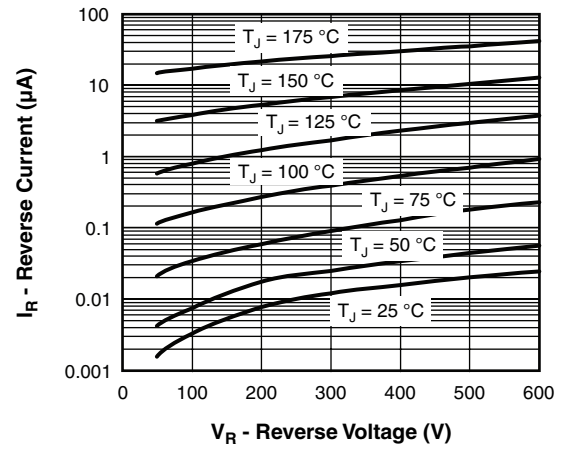


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

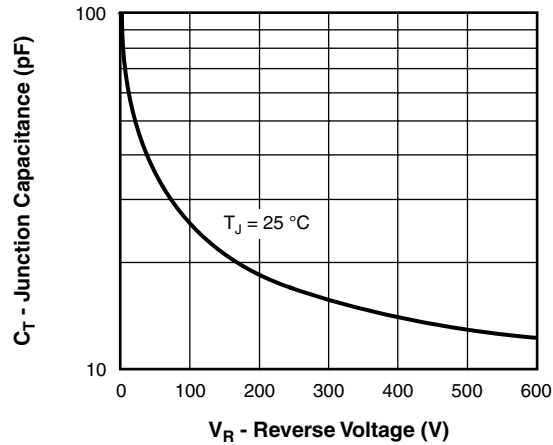


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

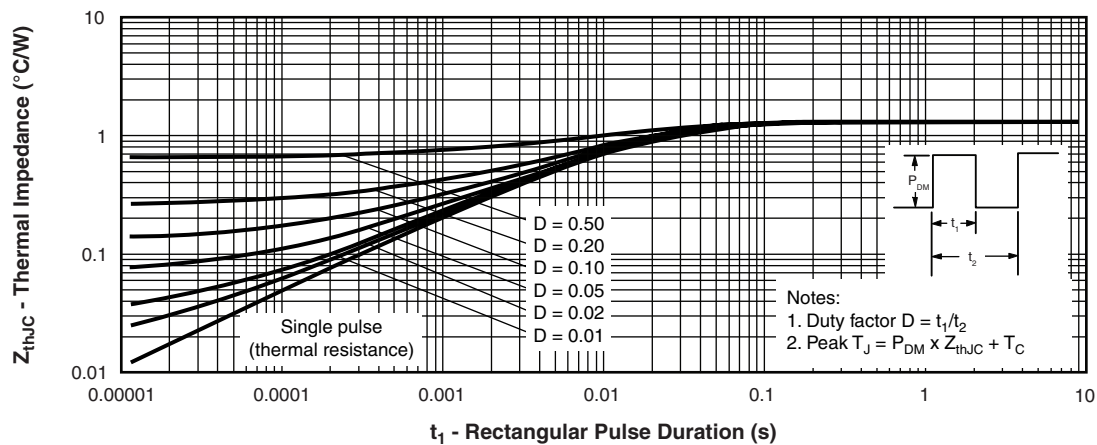


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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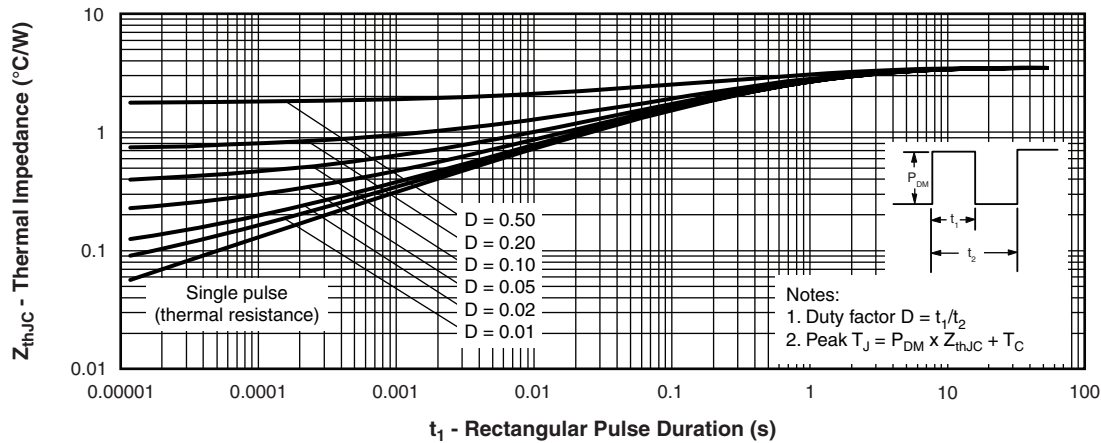


Fig. 5 - Maximum Thermal Impedance Z_{thJC} Characteristics (FULL-PAK)

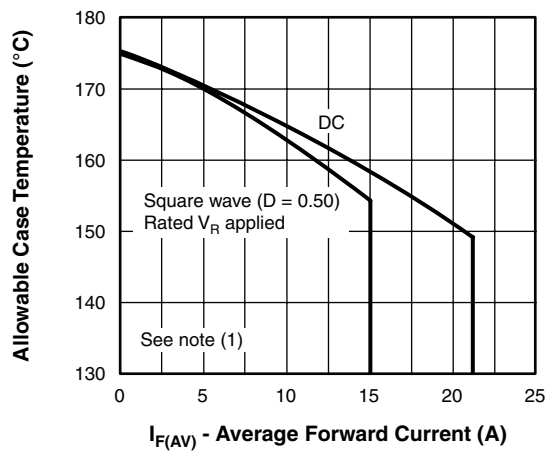


Fig. 6 - Maximum Allowable Case Temperature vs. Average Forward Current

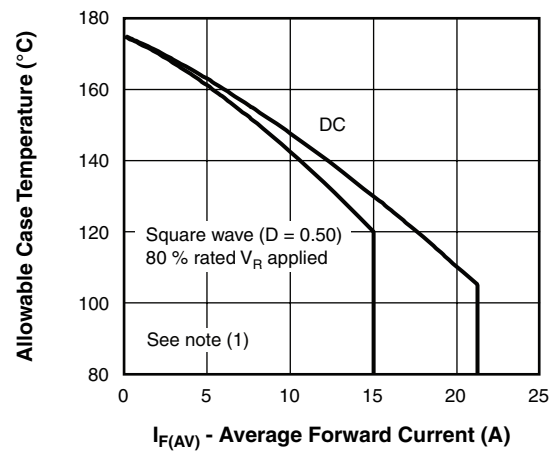


Fig. 7 - Maximum Allowable Case Temperature vs. Average Forward Current (FULL-PAK)

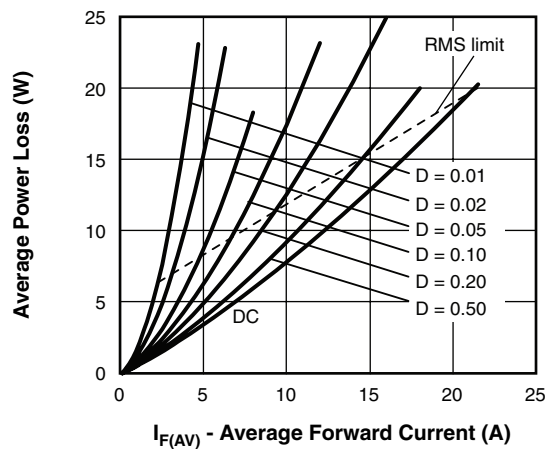


Fig. 8 - Forward Power Loss Characteristics

Note

- (1) Formula used: $T_C = T_J - (P_d + P_{dREV}) \times R_{thJC}$;
 P_d = Forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 8);
 P_{dREV} = Inverse power loss = $V_{R1} \times I_R (1 - D)$; I_R at V_{R1} = Rated V_R

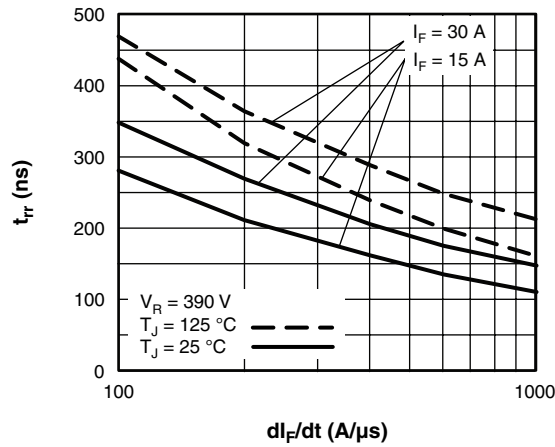


Fig. 9 - Typical Reverse Recovery Time vs. dI_F/dt

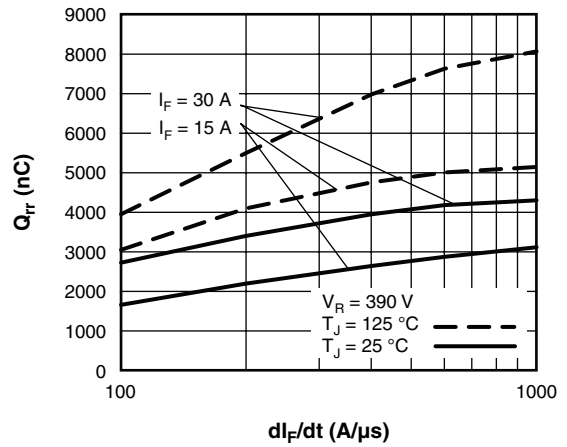


Fig. 10 - Typical Stored Charge vs. dI_F/dt

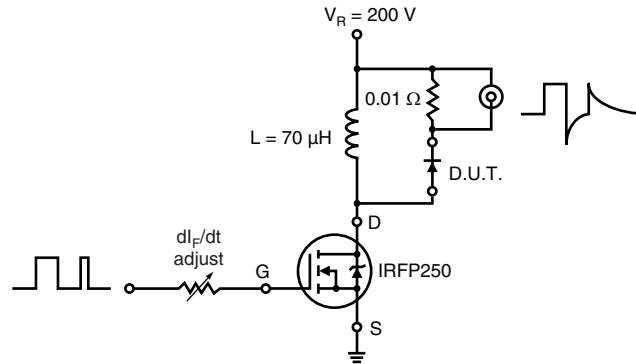


Fig. 11 - Reverse Recovery Parameter Test Circuit

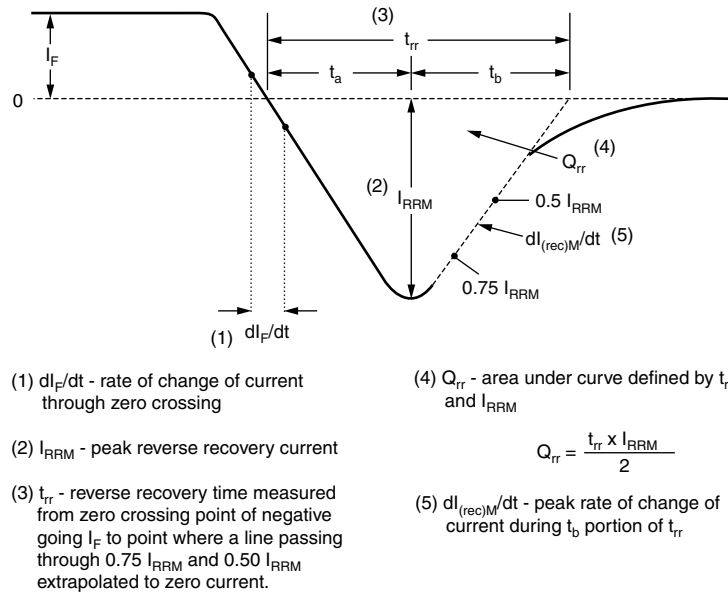


Fig. 12 - Reverse Recovery Waveform and Definitions

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ORDERING INFORMATION TABLE

Device code	15	E	T	L	06	FP	PbF
	①	②	③	④	⑤	⑥	⑦

- 1** - Current rating (15 = 15 A)
- 2** - E = Single diode
- 3** - T = TO-220, D²PAK
- 4** - L = Ultralow V_F hyperfast recovery
- 5** - Voltage rating (06 = 600 V)
- 6** -
 - None = TO-220AC
 - FP = TO-220 FULL-PAK
- 7** -
 - None = Standard production
 - PbF = Lead (Pb)-free

Tube standard pack quantity: 50 pieces

LINKS TO RELATED DOCUMENTS	
Dimensions	www.vishay.com/doc?95039
Part marking information	www.vishay.com/doc?95045
SPIICE model	www.vishay.com/doc?95270



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