

VS-11DQ05, VS-11DQ05-M3, VS-11DQ06, VS-11DQ06-M3

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Vishay Semiconductors

ELECTRICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS		
Maximum forward voltage drop See fig. 1	V _{FM} ⁽¹⁾	1 A	- T _J = 25 °C	0.58	V		
		2 A		0.76			
		1 A	T _J = 125 °C	0.53			
		2 A		0.64			
Maximum reverse leakage current See fig. 2	I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	1.0	- mA		
		T _J = 125 °C		11			
Typical junction capacitance	C _T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		55	pF		
Typical series inductance	L _S	Measured lead to lead 5 mm from package body		8.0	nH		
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs		

Note

 $^{^{(1)}\,}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Maximum junction and storage temperature range	T _J ⁽¹⁾ , T _{Stg}		- 40 to 150	°C		
Maximum thermal resistance, junction to ambient	R _{thJA}	DC operation Without cooling fin	100	°C/W		
Typical thermal resistance, junction to lead	R _{thJL}	DC operation See fig. 4	81	C/VV		
Approximate weight			0.33	g		
			0.012	OZ.		
Marking device		Case style DO-204AL (DO-41)	11DQ05			
			11D	Q06		

Note

(1) $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink

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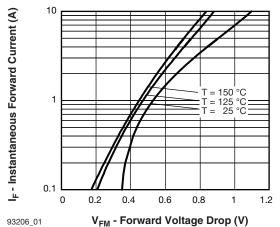


Fig. 1 - Maximum Forward Voltage Drop Characteristics

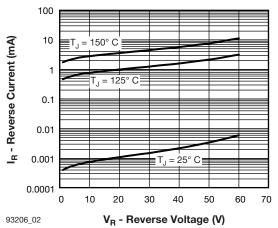
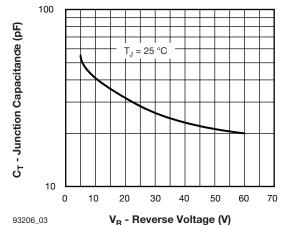


Fig. 2 - - Typical Values of Reverse Current vs. Reverse Voltage



V_R - Reverse Voltage (V)
Fig. 3 - - Typical Junction Capacitance vs.
Reverse Voltage

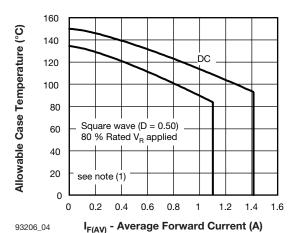


Fig. 4 - Maximum Ambient Temperature vs.

Average Forward Current, Printed Circuit Board Mounted

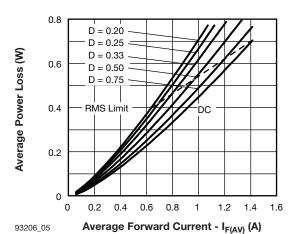
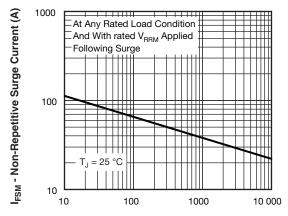


Fig. 5 - Forward Power Loss Characteristics



93206_06 t_p - Square Wave Pulse Duration (μs)
Fig. 6 - Maximum Non-Repetitive Surge Current

Reverse Voltage

Note

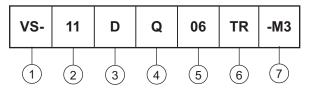
(1) Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$; $Pd = Forward power loss = I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6); $Pd_{REV} = Inverse power loss = V_{R1} \times I_R$ (1 - D); I_R at $V_{R1} = 80$ % rated V_R

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ORDERING INFORMATION TABLE





1 - Vishay Semiconductors product

11 = 1.1 A (axial and small packages - current is x 10)

D = DO-41 package

4 - Q = Schottky Q.. series

- 06 = Voltage ratings - 05 = 50 V 06 = 60 V

6 - TR = Tape and reel package

None = Bulk package

7 - Environmental digit

• None = Lead (Pb)-free and RoHS compliant

• -M3 = Halogen-free, RoHS compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)					
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION		
VS-11DQ05	1000	1000	Bulk		
VS-11DQ05TR	5000	5000	Tape and reel		
VS-11DQ05-M3	1000	1000	Bulk		
VS-11DQ05TR-M3	5000	5000	Tape and reel		
VS-11DQ06	1000	1000	Bulk		
VS-11DQ06TR	5000	5000	Tape and reel		
VS-11DQ06-M3	1000	1000	Bulk		
VS-11DQ06TR-M3	5000	5000	Tape and reel		

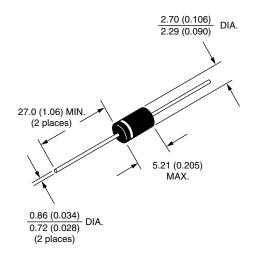
LINKS TO RELATED DOCUMENTS			
Dimensions	www.vishay.com/doc?95241		
Part marking information	www.vishay.com/doc?95304		
Packaging information	www.vishay.com/doc?95338		

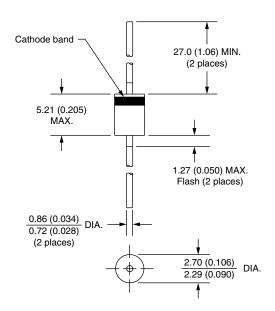


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Axial DO-204AL (DO-41)

DIMENSIONS in millimeters (inches)





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