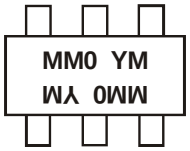
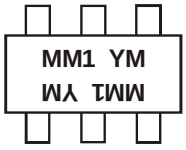
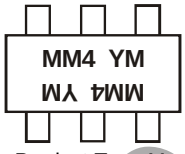
Marking Information



MM0 = Product Type Marking Code
YM = Date Code Marking
Y = Year (ex: E = 2017)
M = Month (ex: 9 = September)



MM1 = Product Type Marking Code
YM = Date Code Marking
Y = Year (ex: E = 2017)
M = Month (ex: 9 = September)



MM4 = Product Type Marking Code
YM = Date Code Marking
Y = Year (ex: E = 2017)
M = Month (ex: 9 = September)

Date Code Key

Year	2012	-	2017	2018	2019	2020	2021	2022	2023	2024
Code	Z	-	E	F	G	H	I	J	K	L

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic				Symbol	Value	Unit
Drain-Source Voltage				V_{DSS}	60	V
Gate-Source Voltage				V_{GSS}	±20	V
Continuous Drain Current (Note 5)	V_{GS} = 10V	Steady State	T_A = +25°C T_A = +70°C	I_D	180 140	mA
Continuous Drain Current (Note 5)	V_{GS} = 5V	Steady State	T_A = +25°C T_A = +70°C	I_D	150 120	mA
Continuous Drain Current (Note 6)	V_{GS} = 10V	Steady State	T_A = +25°C T_A = +70°C	I_D	200 160	mA
Continuous Drain Current (Note 6)	V_{GS} = 5V	Steady State	T_A = +25°C T_A = +70°C	I_D	170 140	mA
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)				I_{DM}	700	mA

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	P_D	300	mW
Thermal Resistance, Junction to Ambient (Note 5)	$R_{\theta JA}$	435	°C/W
Total Power Dissipation (Note 6)	P_D	400	mW
Thermal Resistance, Junction to Ambient (Note 6)	$R_{\theta JA}$	330	°C/W
Thermal Resistance, Junction to Case (Note 6)	$R_{\theta JC}$	139	°C/W
Operating and Storage Temperature Range	T_J, T_{STG}	-55 to +150	°C

Notes: 5. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper pad layout.

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV_{DSS}	60	—	—	V	V_{GS} = 0V, I_D = 250µA
Zero Gate Voltage Drain Current	I_{DSS}	—	—	1.0	µA	V_{DS} = 60V, V_{GS} = 0V
Gate-Body Leakage	I_{GSS}	—	—	±5	µA	V_{GS} = ±20V, V_{DS} = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	$V_{GS(TH)}$	0.8	—	2.5	V	V_{DS} = V_{GS}, I_D = 250µA
Static Drain-Source On-Resistance	$R_{DS(ON)}$	—	—	8	Ω	V_{GS} = 5.0V, I_D = 0.115A
		—	—	6	Ω	V_{GS} = 10.0V, I_D = 0.115A
Forward Transconductance	g_{FS}	80	—	—	mS	V_{DS} = 10V, I_D = 0.115A
Diode Forward Voltage	V_{SD}	—	0.8	1.2	V	V_{GS} = 0V, I_S = 115mA
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C_{iss}	—	22.0	—	pF	V_{DS} = 25V, V_{GS} = 0V, f = 1.0MHz
Output Capacitance	C_{oss}	—	3.2	—		
Reverse Transfer Capacitance	C_{rss}	—	2.0	—		
Gate Resistance	R_G	—	88	—	Ω	V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz
Total Gate Charge V_{GS} = 10V	Q_g	—	0.87	—	nC	V_{GS} = 10V, V_{DS} = 30V, I_D = 150mA
Total Gate Charge V_{GS} = 4.5V	Q_g	—	0.43	—		
Gate-Source Charge	Q_{gs}	—	0.11	—		
Gate-Drain Charge	Q_{gd}	—	0.11	—		
Turn-On Delay Time	$t_{D(ON)}$	—	3.3	—	ns	V_{DD} = 30V, I_D = 0.115A, V_{GEN} = 10V, R_{GEN} = 25Ω
Turn-On Rise Time	t_R	—	3.2	—		
Turn-Off Delay Time	$t_{D(OFF)}$	—	12.0	—		
Turn-Off Fall Time	t_F	—	6.3	—		

Notes: 7. Short duration pulse test used to minimize self-heating effect.
8. Guaranteed by design. Not subject to production testing.

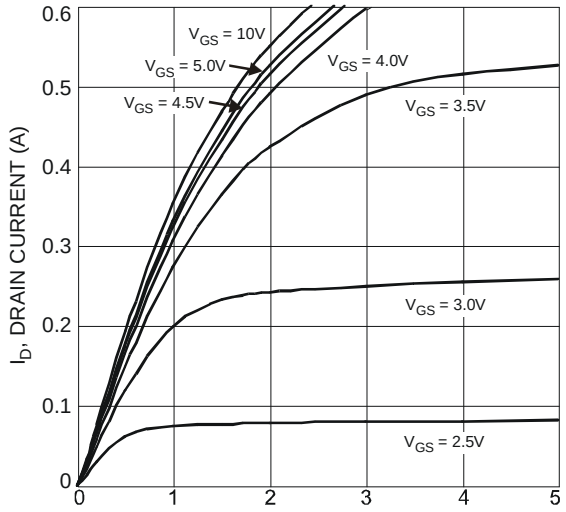


Figure 1 Typical Output Characteristic

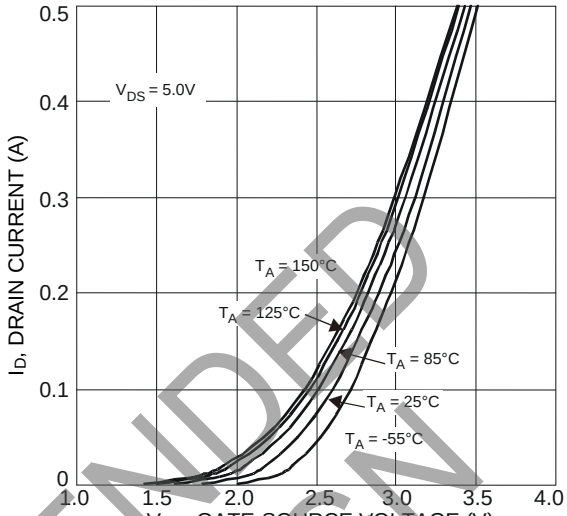


Figure 2 Typical Transfer Characteristics

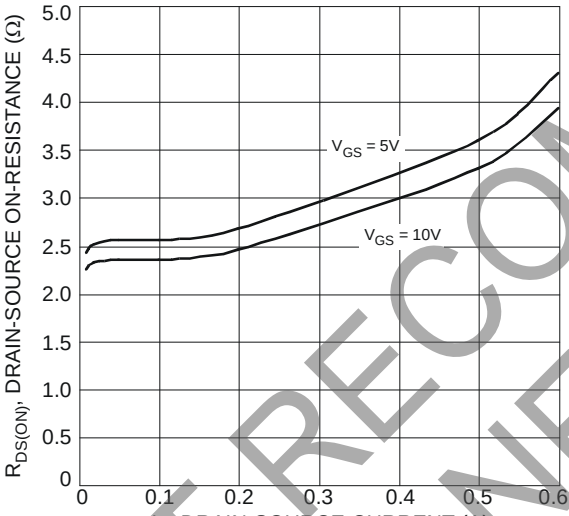


Figure 3 Typical On-Resistance vs. Drain Current and Gate Voltage

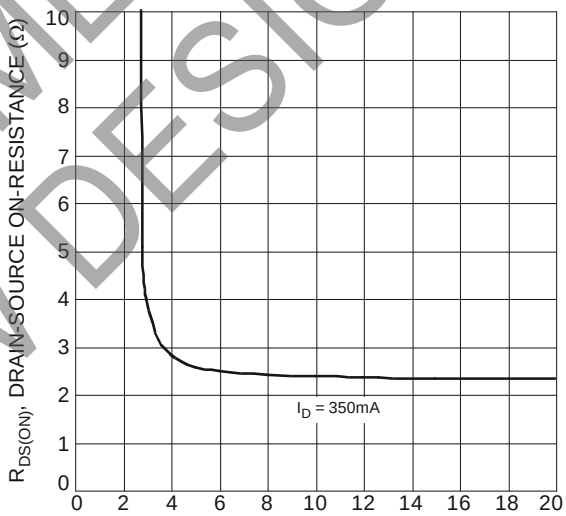


Figure 4 Typical Drain-Source On-Resistance vs. Gate-Source Voltage

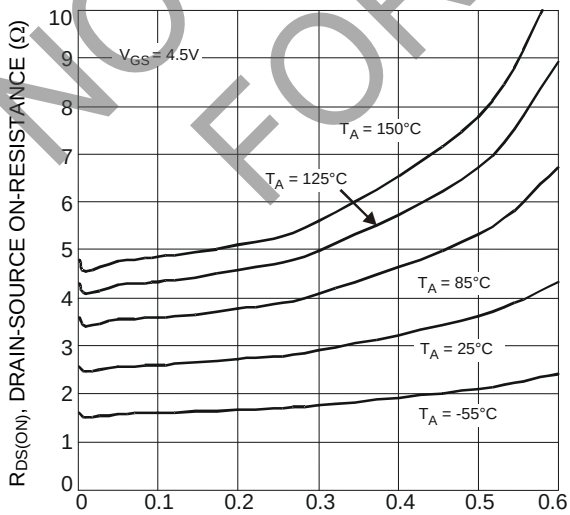


Figure 5 Typical On-Resistance vs. Drain Current and Temperature

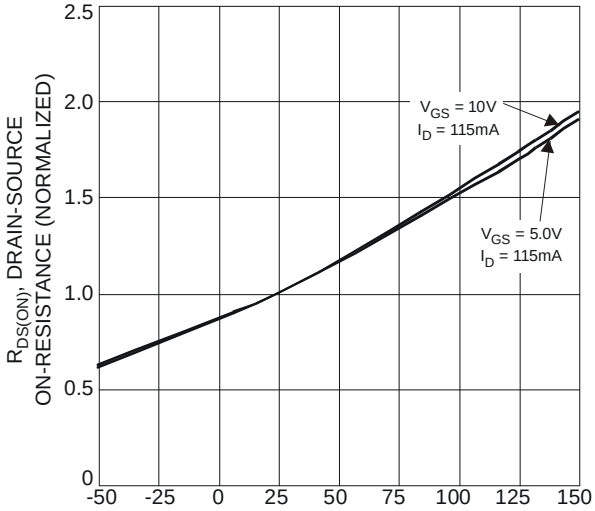


Figure 6 On-Resistance Variation with Temperature

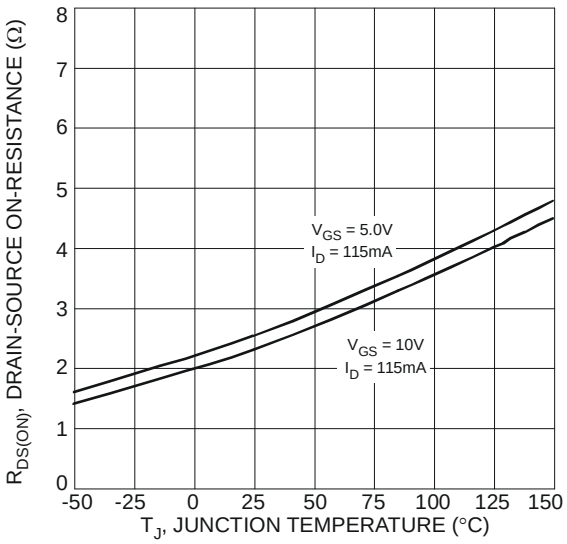


Figure 7 On-Resistance Variation with Temperature

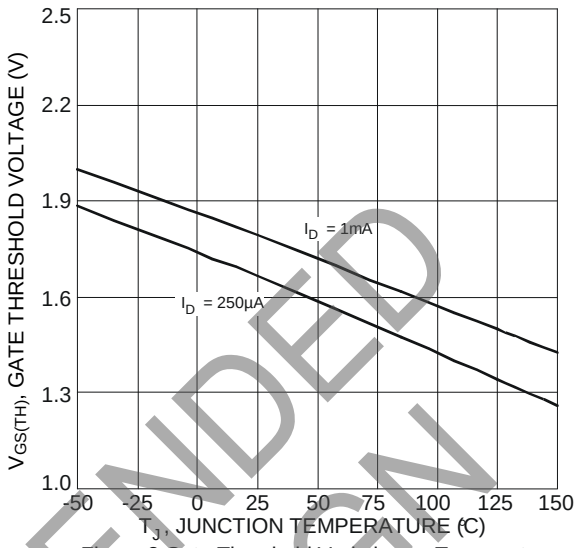


Figure 8 Gate Threshold Variation vs.Temperature

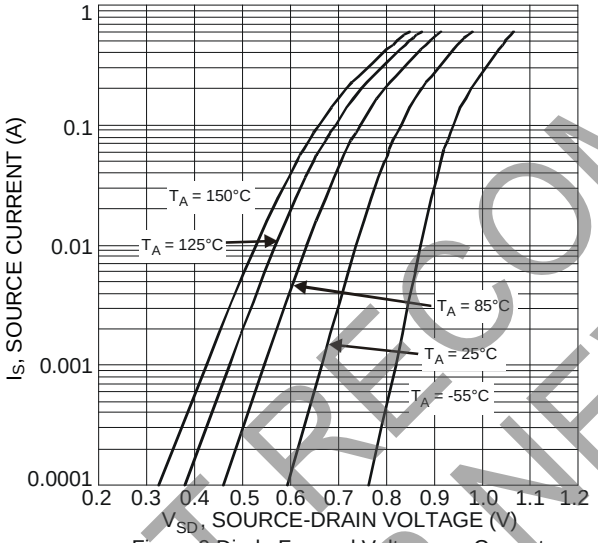


Figure 9 Diode Forward Voltage vs. Current

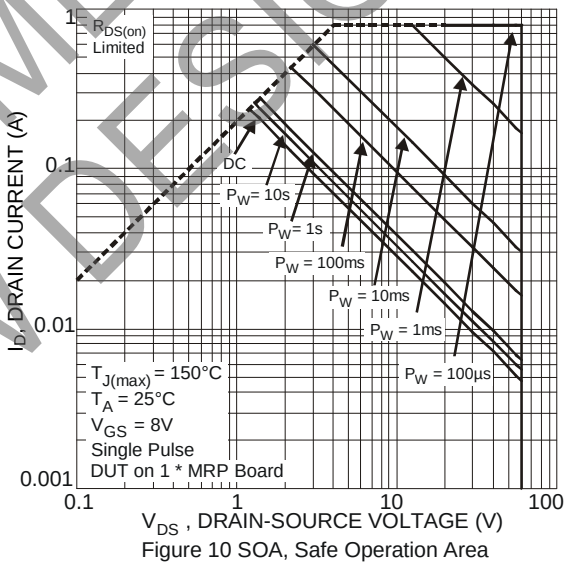
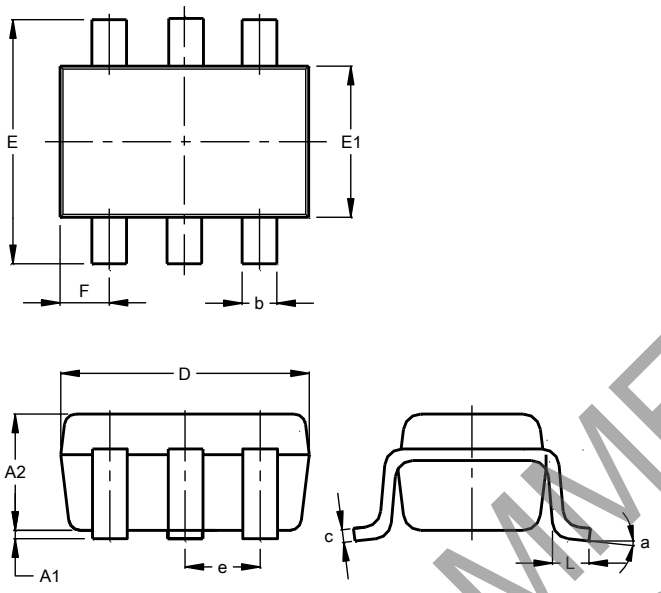


Figure 10 SOA, Safe Operation Area

Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT363



SOT363			
Dim	**Min**	**Max**	**Typ**
A1	0.00	0.10	0.05
A2	0.90	1.00	1.00
b	0.10	0.30	0.25
c	0.10	0.22	0.11
D	1.80	2.20	2.15
E	2.00	2.20	2.10
E1	1.15	1.35	1.30
e	0.650 BSC		
F	0.40	0.45	0.425
L	0.25	0.40	0.30
a	0°	8°	--
All Dimensions in mm			

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

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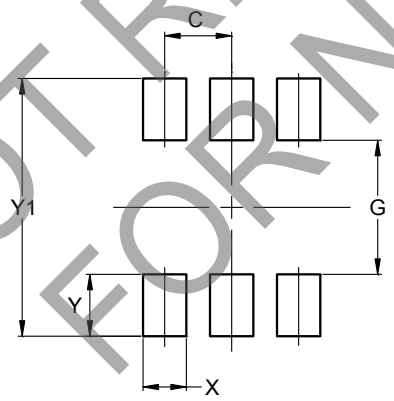

Dimensions	Value (in mm)
C	0.650
G	1.300
X	0.420
Y	0.600
Y1	2.500

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