

ELECTRICAL CHARACTERISTICS* @ 25°C

JEDEC TYPE NO. (NOTE1)	NOMINAL ZENER VOLTAGE V _Z @ I _{ZT} (NOTE 2)	ZENER TEST CURRENT I _{ZT}	MAXIMUM ZENER IMPEDANCE Z _{ZT} @ I _{ZT} (NOTE 3)	MAXIMUM REVERSE CURRENT @ V _R = 1 VOLT		MAXIMUM ZENER CURRENT I _{ZM} (NOTE 4)	TYPICAL TEMP COEFF. OF ZENER VOLTAGE α _{VZ}
				@25°C	@+150°C		
		VOLTS	mA	OHMS	μA	μA	mA
1N4370	2.4	20	30	100	200	150	-.085
1N4371	2.7	20	30	75	150	135	-.080
1N4372	3.0	20	29	50	100	120	-.075
1N746	3.3	20	28	10	30	110	-.066
1N747	3.6	20	24	10	30	100	-.058
1N748	3.9	20	23	10	30	95	-.046
1N749	4.3	20	22	2	30	85	-.033
1N750	4.7	20	19	2	30	75	-.015
1N751	5.1	20	17	1	20	70	+/- .010
1N752	5.6	20	11	1	20	65	+0.030
1N753	6.2	20	7	.1	20	60	+0.049
1N754	6.8	20	5	.1	20	55	+0.053
1N755	7.5	20	6	.1	20	50	+0.057
1N756	8.2	20	8	.1	20	45	+0.060
1N757	9.1	20	10	.1	20	40	+0.061
1N758	10.0	20	17	.1	20	35	+0.062
1N759	12.0	20	30	.1	20	30	+0.062

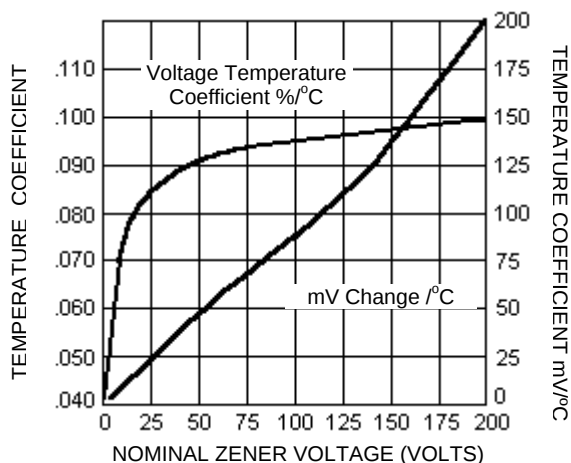
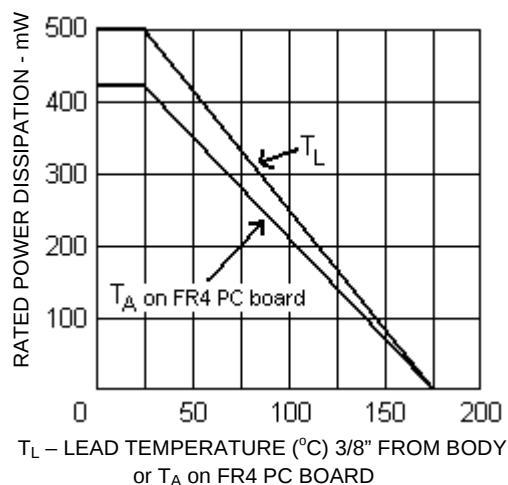
*** JEDEC Registered Data**

NOTE 1: Standard tolerance on JEDEC types shown is +/- 10%. Suffix letter A denotes +/- 5% tolerance; suffix letter C denotes +/- 2%; and suffix letter D denotes +/- 1% tolerance.

NOTE 2: Voltage measurements to be performed 20 seconds after application of dc test current.

NOTE 3: Zener impedance derived by superimposing on I_{ZT} , a 60 cps, rms ac current equal to 10% I_{ZT} (2 mA ac). See MicroNote 202 for typical zener impedance variation with different operating currents.

NOTE 4: Allowance has been made for the increase in V_Z due to Z_Z and for the increase in junction temperature as the unit approaches thermal equilibrium at the power dissipation of 400 mW.

GRAPHS


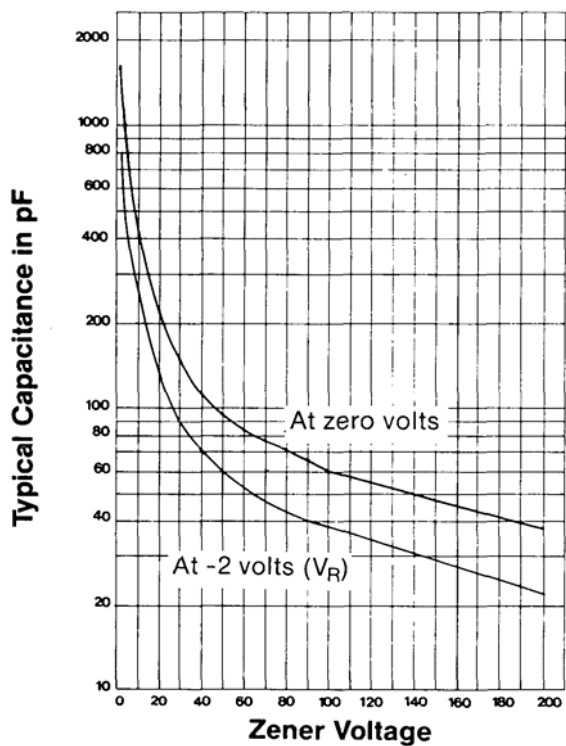
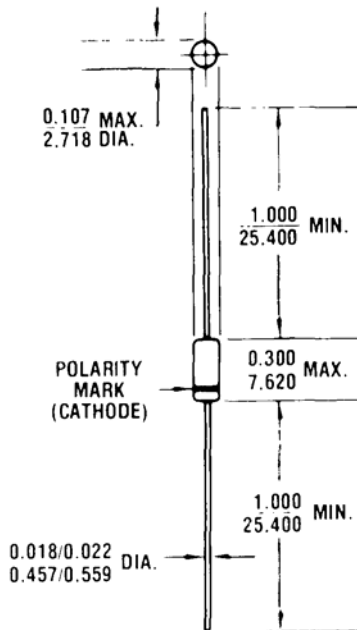


FIGURE 3
CAPACITANCE vs. ZENER VOLTAGE
(TYPICAL)

PACKAGE DIMENSIONS



All dimensions in: INCH
mm