

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

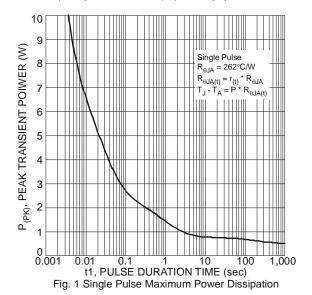
Characteristic			Symbol	Value	Unit
Drain-Source Voltage		V_{DSS}	-30	V	
Gate-Source Voltage			V _{GSS}	±8	V
Continuous Drain Current	Steady State	$T_A = +25$ °C (Note 6) $T_A = +85$ °C (Note 6) $T_A = +25$ °C (Note 5)	I _D	-0.76 -0.55 -0.54	А
Pulsed Drain Current (Note 7)			I _{DM}	2	Α

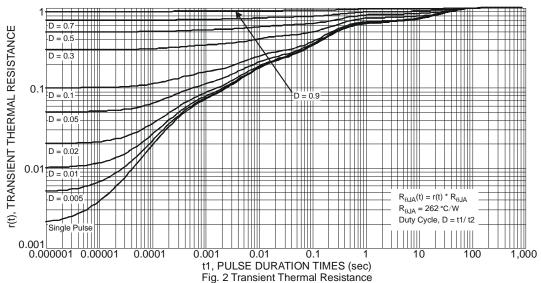
Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Dower Discination	(Note 5)	Б	0.46	W	
Power Dissipation	(Note 6)	P_{D}	0.92		
Thermal Resistance, Junction to Ambient	(Note 5)		271	°C/W	
Thermal Resistance, Junction to Ambien	(Note 6)	$R_{\theta JA}$	136		
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +150	°C	

Notes:

- 5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
- 6. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal vias to bottom layer 1inch square copper plate.
- 7. Device mounted on minimum recommended pad layout test board, 10µs pulse duty cycle = 1%.







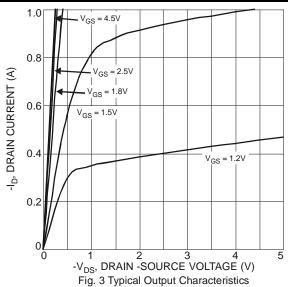
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

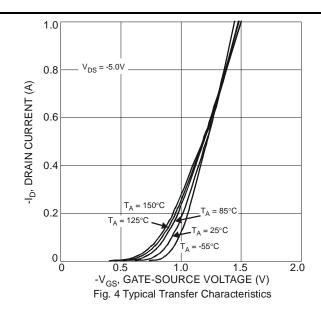
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)							
Drain-Source Breakdown Voltage	BV _{DSS}	-30	1	-	V	$V_{GS} = 0V, I_D = -250\mu A$	
Zero Gate Voltage Drain Current T _J = +25°C	I _{DSS}	-	-	-1	μΑ	$V_{DS} = -30V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	-	-	±3	μΑ	$V_{GS} = \pm 8V$, $V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	V _{GS(TH)}	-0.5	-0.6	-1.1	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	
		-	0.45	1	Ω	$V_{GS} = -4.5V, I_D = -400mA$	
Static Drain-Source On-Resistance	R _{DS(ON)}		0.54	1.5		$V_{GS} = -2.5V, I_D = -200mA$	
			0.64	2		$V_{GS} = -1.8V, I_D = -100mA$	
Forward Transfer Admittance	Y _{fs}	50	-	-	mS	$V_{DS} = -3V, I_{D} = -300 \text{mA}$	
Diode Forward Voltage	V _{SD}	-	-	-1.2	V	$V_{GS} = 0V, I_{S} = -300mA$	
DYNAMIC CHARACTERISTICS (Note9)							
Input Capacitance	C _{iss}	-	76	150	pF	V _{DS} = -15V, V _{GS} = 0V, f = 1.0MHz	
Output Capacitance	Coss	-	9	20	pF		
Reverse Transfer Capacitance	C _{rss}	-	6.43	15	pF		
Gate Resistance	Rg	-	167	-	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge	Qg		0.9	-	nC	$V_{GS} = -4.5V, V_{DS} = -15V, I_{D} = -1A$	
Total Gate Charge	Qg	-	1.5	-	nC	V _{GS} = -8V, V _{DS} = -15V, -1 _D = -1A	
Gate-Source Charge	Q_{gs}	-	0.1	-	nC		
Gate-Drain Charge	Q _{qd}	-	0.2	-	nC		
Turn-On Delay Time	t _{D(ON)}	-	4.98	-	ns	$V_{DD} = -10V, R_L = 10\Omega$ $V_{GS} = -4.5V, R_g = 6\Omega$	
Turn-On Rise Time	t _R	-	5.85	-	ns		
Turn-Off Delay Time	t _{D(OFF)}	-	35.7	-	ns		
Turn-Off Fall Time	t _F	-	16.6	-	ns		

Notes:

- 8. Short duration pulse test used to minimize self-heating effect.
- 9. Guaranteed by design. Not subject to product testing.

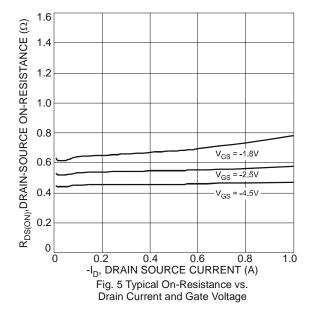
Typical Electrical Characteristics

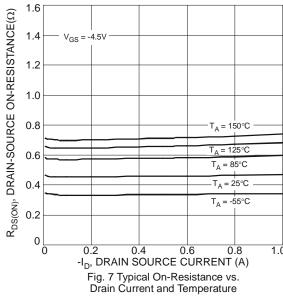


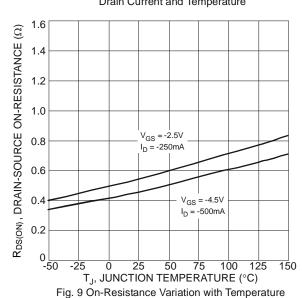


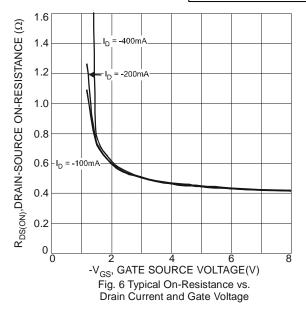


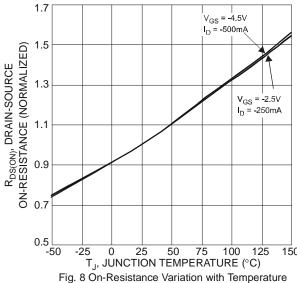












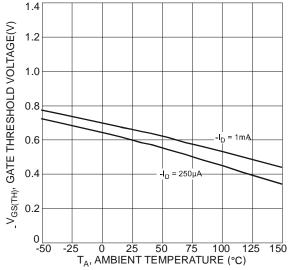
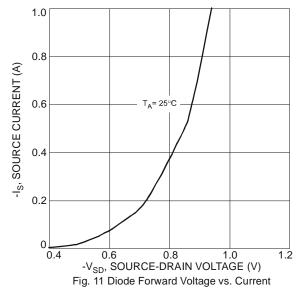


Fig. 10 Gate Threshold Variation vs. Ambient Temperature







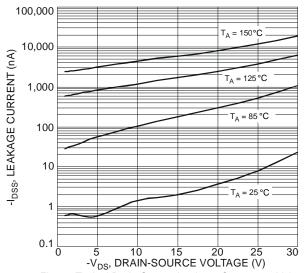
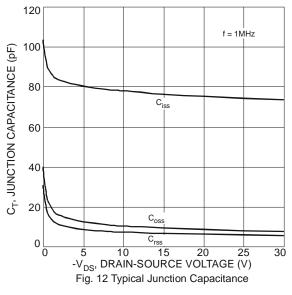
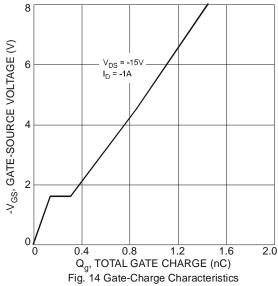


Fig. 13 Typical Drain-Source Leakage Current vs. Voltage



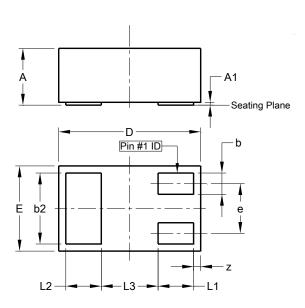


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Datasheet number: DS35587 Rev. 2 - 2 www.diodes.com



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

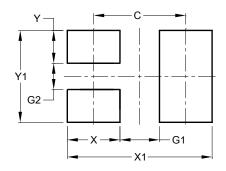


X2-DFN1006-3

X2-DFN1006-3					
Dim	Min	Max	Тур		
Α		0.40			
A1	0.00	0.05	0.03		
b	0.10	0.20	0.15		
b2	0.45	0.55	0.50		
D	0.95	1.05	1.00		
Е	0.55	0.65	0.60		
е	-	-	0.35		
L1	0.20	0.30	0.25		
L2	0.20	0.30	0.25		
L3	-	-	0.40		
z	0.02	0.08	0.05		
All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



X2-DFN1006-3

Dimensions	Value (in mm)		
С	0.70		
G1	0.30		
G2	0.20		
Х	0.40		
X1	1.10		
Y	0.25		
Y1	0.70		



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