

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V_{DSS}	-30	V
Gate-Source Voltage			V_{GSS}	±20	V
Continuous Drain Current (Note 6)	V _{GS} = -10V	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I _D	250 200	mA
Pulsed Drain Current (Note 6)			I _{DM}	-1	Α

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Units	
Total Power Dissipation	(Note 5)	В	300	mW	
Total Fower Dissipation	(Note 6)	P _D	432		
Thermal Resistance, Junction to Ambient	(Note 5)	D	398		
Thermal Resistance, Junction to Ambient	(Note 6)	$R_{\theta JA}$	290	°C/W	
Thermal Resistance, Junction to Case	(Note 5)	$R_{\theta JC}$	142		
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to 150	°C	

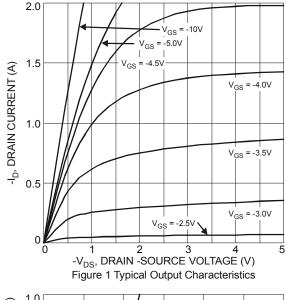
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

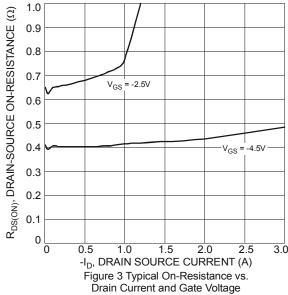
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)	Syllibol	IVIIII	тур	IVIAX	Ullit	Test Condition	
Drain-Source Breakdown Voltage	BV _{DSS}	-30	_	_	V	$V_{GS} = 0V, I_{D} = -1mA$	
Zero Gate Voltage Drain Current T _J = +25°C	I _{DSS}	-	_	-1	μA	$V_{DS} = -30V, V_{GS} = 0V$	
Gate-Source Leakage	IGSS		_	±10	μΑ	$V_{GS} = \pm 16V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)					VGS - 110V, VDS - 0V		
Gate Threshold Voltage	V _{GS(th)}	-1.4	_	-2.4	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	
Gate Threshold Voltage	V GS(tn)	-1		2.4	V	$V_{GS} = V_{GS}, I_D = -2.50 \mu A$ $V_{GS} = -10 V, I_D = -0.5 A$	
Static Drain-Source On-Resistance	R _{DS} (ON)	-	-	4	Ω	$V_{GS} = -4.5V$, $I_D = -0.3A$	
Forward Transfer Admittance	IV. I		6	-	S		
	Y _{fs}	-	-		V	$V_{DS} = -10V, I_D = -400mA$	
Diode Forward Voltage	V_{SD}	-	8.0	1.2	V	$V_{GS} = 0V, I_S = -300mA$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	C _{iss}	-	51.16	-	pF	V _{DS} = -15V, V _{GS} = 0V, -f = 1.0MHz	
Output Capacitance	Coss	-	10.85	-	pF		
Reverse Transfer Capacitance	C _{rss}	-	8.88	-	pF		
Gate Resistance	R_g	-	275	-	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge	Qg	-	0.6	-	nC	V _{GS} = -4.5V	
Total Gate Charge	Qq	-	1.2	-	nC	$V_{DS} = -10V,$ $V_{DS} = -10V,$ $I_{D} = -1A$	
Gate-Source Charge	Q _{gs}	-	0.2	-	nC		
Gate-Drain Charge	Q_{gd}	-	0.3	-	nC		
Turn-On Delay Time	t _{D(on)}	-	9.86	-	ns	V_{DS} = -15V, I_{D} = -1A V_{GS} = -10V, R_{G} = 6 Ω	
Turn-On Rise Time	t _r	-	11.5	-	ns		
Turn-Off Delay Time	t _{D(off)}	-	31.8	-	ns		
Turn-Off Fall Time	t _f	-	21.9	-	ns		

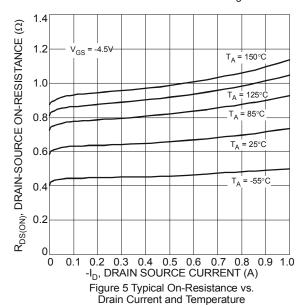
Notes:

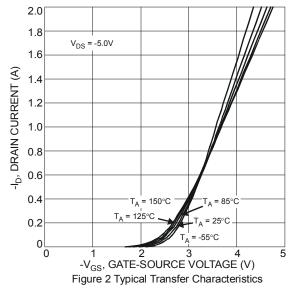
- 5. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
- Bevice mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper pad layout
 Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to production testing.

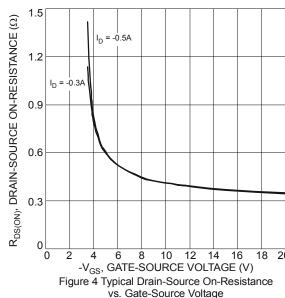


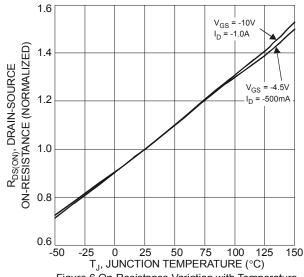




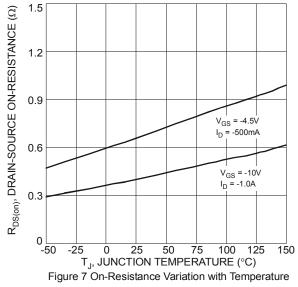


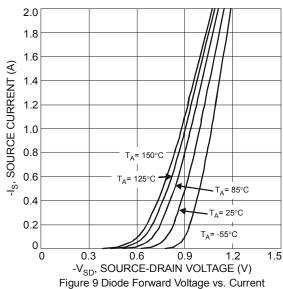


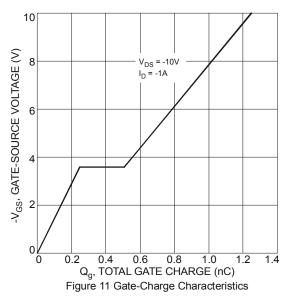












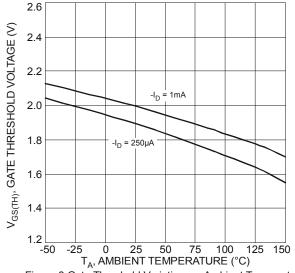
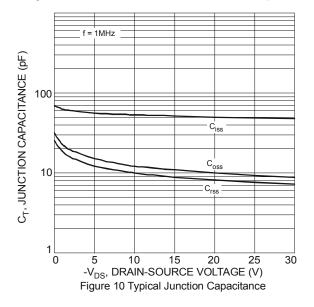


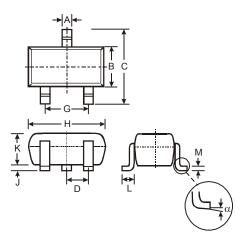
Figure 8 Gate Threshold Variation vs. Ambient Temperature





Package Outline Dimensions

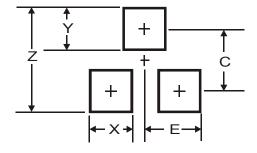
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



	SOT323					
Dim	Min	Max	Тур			
Α	0.25	0.40	0.30			
В	1.15	1.35	1.30			
С	2.00	2.20	2.10			
D	1	1	0.65			
G	1.20	1.40	1.30			
Н	1.80	2.20	2.15			
J	0.0	0.10	0.05			
K	0.90	1.00	0.95			
L	0.25	0.40	0.30			
M	0.10	0.18	0.11			
α	0°	8°	-			
All	All Dimensions in mm					

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	2.8
X	0.7
Y	0.9
С	1.9
Е	1.0



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