

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Units
Drain-Source Voltage			V_{DSS}	-20	V
Gate-Source Voltage			V_{GSS}	±8	V
Continuous Drain Current (Note 6) V - 4 EV	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I _D	-7.6 -6.1	Α
Continuous Drain Current (Note 6) V _{GS} = -4.5V	t<5s	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	Ι _D	-9.5 -7.6	А
Pulsed Drain Current (10µs pulse, duty cycle = 1%)			I_{DM}	-40	Α
Continuous Source-Drain Diode Current T _A = +25°C		Is	-2	Α	
Avalanche Current (Note 7) L = 0.1mH			I _{AS}	- 23	Α
Repetitive Avalanche Energy (Note 7) L = 0.1mH			E _{AS}	27	mJ

Thermal Characteristics

Characteristic		Symbol	Value	Units	
Total Dower Dissination (Note 5)	T _A = +25°C	Б	0.73	W	
Total Power Dissipation (Note 5)	T _A = +70°C	P_D	0.47		
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	П	171	°C/W	
	t<5s	$R_{\theta JA}$	112		
Total Power Dissipation (Note 6)	$T_A = +25^{\circ}C$	В	2.03	W	
	T _A = +70°C	P_{D}	1.30		
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	П	62	°C/W	
Thermal Resistance, Junction to Ambient (Note 6)	t<5s	$R_{\theta JA}$	40		
Thermal Resistance, Junction to Case (Note 6)	Steady State	$R_{ heta JC}$	9.3		
Operating and Storage Temperature Range		T _{J,} T _{STG}	-55 to +150	°C	

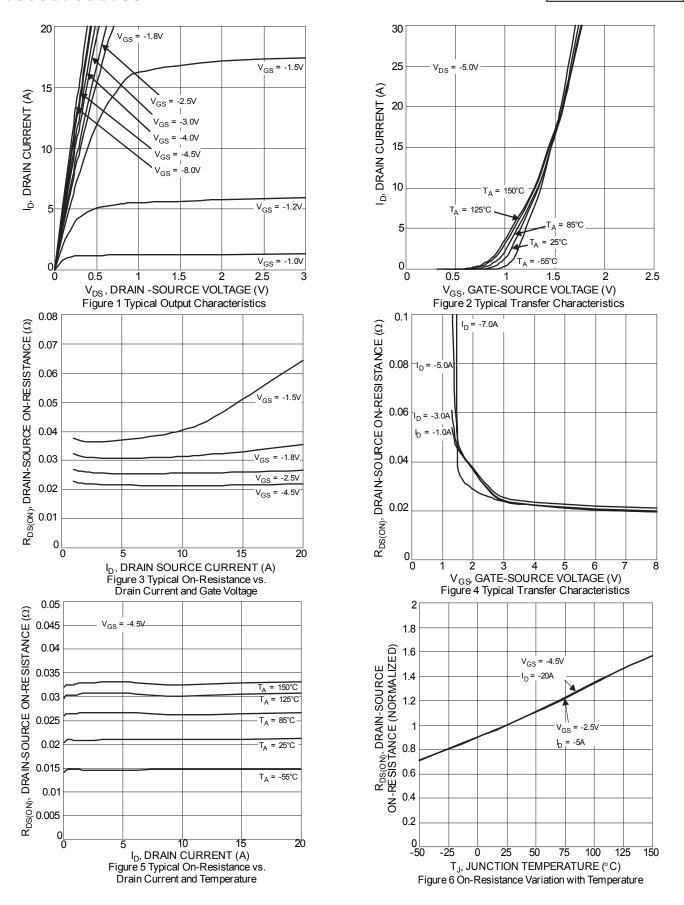
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)							
Drain-Source Breakdown Voltage	BV _{DSS}	-20	_	_	V	$V_{GS} = 0V, I_D = -250\mu A$	
Zero Gate Voltage Drain Current T _J = +25°C	I _{DSS}	_		-1	μΑ	$V_{DS} = -20V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 5V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	V _{GS(th)}	-0.4		-1.0	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	
		_		27	- mΩ	$V_{GS} = -4.5V, I_D = -7.0A$	
Static Drain-Source On-Resistance	D			32		$V_{GS} = -2.5V$, $I_D = -5.0A$	
Static Drain-Source On-Resistance	R _{DS(ON)}		_	50		$V_{GS} = -1.8V$, $I_D = -3.0A$	
			_	90		$V_{GS} = -1.5V, I_D = -1.0A$	
Diode Forward Voltage	V _{SD}	_	-0.8	-1.2	V	$V_{GS} = 0V, I_{S} = -1.0A$	
DYNAMIC CHARACTERISTICS (Note 9)						•	
Input Capacitance	C _{iss}	_	1837	_		V _{DS} = -15V, V _{GS} = 0V, f = 1.0MHz	
Output Capacitance	Coss	_	131	_	pF		
Reverse Transfer Capacitance	C _{rss}	_	115	_		1 - 1.0101112	
Gate Resistance	R_{g}	_	14.8	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge (V _{GS} = -4.5V)	Qq	_	27	_		V _{DS} = -15V, V _{GS} = -4.5V, I _D = -4.0A	
Gate-Source Charge	Q_{gs}	_	2.8	_	nC		
Gate-Drain Charge	Q _{qd}	_	3.1	_			
Turn-On Delay Time	t _{D(on)}	_	5.8	_		V _{DS} = -15V, V _{GS} = -4.5V,	
Turn-On Rise Time	t _r	_	19.3	_			
Turn-Off Delay Time	t _{D(off)}	_	168.5	_	ns	$R_G = 1\Omega$, $I_D = -4.0A$	
Turn-Off Fall Time	t _f	_	77.3	_			
Reverse Recovery Time	t _{rr}	_	46.5	_	ns	I _F = -1.0A, di/dt = 100A/μs	
Reverse Recovery Charge	Qrr	_	33.8	_	nC	I _F = -1.0A, di/dt = 100A/µs	

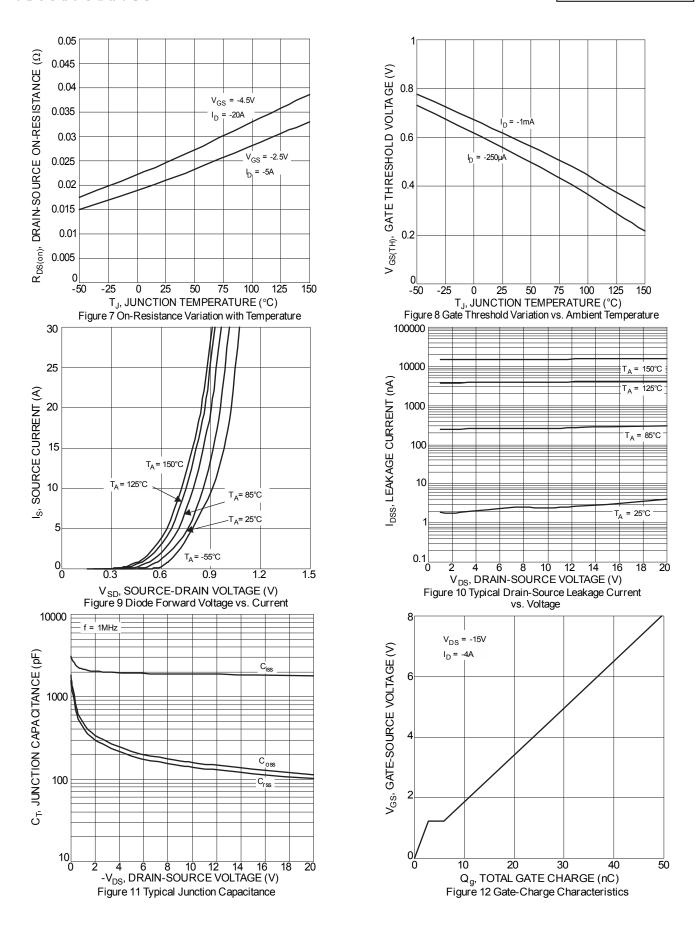
 Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
I_{AS} and E_{AS} rating are based on low frequency and duty cycles to keep T_J = +25°C.
Short duration pulse test used to minimize self-heating effect. Notes:

9. Guaranteed by design. Not subject to product testing.



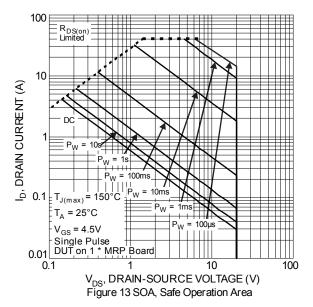


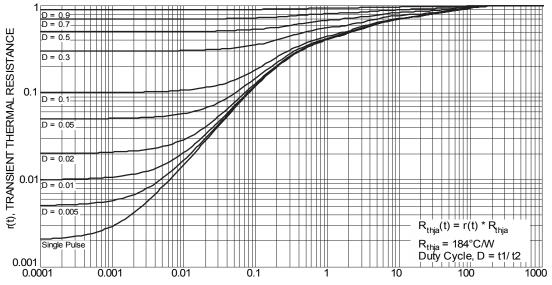




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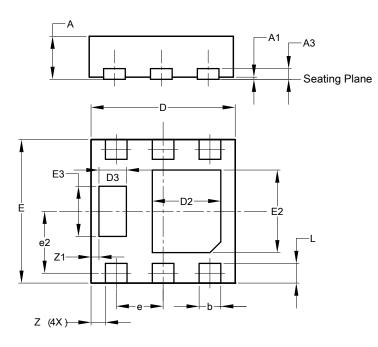


t1, PULSE DURATION TIMES (sec) Figure 14 Transient Thermal Resistance



Package Outline Dimensions

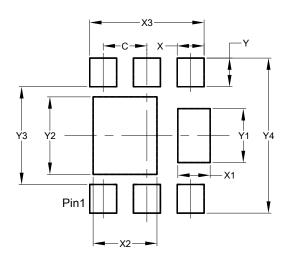
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



U-DFN2020-6							
(Type F)							
Dim	Min	Тур					
Α	0.57	0.63	0.60				
A1	0	0.05	0.03				
A3	-	-	0.15				
b	0.25	0.35	0.30				
D	1.95 2.05 2.00						
D2	0.85	1.05	0.95				
D3	0.33	0.43	0.38				
е	0.65 BSC						
e2	0.863 BSC						
Е	1.95	2.05	2.00				
E2	1.05	1.25	1.15				
E3	0.65	0.75	0.70				
L	0.225	0.325	0.275				
Z	0.20 BSC						
Z 1	0.110 BSC						
All Dimensions in mm							

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)		
С	0.650		
Х	0.400		
X1	0.480		
X2	0.950		
Х3	1.700		
Υ	0.425		
Y1	0.800		
Y2	1.150		
Y3	1.450		
Y4	2.300		



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