

**Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain Source Voltage	V <sub>DSS</sub>	50	V
Gate-Source Voltage	V <sub>GSS</sub>	±20	V
Drain Current (Note 5)	I <sub>D</sub>	280	mA

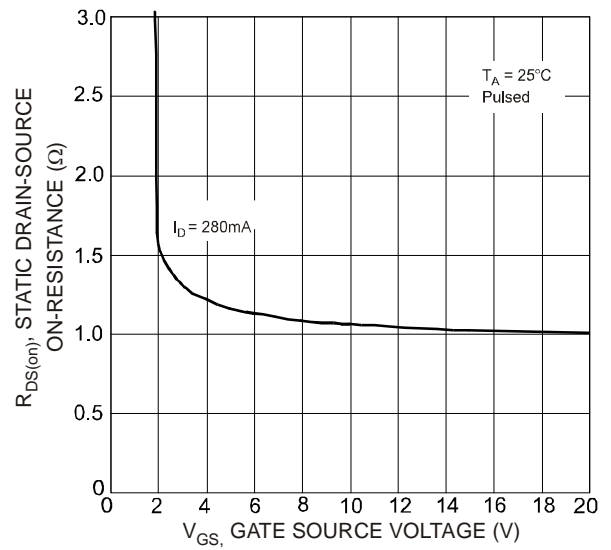
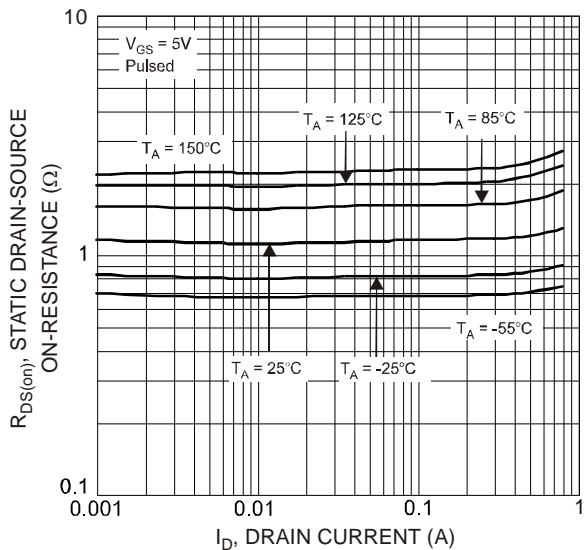
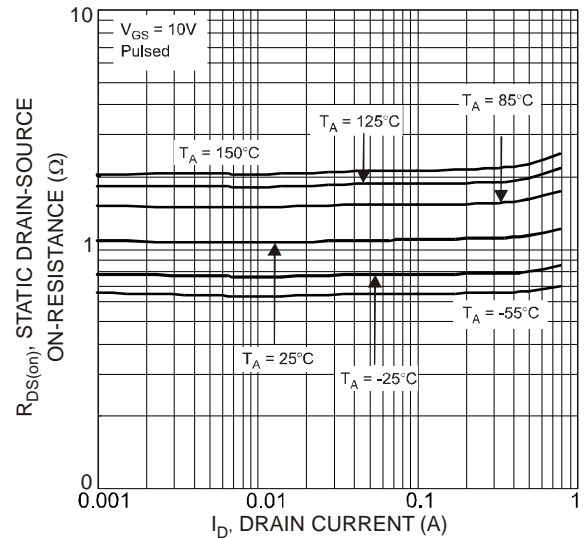
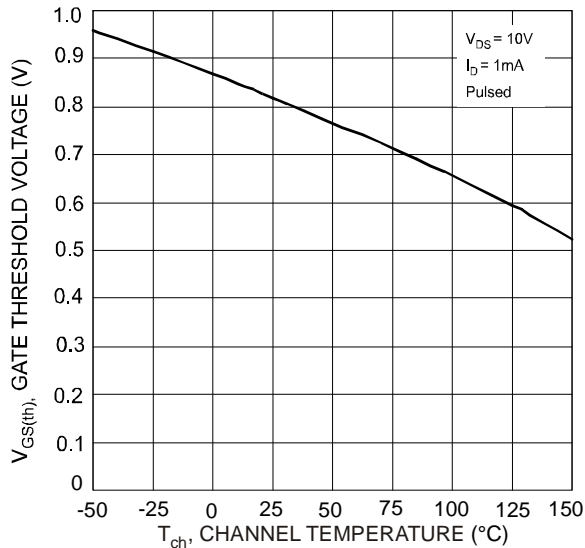
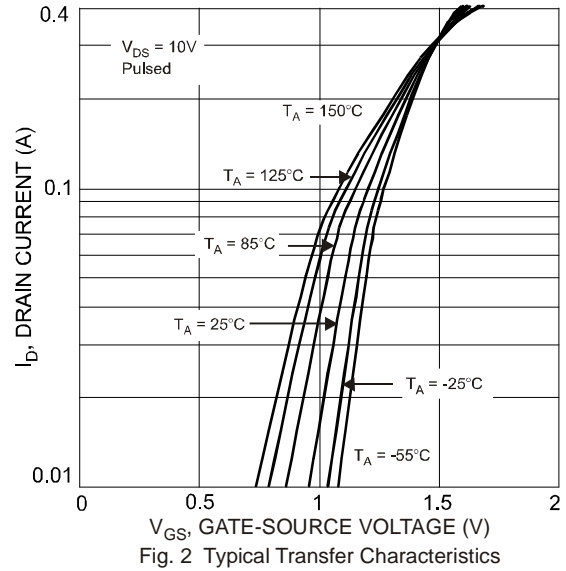
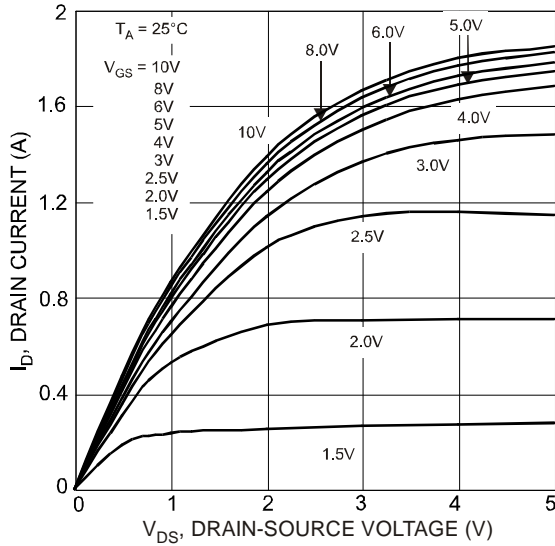
**Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	P <sub>D</sub>	150	mW
Thermal Resistance, Junction to Ambient	R <sub>θJA</sub>	833	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

**Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 6)						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	50	—	—	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = 10μA
Zero Gate Voltage Drain Current @ T <sub>C</sub> = +25°C	I <sub>DSS</sub>	—	—	60	nA	V <sub>DS</sub> = 50V, V <sub>GS</sub> = 0V
Gate-Body Leakage	I <sub>GSS</sub>	—	—	1	μA	V <sub>GS</sub> = ±12V, V <sub>DS</sub> = 0V
				500	nA	V <sub>GS</sub> = ±10V, V <sub>DS</sub> = 0V
				50	nA	V <sub>GS</sub> = ±5V, V <sub>DS</sub> = 0V
ON CHARACTERISTICS (Note 6)						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	0.49	—	1.2	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	—	1.8	3.0	Ω	V <sub>GS</sub> = 1.8V, I <sub>D</sub> = 50mA
		—	1.5	2.5		V <sub>GS</sub> = 2.5V, I <sub>D</sub> = 50mA
		—	1.2	2.0		V <sub>GS</sub> = 5.0V, I <sub>D</sub> = 50mA
On-State Drain Current	I <sub>D(ON)</sub>	0.5	1.4	—	A	V <sub>GS</sub> = 10V, V <sub>DS</sub> = 7.5V
Forward Transconductance	Y <sub>fs</sub>	200	—	—	ms	V <sub>DS</sub> = 10V, I <sub>D</sub> = 0.2A
Source-Drain Diode Forward Voltage	V <sub>SD</sub>	0.5	—	1.4	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = 115mA
DYNAMIC CHARACTERISTICS						
Input Capacitance	C <sub>iss</sub>	—	—	50	pF	V <sub>DS</sub> = 25V, V <sub>GS</sub> = 0V f = 1.0MHz
Output Capacitance	C <sub>oss</sub>	—	—	25	pF	
Reverse Transfer Capacitance	C <sub>rss</sub>	—	—	5.0	pF	

Notes: 5. Device mounted on FR-4 PCB.  
6. Short duration pulse test used to minimize self-heating effect.



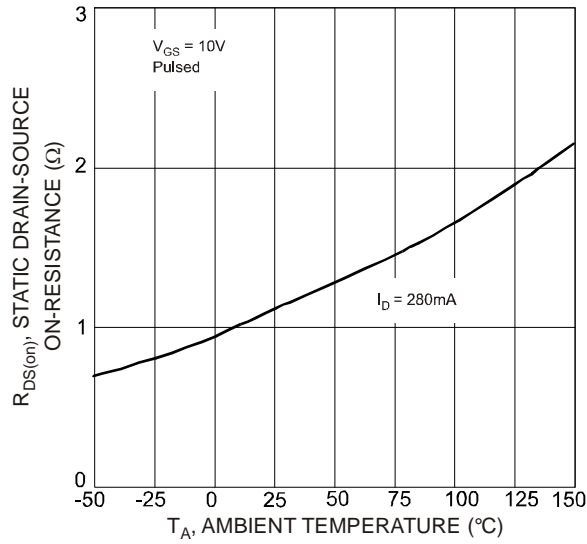


Fig. 7 Static Drain-Source On-State Resistance vs. Ambient Temperature

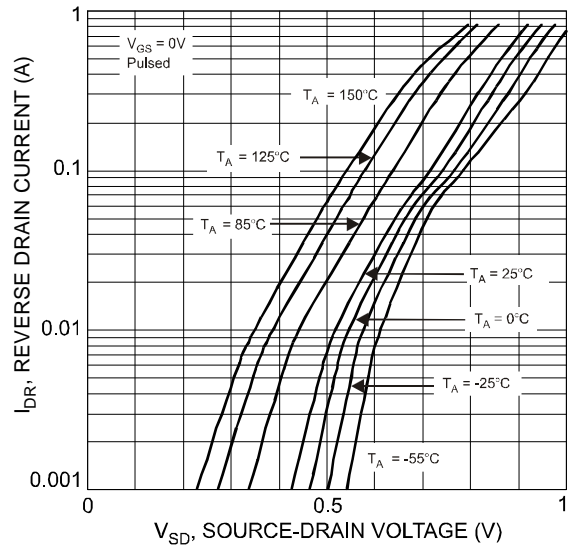


Fig. 8 Reverse Drain Current vs. Source-Drain Voltage

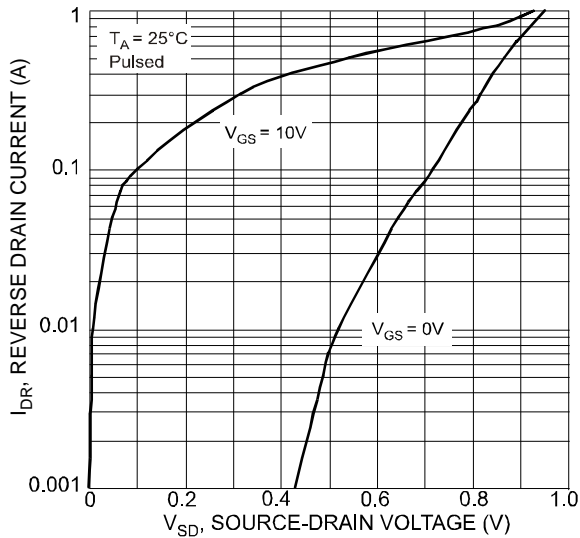


Fig. 9 Reverse Drain Current vs. Source-Drain Voltage

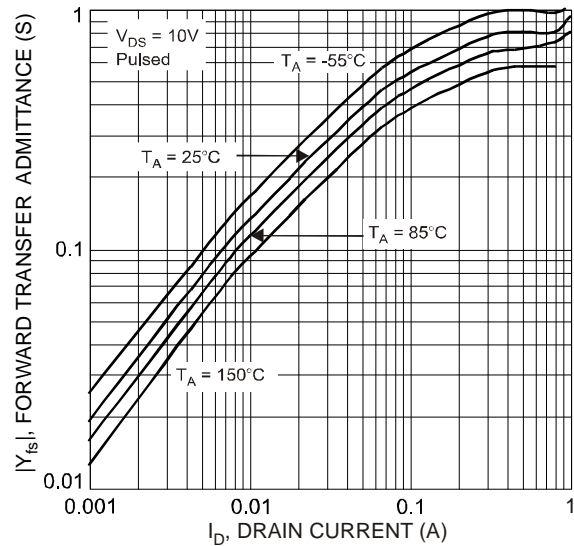
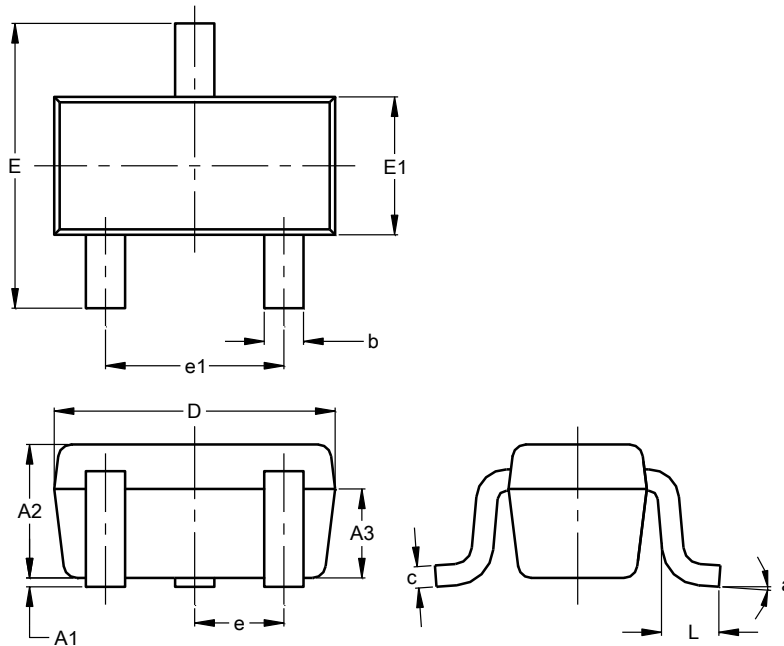


Fig. 10 Forward Transfer Admittance vs. Drain Current

## Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

### SOT523

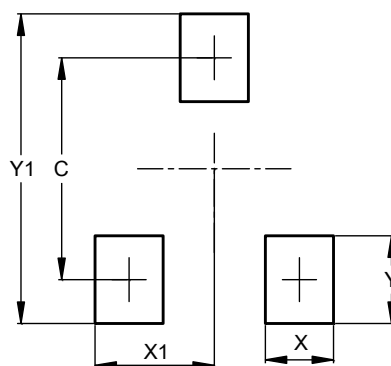


SOT523			
Dim	Min	Max	Typ
A1	0.00	0.10	0.05
A2	0.60	0.80	0.75
A3	0.45	0.65	0.50
b	0.15	0.30	0.22
c	0.10	0.20	0.12
D	1.50	1.70	1.60
E	1.45	1.75	1.60
E1	0.75	0.85	0.80
e	0.50 BSC		
e1	0.90	1.10	1.00
L	0.20	0.40	0.33
a	0°	--	8°
All Dimensions in mm			

## Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

### SOT523



Dimensions	Value (in mm)
C	1.29
X	0.40
X1	0.70
Y	0.51
Y1	1.80

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