

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain Source Voltage	V _{DSS}	30	V
Gate-Source Voltage	V _{GSS}	±12	V
Drain Current (Note 5)	I _D	T _A = +25°C 3.8	A
		T _A = +70°C 3.1	
Drain Current (Note 5)	I _{DM}	15	A
Body-Diode Continuous Current (Note 5)	I _S	2.0	A

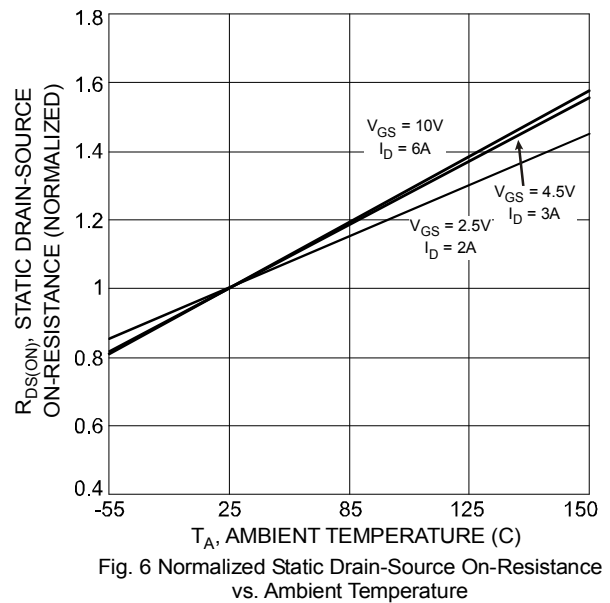
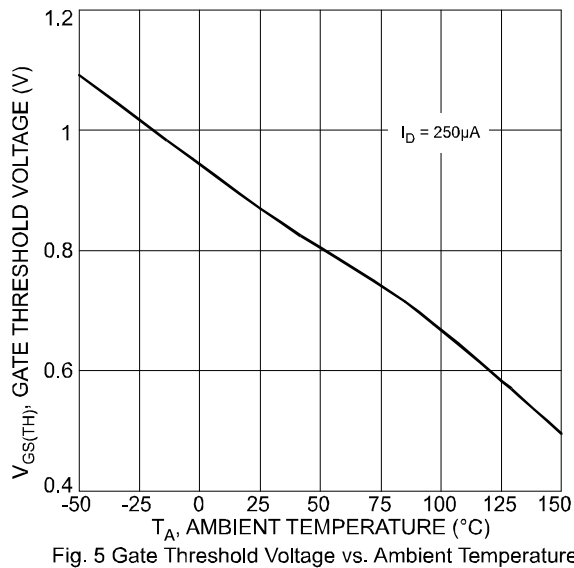
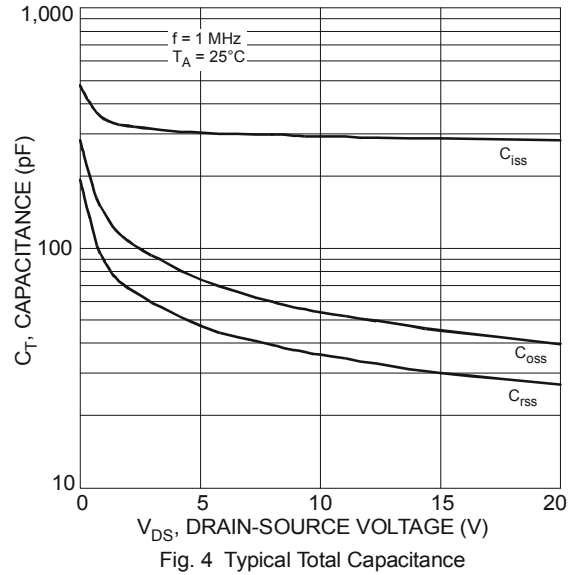
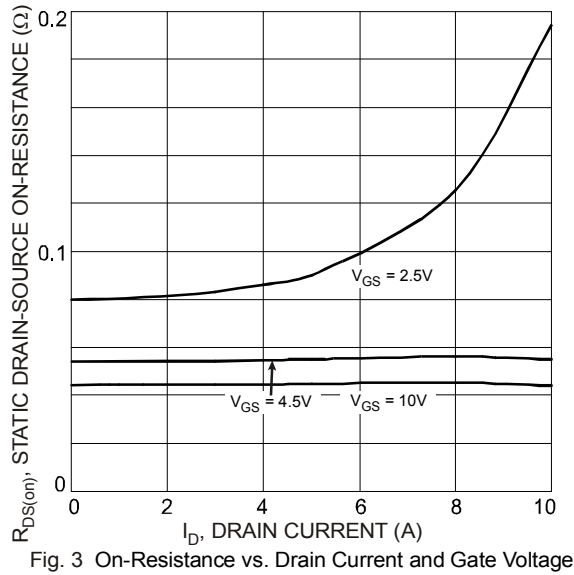
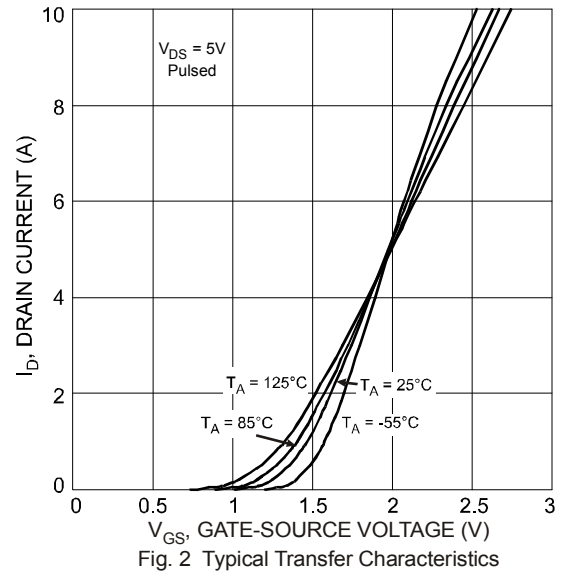
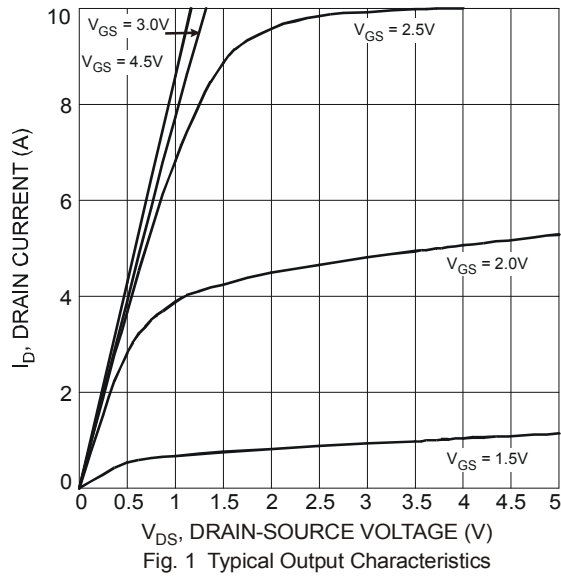
Thermal Characteristics

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	P _D	1.4	W
Thermal Resistance, Junction to Ambient @T _A = +25°C (Note 5)	R _{θJA}	90	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 6)						
Drain-Source Breakdown Voltage	BV _{DSS}	30	—	—	V	V _{GS} = 0V, I _D = 250μA
Zero Gate Voltage Drain Current	I _{DSS}	—	—	800	nA	V _{DS} = 28V, V _{GS} = 0V
Gate-Body Leakage	I _{GSS}	—	—	±80 ±800	nA	V _{GS} = ±12V, V _{DS} = 0V V _{GS} = ±19V, V _{DS} = 0V
ON CHARACTERISTICS (Note 6)						
Gate Threshold Voltage	V _{GS(th)}	0.62	0.92	1.4	V	V _{DS} = V _{GS} , I _D = 250μA
Static Drain-Source On-Resistance	R _{DS(on)}	—	39 52 90	54 72 115	mΩ	V _{GS} = 10V, I _D = 3.8A V _{GS} = 4.5V, I _D = 3.6A V _{GS} = 2.5V, I _D = 3.1A
Forward Transconductance	Y _{fs}	—	3	—	S	V _{DS} = 5V, I _D = 3.1A
Source-Drain Diode Forward Voltage	V _{SD}	—	—	1.16	V	V _{GS} = 0V, I _S = 2.0A
DYNAMIC CHARACTERISTICS (Note 7)						
Gate Resistance	R _g	-	4.17	-	Ω	V _{DS} = 0V, V _{GS} = 0V, f = 1MHz
Total Gate Charge (10V)	Q _g	-	8.2	-	nC	V _{GS} = 10V, V _{DS} = 10V, I _D = 3.8A
Total Gate Charge (4.5V)	Q _g	-	3.7	-	nC	V _{GS} = 4.5V, V _{DS} = 10V, I _D = 3.8A
Gate-Source Charge	Q _{gs}	-	0.7	-	nC	
Gate-Drain Charge	Q _{gd}	-	1.1	-	nC	
Turn-On Delay Time	t _{D(on)}	-	1.14	-	ns	V _{DD} = 15V, V _{GEN} = 10V, R _{GEN} = 6Ω, R _L = 3.9Ω
Turn-On Rise Time	t _r	-	3.49	-	ns	
Turn-Off Delay Time	t _{D(off)}	-	15.02	-	ns	
Turn-Off Fall Time	t _f	-	3.26	-	ns	
Input Capacitance	C _{iss}	—	305	—	pF	V _{DS} = 5V, V _{GS} = 0V f = 1.0MHz
Output Capacitance	C _{oss}	—	74	—	pF	
Reverse Transfer Capacitance	C _{rss}	—	48	—	pF	

- Notes:
5. Device mounted on FR-4 PCB. t ≤ 5 sec.
 6. Short duration pulse test used to minimize self-heating effect.
 7. Guaranteed by design. Not subject to production testing.



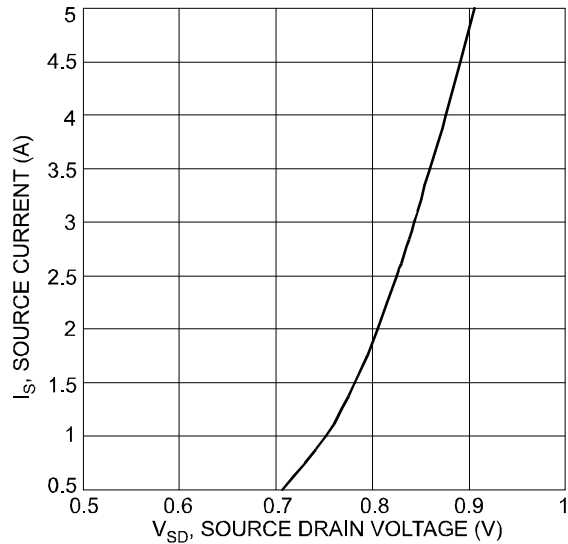


Fig. 7 Reverse Drain Current vs. Source-Drain Voltage

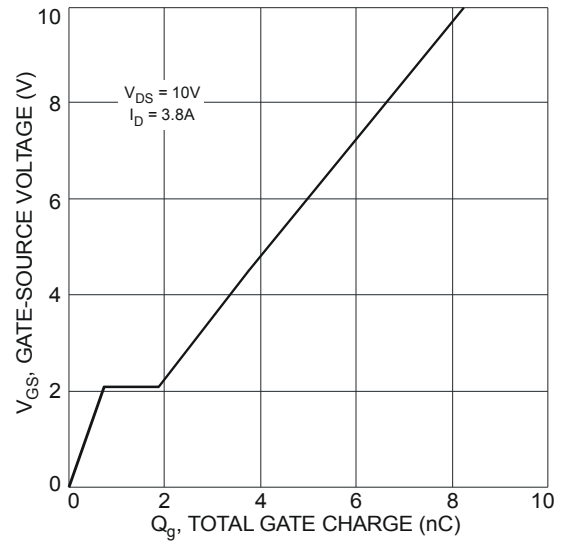
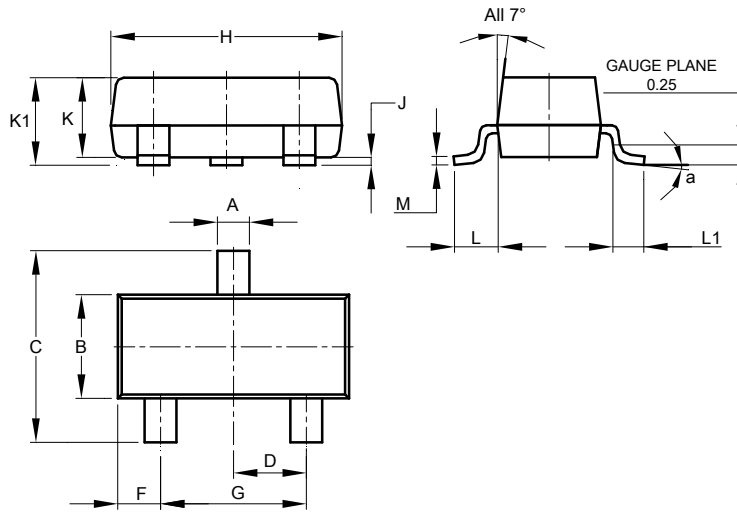


Fig. 8 Gate-Source Voltage vs. Total Gate Charge

Package Outline Dimensions

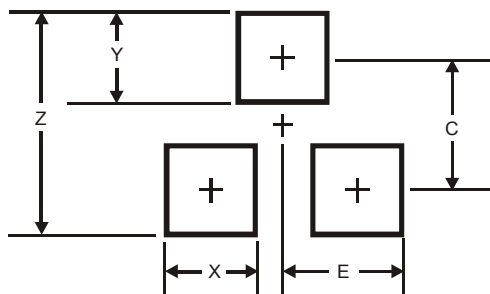
Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> for latest version.



SOT23			
Dim	Min	Max	Typ
A	0.37	0.51	0.40
B	1.20	1.40	1.30
C	2.30	2.50	2.40
D	0.89	1.03	0.915
F	0.45	0.60	0.535
G	1.78	2.05	1.83
H	2.80	3.00	2.90
J	0.013	0.10	0.05
K	0.890	1.00	0.975
K1	0.903	1.10	1.025
L	0.45	0.61	0.55
L1	0.25	0.55	0.40
M	0.085	0.150	0.110
α	8°		
All Dimensions in mm			

Suggested Pad Layout

Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for the latest version.



Dimensions	Value (in mm)
Z	2.9
X	0.8
Y	0.9
C	2.0
E	1.35

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