Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load.
For capacitive load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V_{RRM} V_{RWM} V_R	200	V
RMS Reverse Voltage	$V_{R(RMS)}$	140	V
Average Rectified Output Current (See Figure 4)	I_O	1.0	A
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I_{FSM}	30	A

Thermal Characteristics

Characteristic		Symbol	Typ	Max	Unit
Power Dissipation (Note 6)	@ T_A = +25°C	P_D	—	1.0	W
Thermal Resistance Junction to Soldering Point (Note 7)		$R_{\theta JS}$	—	6	°C/W
Thermal Resistance Junction to Ambient (Note 6)	@T_A = +25°C	$R_{\theta JA}$	116	—	°C/W
Thermal Resistance Junction to Ambient (Note 8)	@T_A = +25°C	$R_{\theta JA}$	182	—	°C/W
Operating and Storage Temperature Range		T_J, T_{STG}	-65 to +150		°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Minimum Reverse Breakdown Voltage	@I_R= 5μA	$V_{(BR)}$	200	V
Maximum Forward Voltage Drop	@ I_F = 0.6A @ I_F = 1.0A	V_{FM}	0.90 0.98	V
Peak Reverse Current at Rated DC Blocking Voltage (Note 5)	@ T_A = +25°C @ T_A = +100°C	I_{RM}	5.0 200	μA
Reverse Recovery Time (Note 9)		t_{RR}	25	ns
Typical Total Capacitance (f = 1MHz, V_R = 4VDC)		C_T	27	pF

Notes:
5. Short duration pulse test used to minimize self-heating effect.
6. Device mounted on 1" x 1", Polymide PCB; 2 oz. Cu pad layout as shown on Diodes Incorporated's website http://www.diodes.com/package-outlines.html.
7. Theoretical $R_{\theta JS}$ calculated from the top center of the die straight down to the PCB cathode tab solder junction.
8. Device mounted on FR-4 PCB, 2 oz. Copper, minimum recommended pad layout per http://www.diodes.com/package-outlines.html.
9. Measured with I_F = 0.5A, I_R = 1.0A, I_{RR} = 0.25A. See figure 7.

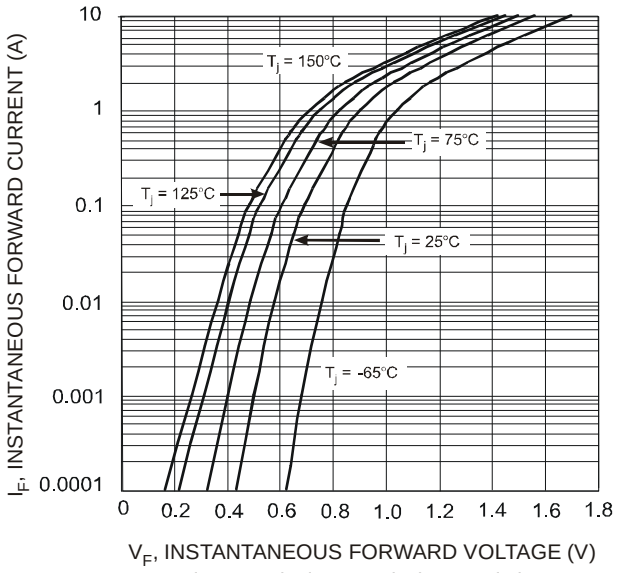


Fig. 1 Typical Forward Characteristics

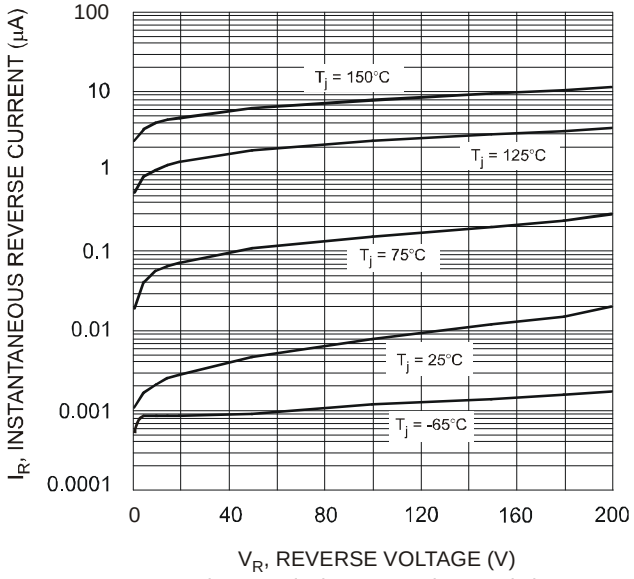


Fig. 2 Typical Reverse Characteristics

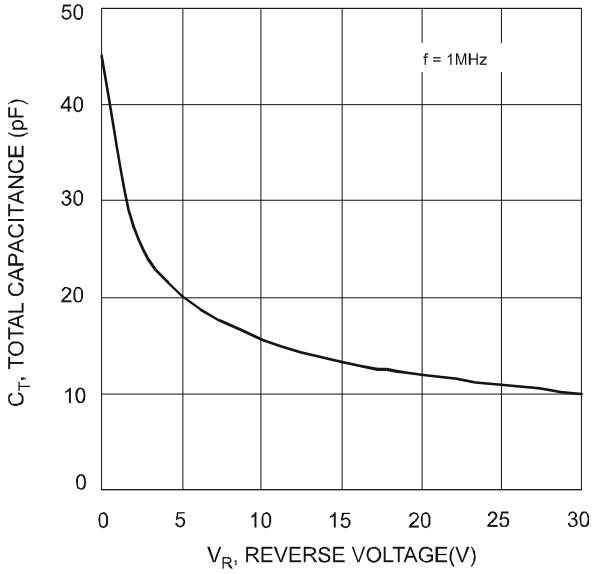


Fig. 3 Typical Total Capacitance

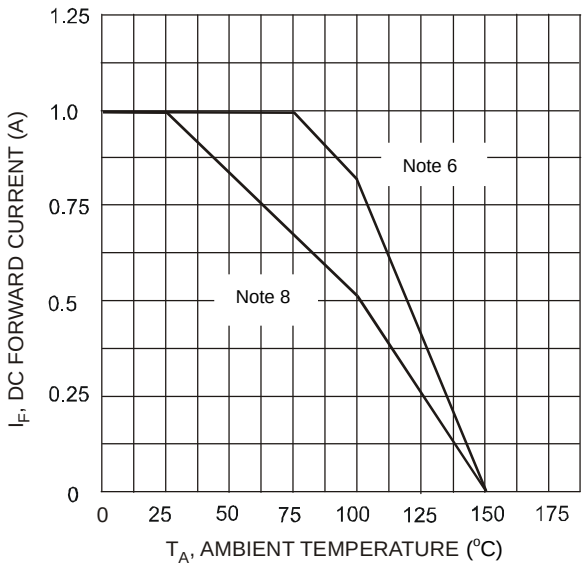


Fig. 4 DC Forward Current Derating

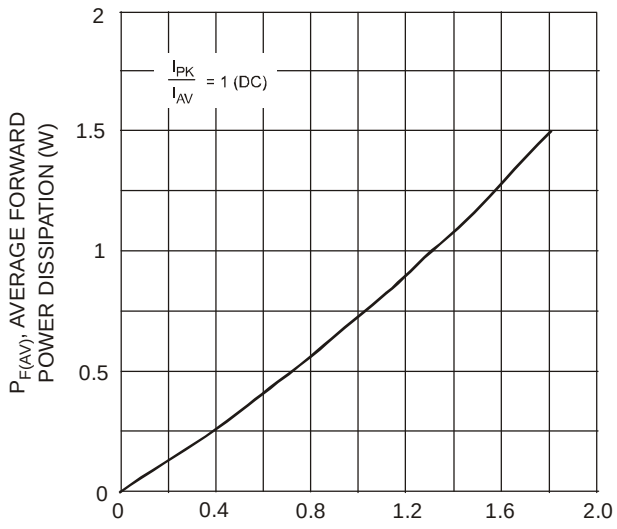


Fig. 5 Forward Power Dissipation

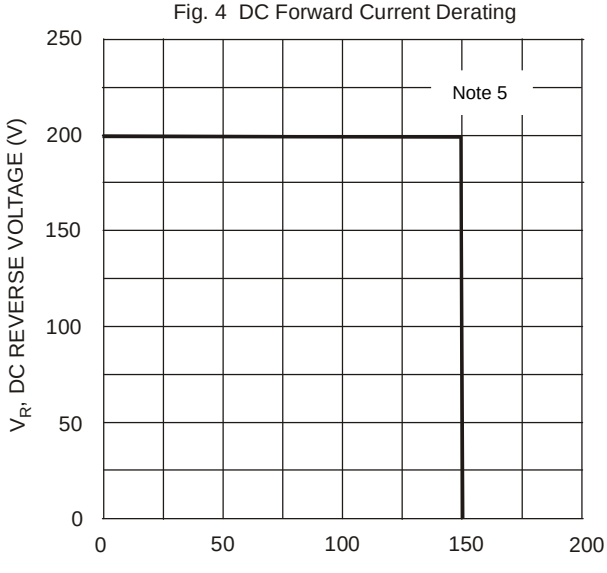
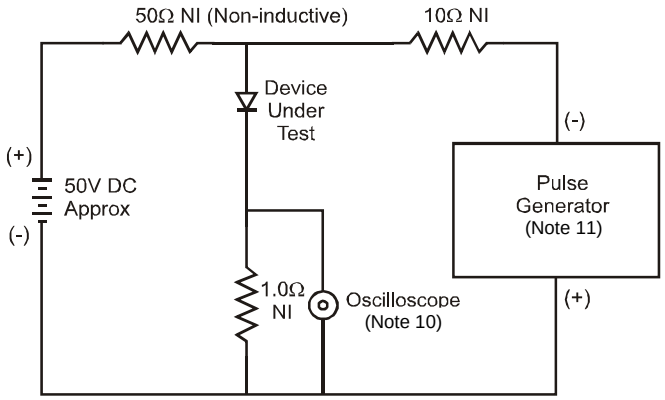
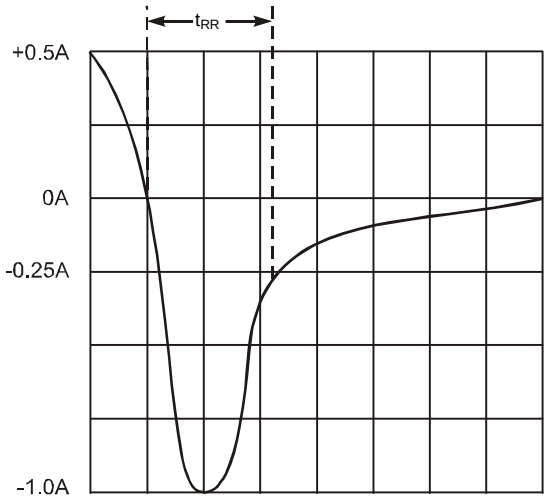


Fig. 6 Operating Temperature Derating



Notes:
10. Rise Time=7.0ns max. Input Impedance=1.0MΩ. 22pF.
11. Rise Time=10ns max. Input Impedance=50Ω.



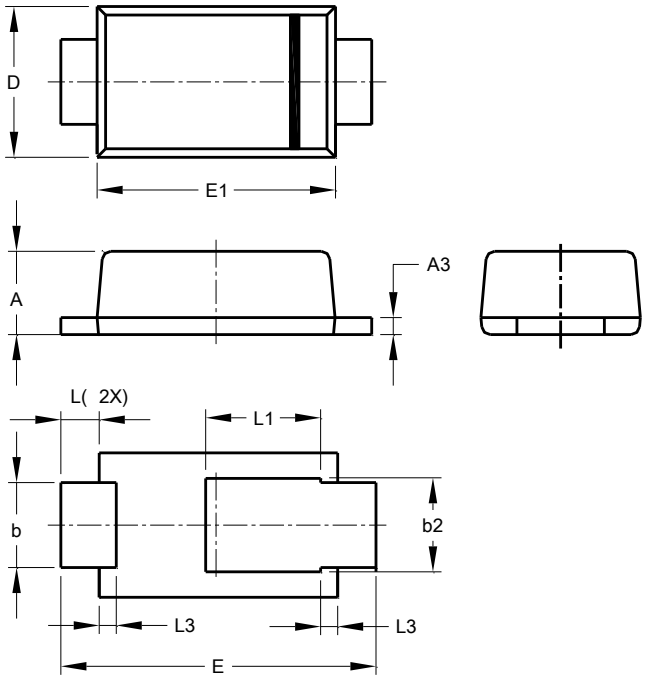
Set time base for 50/100 ns/cm

Fig. 7 Reverse Recovery Time Characteristic and Test Circuit

Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI123

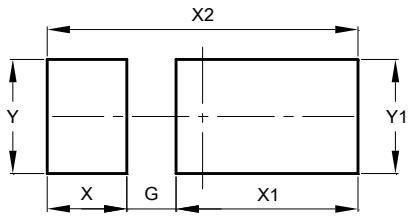


PowerDI123			
Dim	**Min**	**Max**	**Typ**
A	0.93	1.00	0.98
A3	0.15	0.25	0.20
b	0.85	1.25	1.00
b2	1.025	1.125	1.10
D	1.63	1.93	1.78
E	3.50	3.90	3.70
E1	2.60	3.00	2.80
L	0.40	0.50	0.45
L1	1.25	1.40	1.35
L3	0.125	0.275	0.20
All Dimensions in mm			

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI123



Dimensions	Value (in mm)
G	0.65
X	1.05
X1	2.40
X2	4.10
Y	1.50
Y1	1.50

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