

Single phase, half wave, 60Hz, resistive or inductive load.

For capacitance load, derate current by 20%

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _R WM V _R	60	V
RMS Reverse Voltage	V _{R(RMS)}	42	V
Average Forward Current	I _{F(AV)}	2.0	Α
Non-Repetitive Peak Forward Surge Current 8.3ms single half sine-wave superimposed on rated load	I _{FSM}	50	А

Thermal Characteristics

Characteristic	Symbol	Тур	Max	Unit
Thermal Resistance Junction to Soldering Point (Note 6)	$R_{\theta JS}$	_	6	°C/W
Thermal Resistance Junction to Ambient (Note 5)	$R_{\theta JA}$	125	_	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-65 to	+150	°C

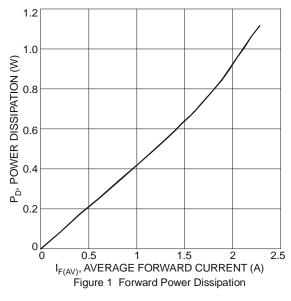
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

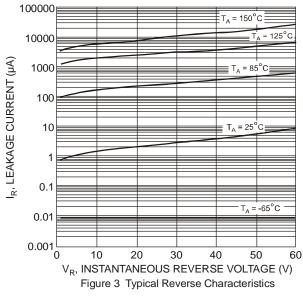
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 7)	$V_{(BR)R}$	60			V	$I_R = 0.2 \text{mA}$
Forward Voltage	V_{F}			0.62 0.56	٧	I _F = 2.0A, T _A = +25°C I _F = 2.0A, T _A = +125°C
Leakage Current (Note 7)	I_R			0.1	mA	$V_R = 60V, T_A = +25^{\circ}C$
Total Capacitance	C _T		67	_	pF	V _R = 10V, f = 1.0MHz

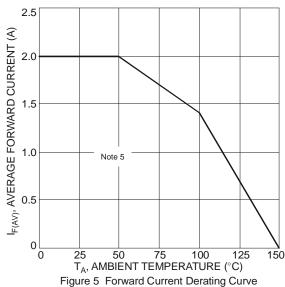
Notes:

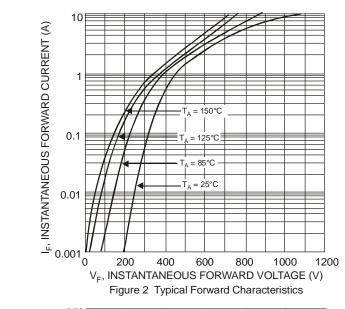
- 5. Part mounted on FR-4 board with 2 oz., minimum recommended copper pad layout, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.
 Theoretical R _{JS} calculated from the top center of the die straight down to the PCB/cathode tab solder junction.
 Short duration pulse test to minimize self-heating effect.











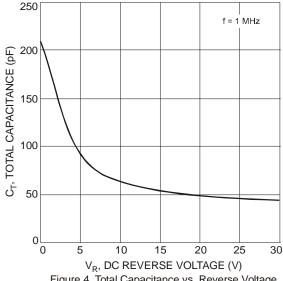
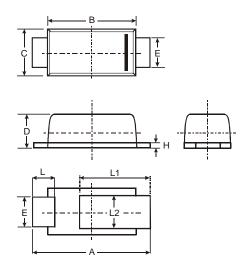


Figure 4 Total Capacitance vs. Reverse Voltage

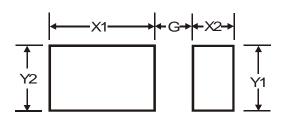


Package Outline Dimensions



PowerDI®123				
Dim	Min	Max	Тур	
Α	3.50	3.90	3.70	
В	2.60	3.00	2.80	
С	1.63	1.93	1.78	
D	0.93	1.00	0.98	
Е	0.85	1.25	1.00	
Н	0.15	0.25	0.20	
L	0.55	0.75	0.65	
L1	1.80	2.20	2.00	
L2	0.95	1.25	1.10	
All Dimensions in mm				

Suggested Pad Layout



Dimensions	Value (in mm)
G	1.0
X1	2.2
X2	0.9
Y1	1.4
Y2	1.4



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