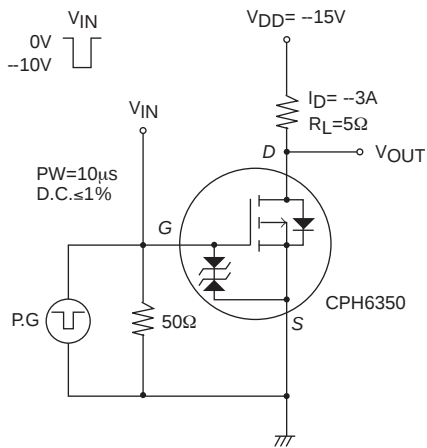
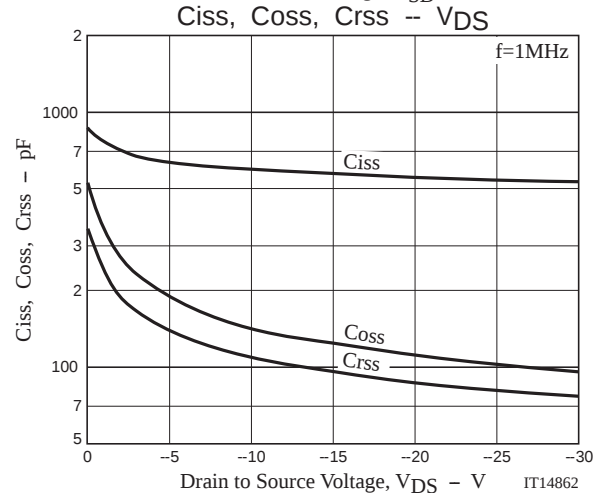
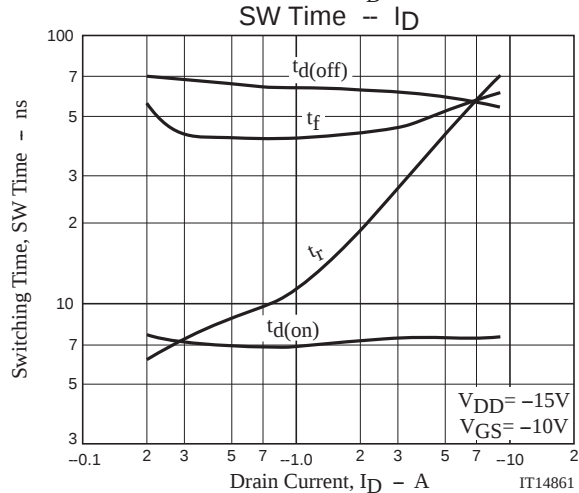
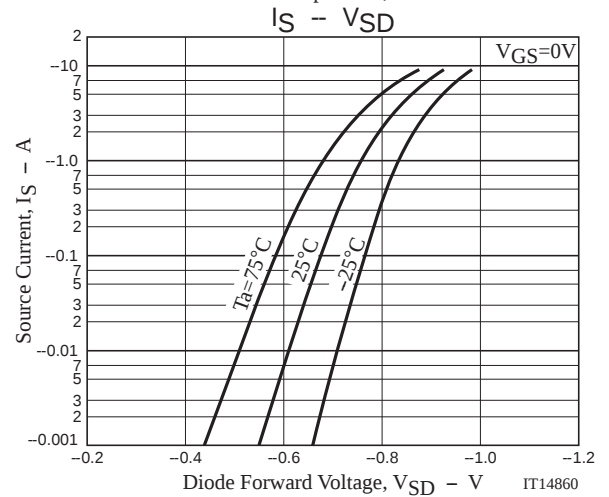
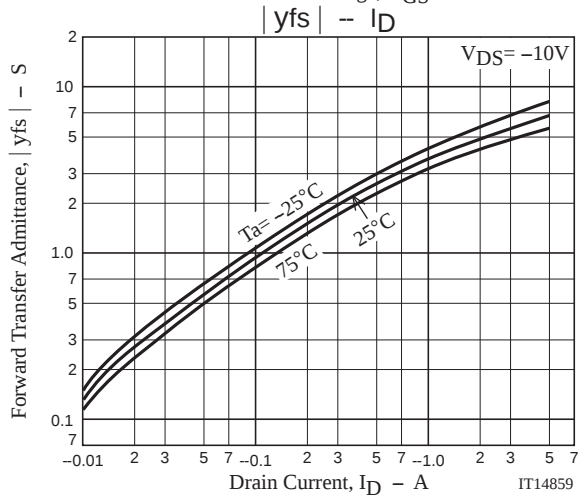
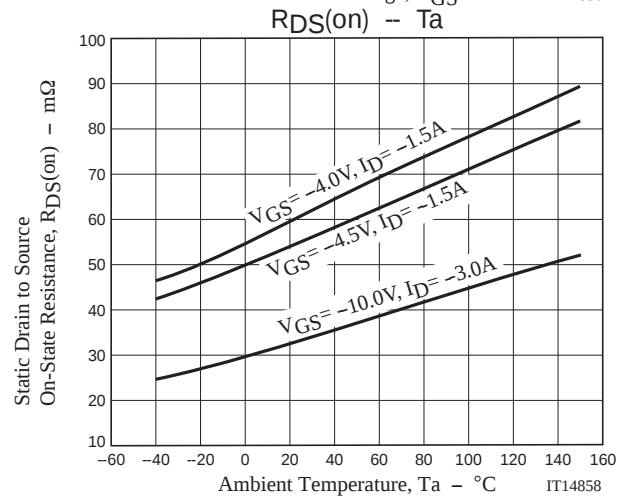
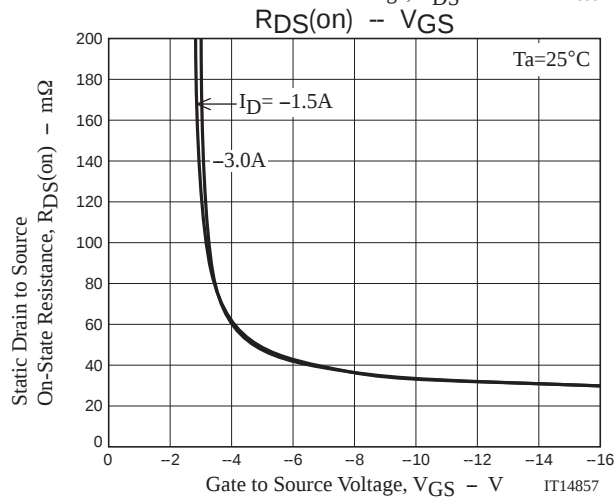
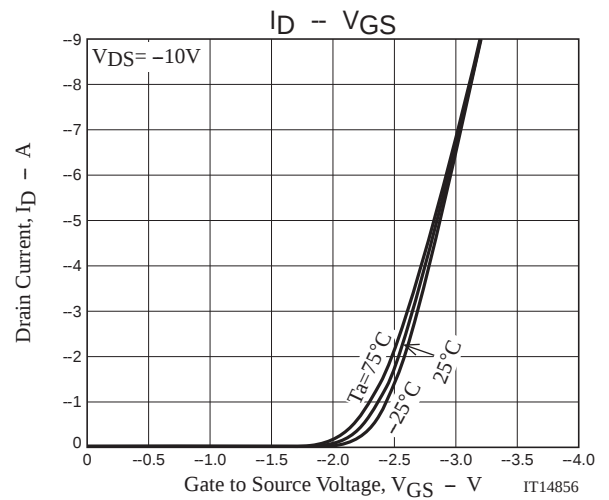
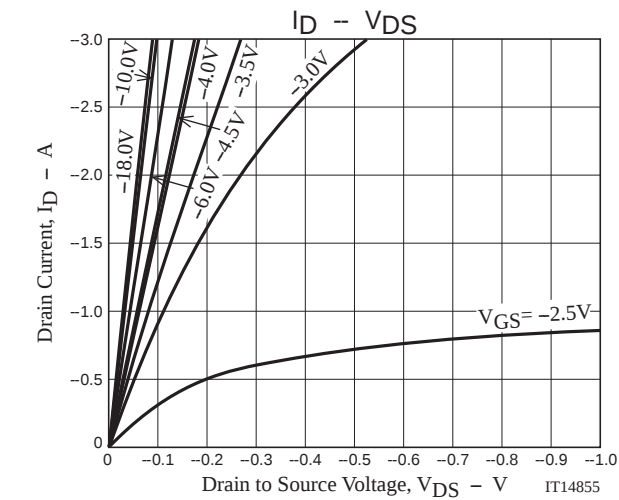


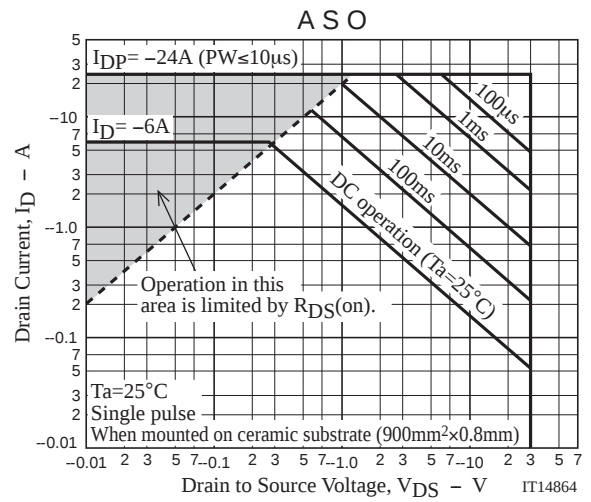
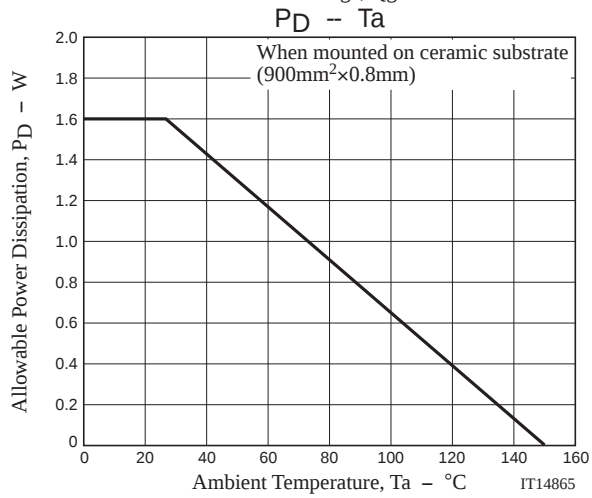
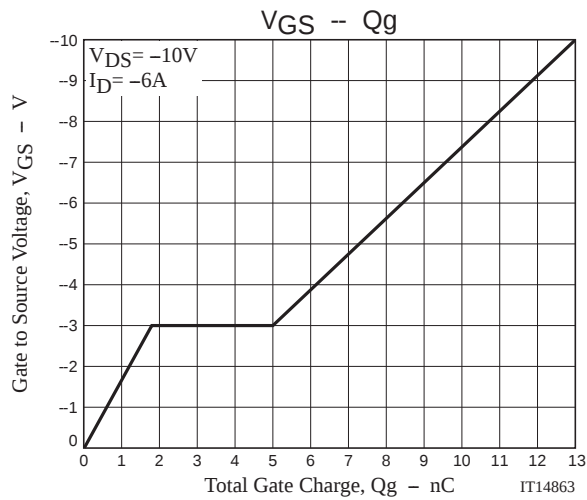
Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain to Source Breakdown Voltage	$V_{(BR)DSS}$	$I_D = -1mA, V_{GS} = 0V$	-30			V
Zero-Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -30V, V_{GS} = 0V$			-1	μA
Gate to Source Leakage Current	I_{GSS}	$V_{GS} = \pm 16V, V_{DS} = 0V$			± 10	μA
Cutoff Voltage	$V_{GS(off)}$	$V_{DS} = -10V, I_D = -1mA$	-1.2		-2.6	V
Forward Transfer Admittance	$ y_{fs} $	$V_{DS} = -10V, I_D = -3A$		5.4		S
Static Drain to Source On-State Resistance	$R_{DS(on)1}$	$I_D = -3A, V_{GS} = -10V$		33	43	m Ω
	$R_{DS(on)2}$	$I_D = -1.5A, V_{GS} = -4.5V$		58	82	m Ω
	$R_{DS(on)3}$	$I_D = -1.5A, V_{GS} = -4V$		61	86	m Ω
Input Capacitance	C_{iss}	$V_{DS} = -10V, f = 1MHz$		600		pF
Output Capacitance	C_{oss}			145		pF
Reverse Transfer Capacitance	C_{rss}			110		pF
Turn-ON Delay Time	$t_d(on)$	See specified Test Circuit.		7.4		ns
Rise Time	t_r			27		ns
Turn-OFF Delay Time	$t_d(off)$			62		ns
Fall Time	t_f			45		ns
Total Gate Charge	Q_g	$V_{DS} = -15V, V_{GS} = -10V, I_D = -6A$		13		nC
Gate to Source Charge	Q_{gs}			1.8		nC
Gate to Drain "Miller" Charge	Q_{gd}			3.2		nC
Diode Forward Voltage	V_{SD}	$I_S = -6A, V_{GS} = 0V$		-0.87	-1.2	V

Switching Time Test Circuit

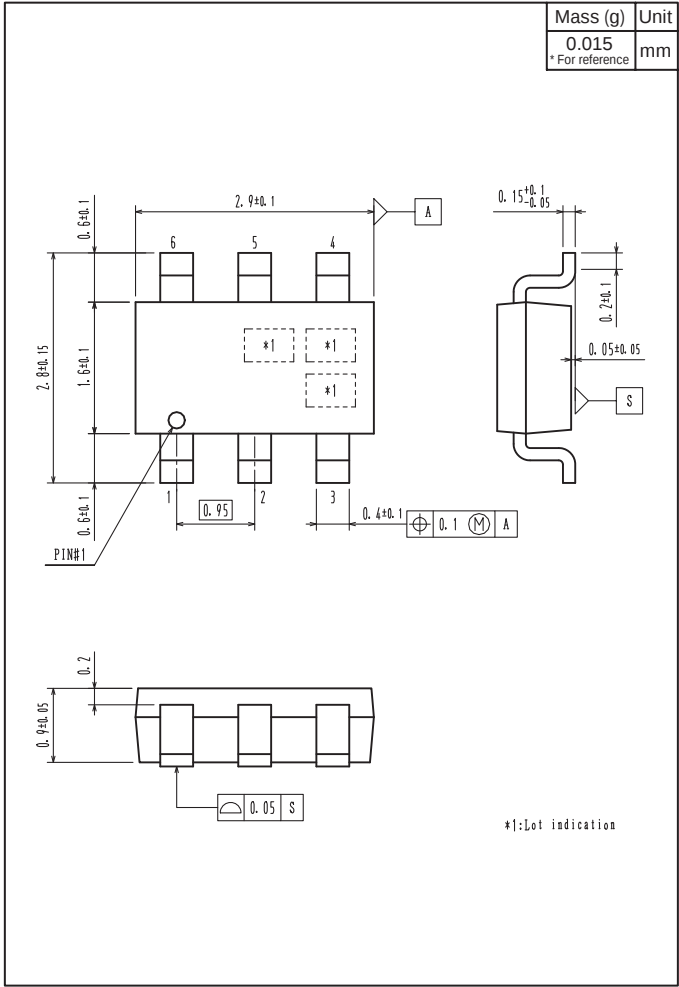




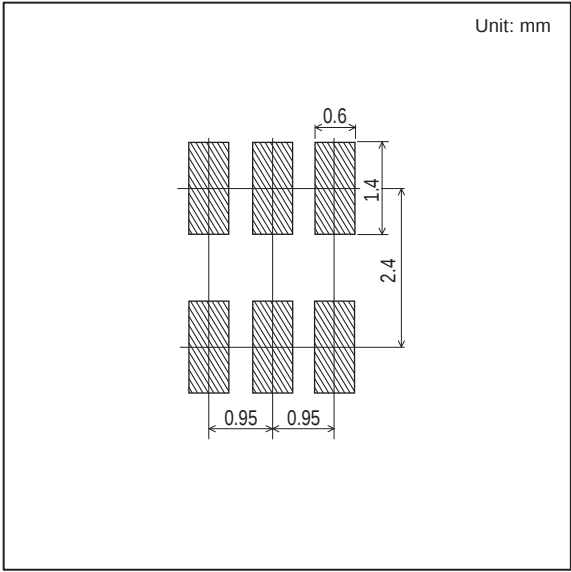


Outline Drawing

CPH6350-TL-E, CPH6350-TL-W



Land Pattern Example



Note on usage : Since the CPH6350 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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