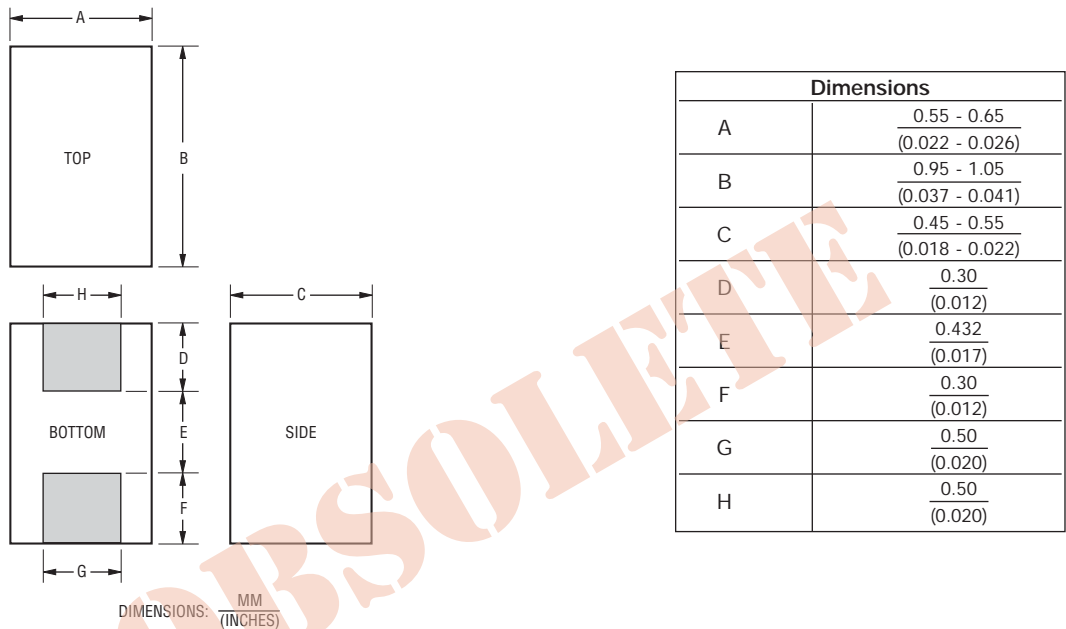


CDDFN2-T4.7C – Surface Mount TVS Diode

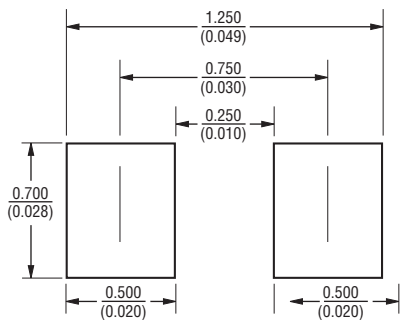
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Product Dimensions

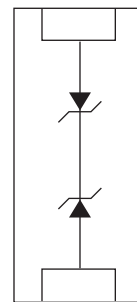
This is a Molded DFN-2 package with lead free 100 % Sn plating on the terminations. It weighs approximately 30 mg.



Recommended PCB Footprint



Block Diagram



Typical Part Marking

CDDFN2-T4.7CE5

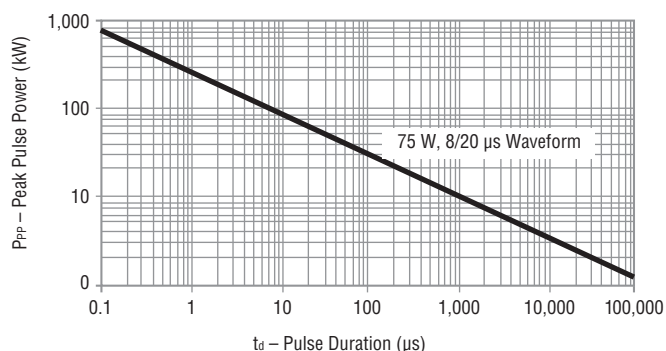
Specifications are subject to change without notice.
Users should verify actual device performance in their specific applications.
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CDDFN2-T4.7C – Surface Mount TVS Diode

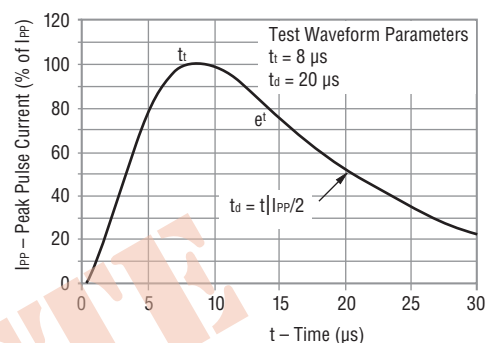
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Performance Graphs

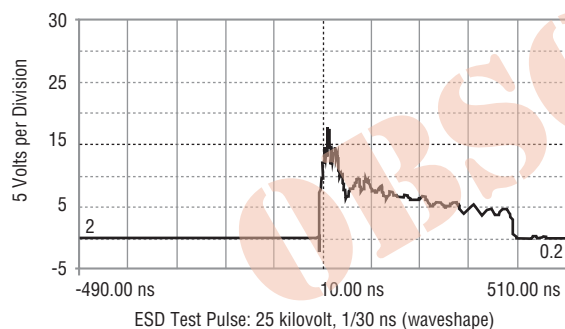
Peak Pulse Power vs Pulse Time



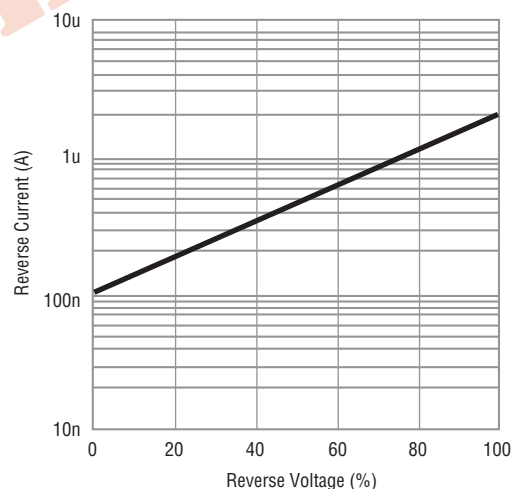
Pulse Wave Form



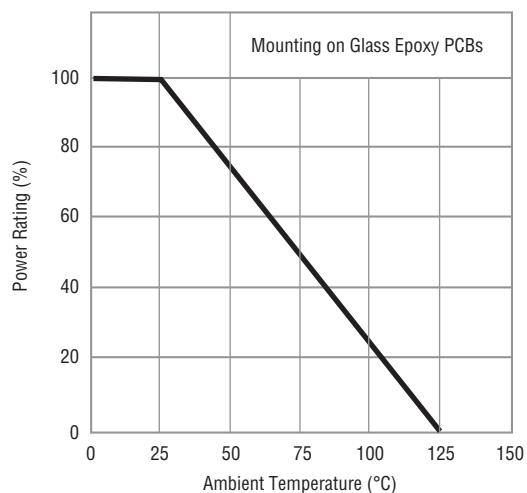
Overshoot & Clamping voltage



Reverse Characteristics



Power Derating Curve



How To Order

Common Code CD DFN2 - T 4.7 C
 Chip Diode
 Package DFN2 = DFN-2 Package
 Model T = Transient Voltage Suppressor
 Working Peak Reverse Voltage 4.7 = 4.7 V_{RM} (Volts)
 Suffix C = Bidirectional Diode

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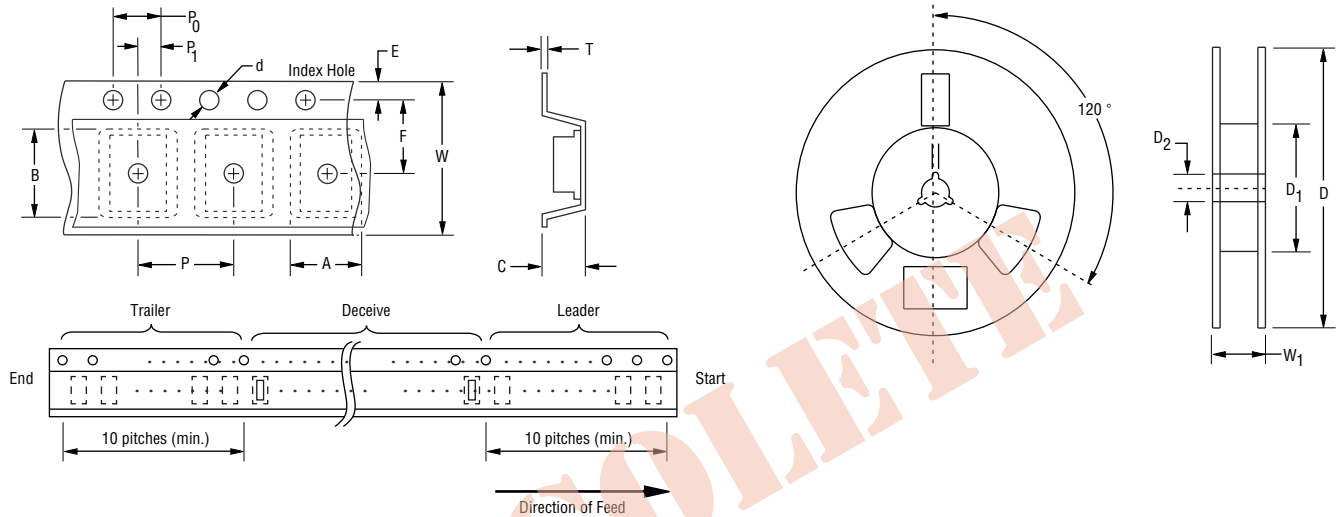
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CDDFN2-T4.7C – Surface Mount TVS Diode

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Packaging Information

The surface mount product is packaged in an 8 mm x 4 mm tape and reel format per EIA-481 standard.



Item	Symbol	DFN-2
Carrier Width	A	0.80 ± 0.10 (0.031 - 0.004)
Carrier Length	B	1.20 ± 0.10 (0.047 - 0.004)
Carrier Depth	C	0.70 ± 0.10 (0.027 - 0.004)
Sprocket Hole	d	1.55 ± 0.05 (0.061 - 0.002)
Reel Outside Diameter	D	178 (7.008)
Reel Inner Diameter	D1	50.0 (1.969) Min.
Feed Hole Diameter	D2	13.0 ± 0.20 (0.512 - 0.008)
Sprocket Hole Position	E	1.75 ± 0.10 (0.069 - 0.004)
Punch Hole Position	F	3.50 ± 0.05 (0.138 - 0.002)
Punch Hole Pitch	P	4.00 ± 0.10 (0.157 - 0.004)
Sprocket Hole Pitch	P0	4.00 ± 0.10 (0.157 - 0.004)
Embossment Center	P1	2.00 ± 0.05 (0.079 - 0.002)
Overall Tape Thickness	T	0.20 ± 0.10 (0.008 - 0.004)
Tape Width	W	8.00 ± 0.20 (0.315 - 0.008)
Reel Width	W1	14.4 (0.567) Max.
Quantity per Reel	—	5,000

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