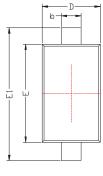
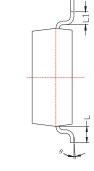
Device	Marking	Zener Voltage Range				Maximum Zener Impedance			Maximum Reverse Current		Typical Temperature		Test Current
		Vz@lzt			Izt	Zzt @Izt	Zzk @Izk	lzk	IR	VR	coeffic IZTC=		IZTC
		Nom(V)	Min(V)	Max(V)	mA	Ω.	2	mA	uA	V	Min	Max	mA
BZT52B15	2WJ	15	14.70	15.30	5	30	200	1.0	0.1	10.5	9.2	13.0	5
BZT52B16	2WK	16	15.68	16.32	5	40	200	1.0	0.1	11.2	10.4	14.0	5
BZT52B18	2WL	18	17.64	18.36	5	45	225	1.0	0.1	12.6	12.4	16.0	5
BZT52B20	2WM	20	19.60	20.40	5	55	225	1.0	0.1	14.0	14.4	18.0	5
BZT52B22	2WN	22	21.56	22.44	5	55	250	1.0	0.1	15.4	16.4	20.0	5
BZT52B24	2WO	24	23.52	24.48	5	70	250	1.0	0.1	16.8	18.4	22.0	5
BZT52B27	2WP	27	26.46	27.54	2	80	300	0.5	0.1	18.9	21.4	25.3	2
BZT52B30	2WQ	30	29.40	30.60	2	80	300	0.5	0.1	21.0	24.4	29.4	2
BZT52B33	2WR	33	32.34	33.66	2	80	325	0.5	0.1	23.1	27.4	33.4	2
BZT52B36	2WS	36	35.28	36.72	2	90	350	0.5	0.1	25.2	30.4	37.4	2
BZT52B39	2WT	39	38.22	39.78	2	130	350	0.5	0.1	27.3	33.4	41.2	2
BZT52B43	2WU	43	41.16	43.84	2	100	700	1.0	0.1	32.0	10.0	12.0	5

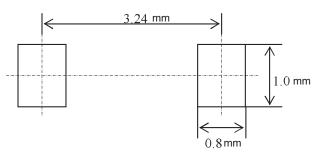
SOD-123 PACKAGE OUTLINEPlastic surface mounted package





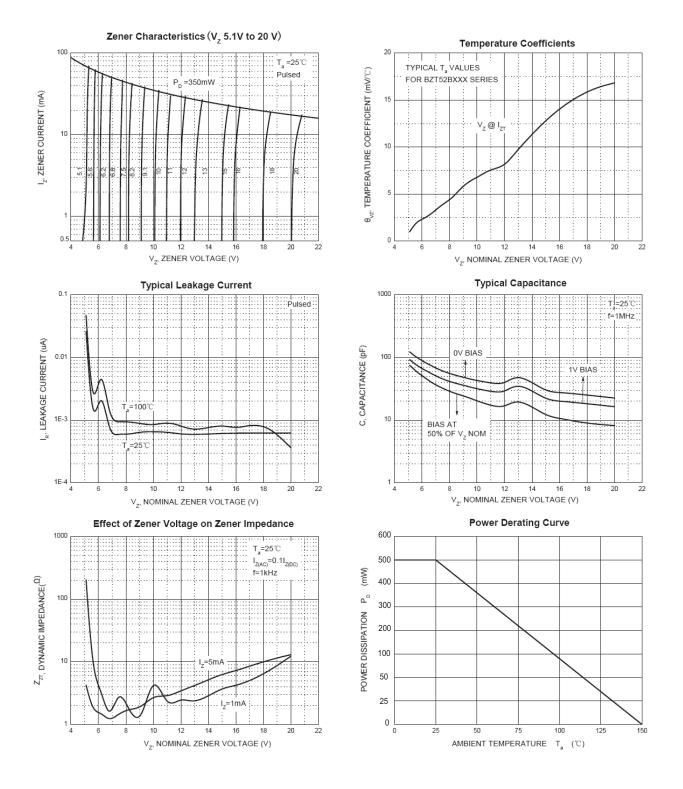
	DIMENSIONS					
SYMBOL	MIN.	MAX.				
А	1.050	1.250				
A1	0.000	0.100				
A2	1.050	1.150				
b	0.450	0.650				
C	0.080	0.150				
D	1.500	1.700				
E	2.600	2,800				
E1	3.550	3,850				
	0.500REF					
L1	0.250	0.450				
θ	0°	8°				

Precautions: PCB Design Recommended land dimensions for SOD-123 diode. Electrode patterns for PCBs





RATING AND CHARACTERISTICS CURVES (BZT52BXXX SERIES)



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PACKAGING OF DIODE

REEL PACK

PACKAGE	PACKING	REEL	COMPONENT	TAPE SPACE	REEL DIA	CARTON SIZE	EA PER	GROSS
	CODE	(EA)	SPACE(mm)	(mm)	(mm)	(mm)	CARTON	WEIGHT(Kg)
SOD-123	-T	3,000			178	438*438*220	180,000	9.00

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