

**Maximum Ratings** (@T<sub>A</sub> = +25°C unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage	V <sub>RRM</sub>	85	V
Working Peak Reverse Voltage	V <sub>RWM</sub>		
DC Blocking Voltage	V <sub>R</sub>		
RMS Reverse Voltage	V <sub>R(RMS)</sub>	60	V
Forward Continuous Current (Note 5)	I <sub>FM</sub>	155	mA
		75	
Repetitive Peak Forward Current	I <sub>FRM</sub>	500	mA
Non-Repetitive Peak Forward Surge Current	I <sub>FSM</sub>	4.0	A
		1.0	
		0.5	

**Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	P <sub>D</sub>	150	mW
Thermal Resistance Junction to Ambient (Note 5)	R <sub>θJA</sub>	833	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-65 to +150	°C

**Electrical Characteristics** (@T<sub>A</sub> = +25°C unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 6)	V <sub>(BR)R</sub>	85	—	—	V	I <sub>R</sub> = 100μA
Forward Voltage	V <sub>F</sub>	—	—	0.715	V	I <sub>F</sub> = 1.0mA
				0.855		I <sub>F</sub> = 10mA
				1.0		I <sub>F</sub> = 50mA
				1.25		I <sub>F</sub> = 150mA
Leakage Current (Note 6)	I <sub>R</sub>	—	—	2.0	μA	V <sub>R</sub> = 75V
				100	μA	V <sub>R</sub> = 75V, T <sub>J</sub> = +150°C
				60	μA	V <sub>R</sub> = 25V, T <sub>J</sub> = +150°C
				30	nA	V <sub>R</sub> = 25V
Total Capacitance	C <sub>T</sub>	—	1.5	—	pF	V <sub>R</sub> = 0, f = 1.0MHz
Reverse Recovery Time	t <sub>rr</sub>	—	—	4.0	ns	I <sub>F</sub> = I <sub>R</sub> = 10mA, I <sub>rr</sub> = 0.1 × I <sub>R</sub> , R <sub>L</sub> = 100Ω

Notes: 5. Device mounted on FR-4 PC board with recommended pad layout, which can be found on our website at [https:// www.diodes.com/package-outlines.html](https://www.diodes.com/package-outlines.html).  
6. Short duration pulse test used to minimize self-heating effect.

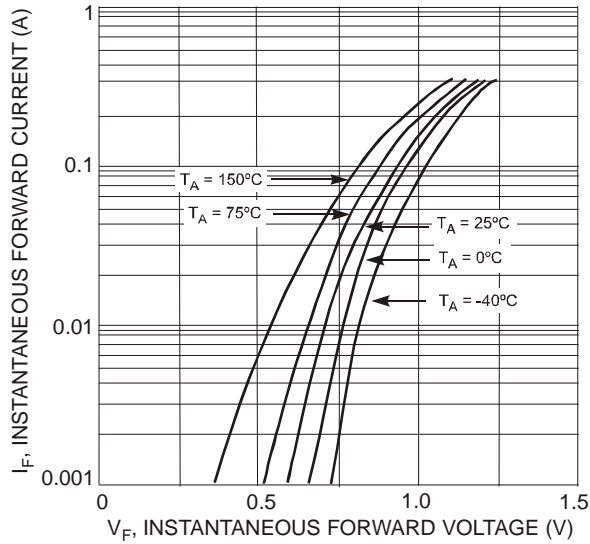


Fig. 1 Typical Forward Characteristics, Per Element

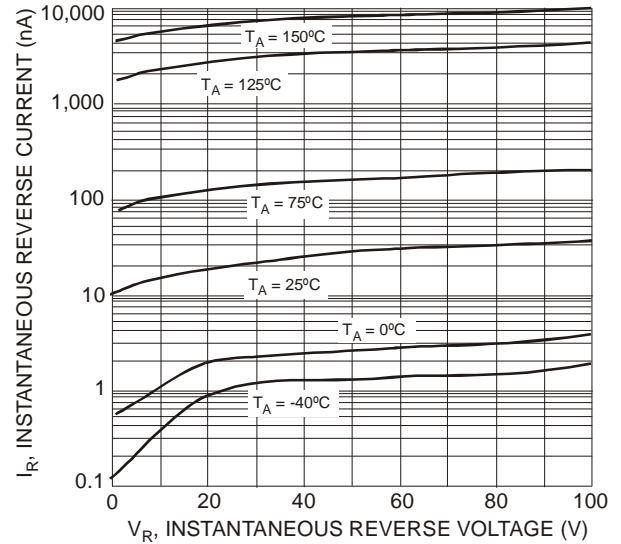


Fig. 2 Typical Reverse Characteristics, Per Element

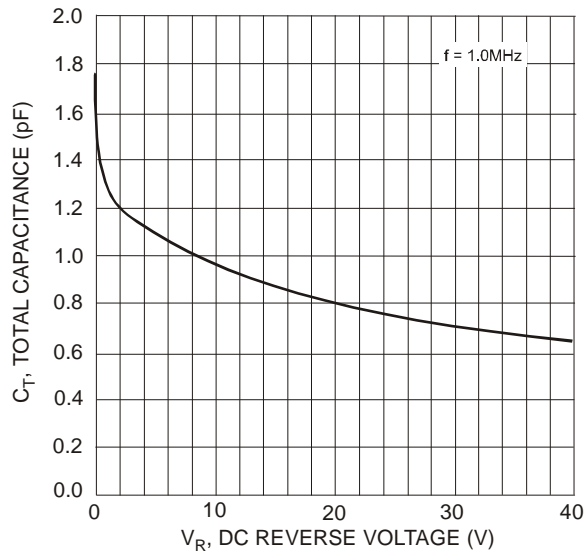


Fig. 3 Total Capacitance vs. Reverse Voltage, Per Element

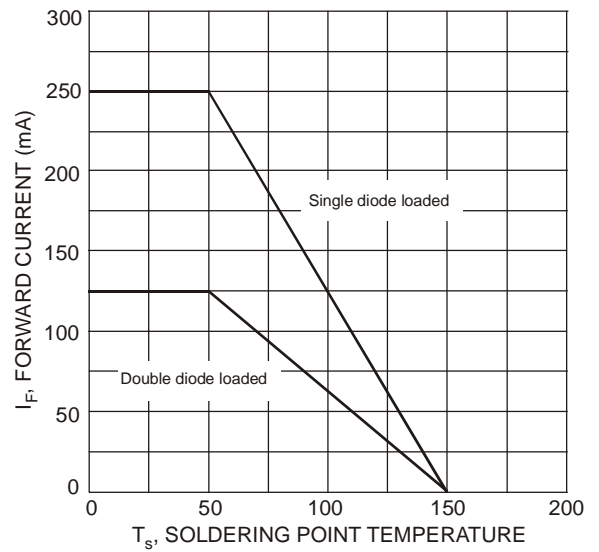
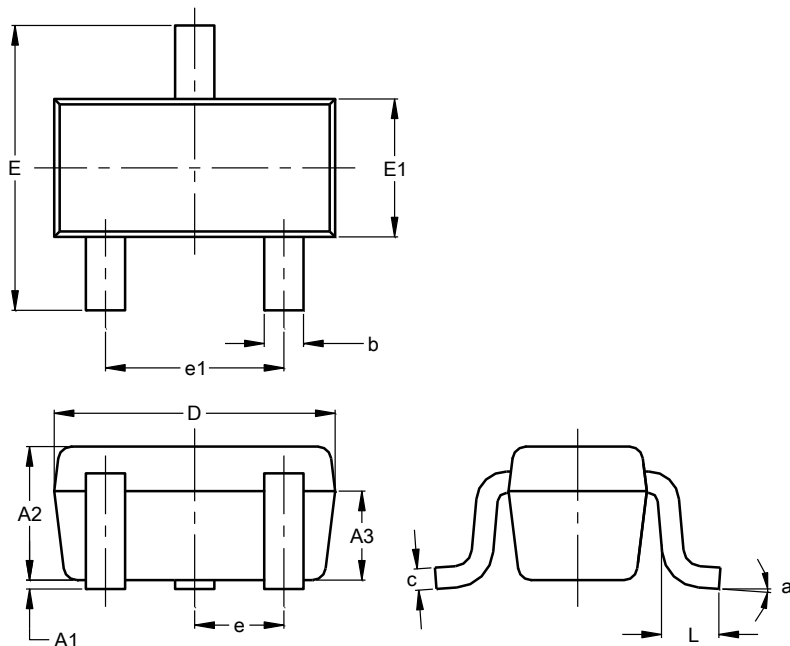


Fig. 4 Current Derating Curve, Total Package

## Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SOT523

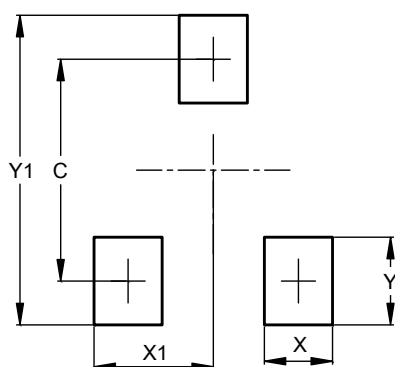


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Dim	Min	Max	Typ
A1	0.00	0.10	0.05
A2	0.60	0.80	0.75
A3	0.45	0.65	0.50
b	0.15	0.30	0.22
c	0.10	0.20	0.12
D	1.50	1.70	1.60
E	1.45	1.75	1.60
E1	0.75	0.85	0.80
e	0.50 BSC		
e1	0.90	1.10	1.00
L	0.20	0.40	0.33
a	0°	--	8°
All Dimensions in mm			

## Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

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Dimensions	Value (in mm)
C	1.29
X	0.40
X1	0.70
Y	0.51
Y1	1.80

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