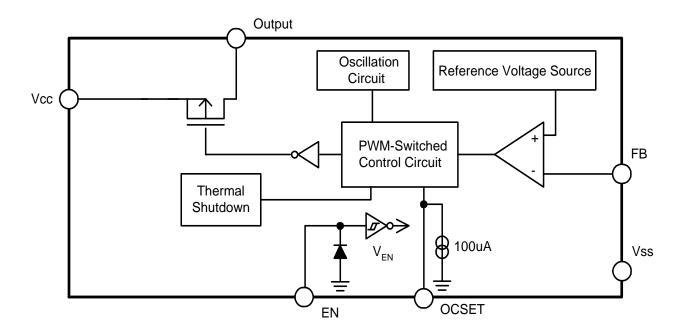


## **Pin Descriptions**

Pin Number	Pin Name	Description
1	FB	Feedback pin
2	EN	Power-off pin H: Normal operation (Step-down operation) L: Step-down operation stopped (All circuits deactivated)
3	OCSET	Add an external resistor to set max output current
4	Vcc	IC power supply pin
5, 6	Output	Switch Pin. Connect external inductor/diode here. Minimize trace area at this pin to reduce EMI
7, 8	V <sub>SS</sub>	GND Pin

## **Functional Block Diagram**



## **Absolute Maximum Ratings**

Symbol	Parameter	Rating	Unit
V <sub>CC</sub>	V <sub>CC</sub> Pin Voltage	$V_{SS}$ - 0.3 to $V_{SS}$ + 20	V
$V_{FB}$	FB Pin Voltage	$V_{SS}$ - 0.3 to $V_{CC}$	V
$V_{EN}$	EN Pin Voltage	$V_{SS}$ - 0.3 to $V_{IN}$ + 0.3	V
V <sub>OUT</sub>	Output Pin Voltage	V <sub>SS</sub> - 0.3 to V <sub>IN</sub> + 0.3	V
$P_{D}$	Power Dissipation	Internally Limited	mW
TJ	Operating Junction Temperature Range	-40 to +125	°C
T <sub>ST</sub>	Storage Temperature Range	-40 to +150	°C

Caution: The absolute maximum ratings are rated values exceeding which the product could suffer physical damage. These values must therefore not be exceeded under any conditions.



# **Recommended Operating Conditions**

Symbol	Parameter	Min	Max	Unit
$V_{IN}$	Input Voltage	3.6	18	V
Гоит	Output Current	0	2	Α
T <sub>A</sub>	Operating Ambient Temperature	-20	+85	°C

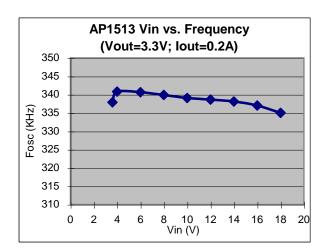
## **Electrical Characteristics** ( $V_{IN} = 12V$ , $T_A = +25$ °C, unless otherwise specified.)

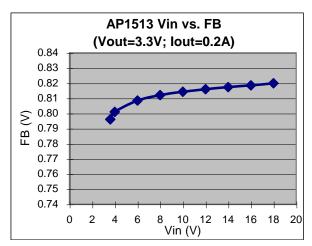
Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
$V_{FB}$	Feedback Voltage	I <sub>OUT</sub> = 0.1A	0.784	0.8	0.816	V
$I_{FB}$	Feedback Bias Current	I <sub>OUT</sub> = 0.1A	_	0.1	0.5	μΑ
I <sub>SW</sub>	Switch Current	_	2.5	_	_	Α
I <sub>SSS</sub>	Current Consumption During Power Off	V <sub>EN</sub> = 0V	_	10	_	μA
ΔV <sub>OUT</sub> /V <sub>OUT</sub>	Line Regulation	V <sub>IN</sub> = 5V to 18V	_	1	2	%
$\Delta V_{OUT}$ / $V_{OUT}$	Load Regulation	I <sub>OUT</sub> = 0.1 to 2A	_	0.2	0.5	%
fosc	Oscillation Frequency	Measure waveform at Output pin	240	300	400	kHz
f <sub>OSC1</sub>	Frequency of Current Limit or Short Circuit Protect	Measure waveform at Output pin	10	_	_	kHz
$V_{SH}$	SN Bis James Vallage	Evaluate oscillation at Output pin	2.0	_	_	
$V_{SL}$	EN Pin Input Voltage	Evaluate oscillation stop at Output pin	_	_	0.8	V
I <sub>SH</sub>		_	_	20		μA
I <sub>SL</sub>	EN Pin Input Leakage Current	_	_	-10	_	μΑ
I <sub>OCSET</sub>	OCSET Pin Bias Current	_	75	90	105	μΑ
t <sub>SS</sub>	Soft-Start Time	_	0.3	2	5	ms
Б	Into week MOSEET D	$V_{IN} = 5V$ , $V_{FB} = 0V$	_	110	150	0
R <sub>DSON</sub>	Internal MOSFET R <sub>DSON</sub>	$V_{IN} = 12V, V_{FB} = 0V$	_	70	100	mΩ
EFFI	Efficiency	$V_{IN} = 12V$ , $V_{OUT} = 5V$ $I_{OUT} = 2A$	_	92	_	%
$\theta_{JA}$	Thermal Resistance Junction-to- Ambient	SO-8 (Note 4)	_	134	_	°C/W
$\theta_{JC}$	Thermal Resistance Junction-to-Case	SO-8 (Note 4)	_	22	_	°C/W

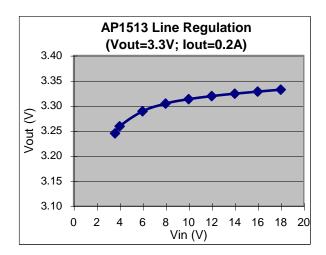
Note: 4. Test conditions: Device mounted on FR-4 PCB, 2"\*2", 2oz copper minimum recommended pad layout, single-sided, PC boards.

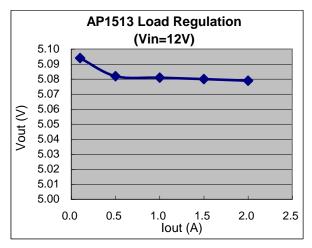


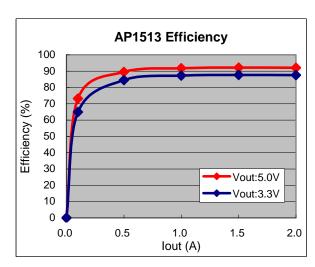
### **Performance Characteristics**





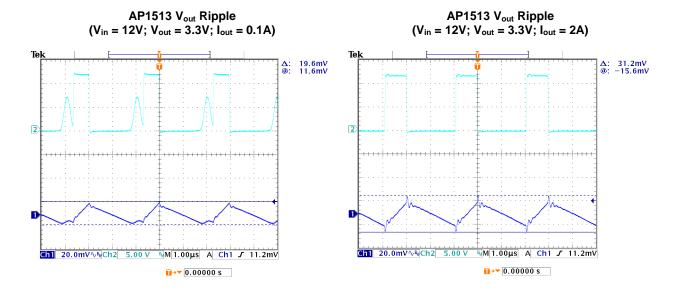




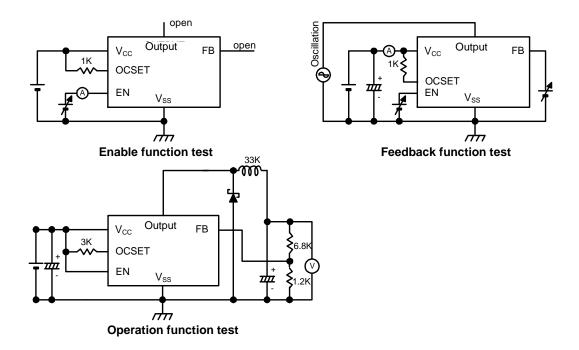




### **Performance Characteristics (Cont.)**



## **Test Circuit**





### **Functional Description**

#### **PWM Control**

The AP1513 is a DC/DC converter that employs pulse width modulation (PWM) scheme. Its pulse width varies in the range of 0% to 99%, based on the output current loading. The output ripple voltage caused by the PWM high frequency switching can easily be reduced through an output filter. Therefore, this converter provides a low ripple output supply over a broad range of input voltage & output current loading.

#### **Under Voltage Lockout**

The under voltage lockout circuit of the AP1513 assures that the high-side MOSFET driver remains in the off state whenever the supply voltage drops below 3.3V. Normal operation resumes once V<sub>CC</sub> rises above 3.5V.

#### **Current Limit Protection**

The current limit threshold is set by external resistor  $R_{OCSET}$  connected from  $V_{CC}$  supply to OCSET pin. The internal sink current  $I_{OCSET}$  (90 $\mu$ A typical) across this resistor sets the voltage at OCSET pin. When the PWM voltage is less than the voltage at OCSET, an over-current condition is triggered.

The current limit threshold is given by the following equation:

$$I_{PEAK} \times R_{DS(ON)} = I_{OCSET} \times R_{OCSET}$$

$$I_{\text{PEAK}} > I_{\text{OUT(MAX)}} + \frac{(\Delta I)}{2}$$

where,

$$\Delta I = \frac{V_{IN} - V_{OUT}}{fs \times L} \times \frac{V_{OUT}}{V_{IN}}$$

 $I_{PEAK}$  is the output peak current;  $R_{DS(ON)}$  is the MOSFET ON resistance;  $f_S$  is the PWM frequency (300kHz typical). Also, the inductor value will affect the ripple current  $\Delta I$ .

The above equation is recommended for input voltage range of 5V to 18V. For input voltage lower than 5V or ambient temperature over +100°C, higher R<sub>OCSET</sub> is recommended.

#### **Inductor Selection**

For most designs, the operation range with inductors is from 22µH to 33µH. The inductor value can be derived from the following equation:

$$L = \frac{V_{IN} - V_{OUT}}{fs \times \Delta I} \times \frac{V_{OUT}}{V_{IN}}$$

Where  $\Delta I_L$  is inductor Ripple Current. Large value inductors lower ripple current and small value inductors result in high ripple current. Choose inductor ripple current approximately 15% of the maximum load current 2A,  $\Delta I_L = 0.30$ A. The DC current rating of the inductor should be at least equal to the maximum load current plus half the ripple current to prevent core saturation (2A+0.15A).

### **Input Capacitor Selection**

This capacitor should be located close to the IC using short leads and the voltage rating should be approximately 1.5 times the maximum input voltage. The RMS current rating requirement for the input capacitor of a buck regulator is approximately 1/2 the DC load current. A low ESR input capacitor sized for maximum RMS current must be used. A 470µF low ESR capacitor for most applications is sufficient.

#### **Output Capacitor Selection**

The output capacitor is required to filter the output voltage and provides regulator loop stability. The important capacitor parameters are the 100kHz Equivalent Series Resistance (ESR), the RMS ripples current rating, voltage rating and capacitance value. For the output capacitor, the ESR value is the most important parameter. The output ripple can be calculated from the following formula.



### Functional Description (Cont.)

$$V_{RIPPLE} = \Delta I_L \times ESR$$

The bulk capacitor's ESR will determine the output ripple voltage and the initial voltage drop after a high slew-rate transient.

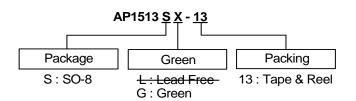
An aluminum electrolytic capacitor's ESR value is related to the capacitance and its voltage rating. In most case, higher voltage electrolytic capacitors have lower ESR values. Most of the time, capacitors with much higher voltage ratings may be needed to provide the low ESR values required for low output ripple voltage.

#### **PCB Layout Guide**

If you need low T<sub>C</sub> & T<sub>J</sub> or large P<sub>D</sub> (Power Dissipation), the dual Output pins (5 & 6) and V<sub>SS</sub> pins (7 & 8) on the SO-8 package are internally connected to die pad, The evaluation board should be allowed for maximum copper area at output pins.

- 1. Connect FB circuits as closely as possible and keep away from inductor flux for pure V<sub>FB</sub>.
- 2. Connect input capacitor to V<sub>CC</sub> and V<sub>SS</sub> pin as closely as possible to get good power filter effect.
- 3. Connect R<sub>OCSET</sub> to V<sub>CC</sub> and OCSET pin as closely as possible.
- 4. Connect ground side of the input capacitor & Schottky & output capacitor as closely as possible and use ground plane for best performance.

### Ordering Information



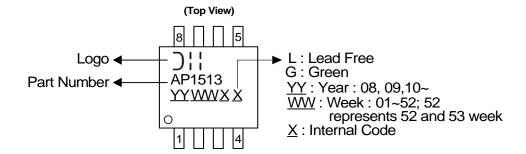
Davisa		Daalaasa				ımber Suffix	Status
Device (Note 5)	Package Code	Package (Note 6)	Green	Quantity	Tube	13"Tape and reel	Status (Note 5)
AP1513SG-13	S	SO-8	Green	2500	NA	-13	In production

Notes: 5. All Lead-Free variants are End of life without replacement.

6. For packaging details, go to our website at: https://www.diodes.com/design/support/packaging/diodes-packaging/diodes-package-outlines-and-pad-layouts/.

## Marking Information (Note 5)

(1) SO-8

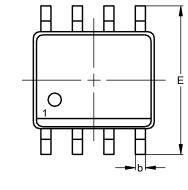


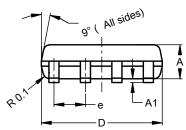


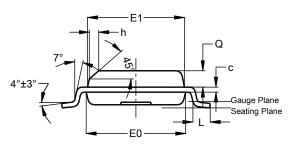
## **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

**SO-8** 





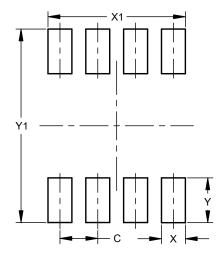


SO-8					
Dim	Min	Max	Тур		
Α	1.40	1.50	1.45		
<b>A</b> 1	0.10	0.20	0.15		
b	0.30	0.50	0.40		
C	0.15	0.25	0.20		
D	4.85	4.95	4.90		
Е	5.90	6.10	6.00		
E1	3.80	3.90	3.85		
E0	3.85	3.95	3.90		
е			1.27		
h	-		0.35		
٦	0.62	0.82	0.72		
D	0.60	0.70	0.65		
All Dimensions in mm					

## **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

SO-8



<b>Dimensions</b>	Value (in mm)
C	1.27
Х	0.802
X1	4.612
Y	1.505
Y1	6.50



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