

## SINGLE PHASE FULL WAVE DIRECT PWM MOTOR DRIVER

AM4961

## **Pin Configuration**

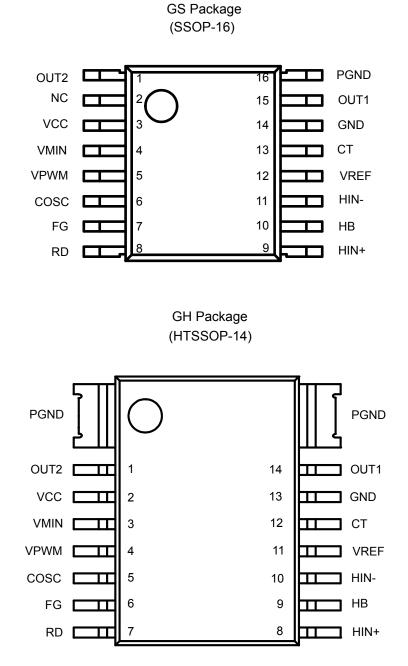


Figure 2. Pin Configuration of AM4961 (Top View)



## SINGLE PHASE FULL WAVE DIRECT PWM MOTOR DRIVER

# Data Sheet

## **Pin Description**

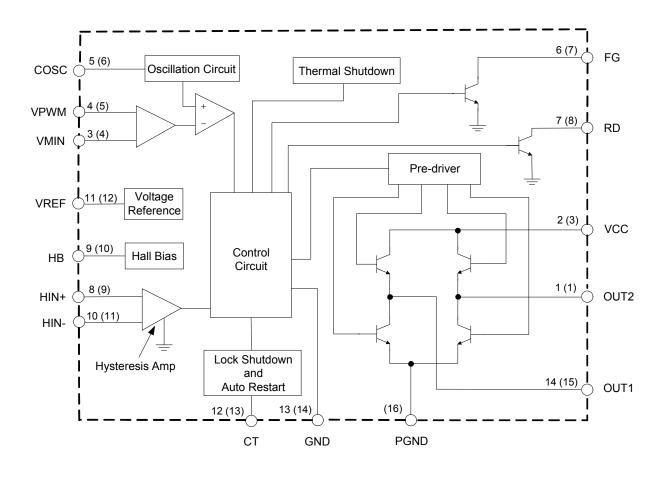
Pin Number		Pin Name	Function			
HTSSOP-14	SSOP-16		Function			
1	1	OUT2	Driver output 2			
	2	NC	No connection			
2	3	VCC	Power supply			
3	4	VMIN	Minimum duty setting			
4	5	VPWM	Adjustable Input			
5	6	COSC	Oscillator capacitor			
6	7	FG	Rotation speed indicator			
7	8	RD	Rotation/lock state indicator			
8	9	HIN+	Hall sensor input +			
9	10	HB	Hall sensor bias regulator			
10	11	HIN-	Hall sensor input -			
11	12	VREF	Reference voltage regulator			
12	13	СТ	Lock and rotation setting capacitor terminal			
13	14	GND	Ground for control circuit			
14	15	OUT1	Driver output 1			
	16	PGND	Power ground			



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## **Functional Block Diagram**



A (B) A for 14-pin B for 16-pin

Figure 3. Functional Block Diagram of AM4961

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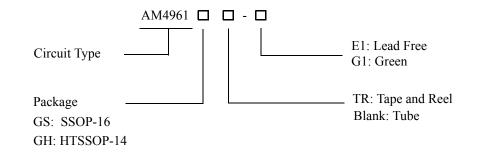
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#### Truth Table

HIN-	HIN+	COSC (Note 1)	СТ	OUT1	OUT2	FG	RD	Mode		
Н	L			Н	L	L		Rotation (Drive)		
L	Н	Н	L	L	Н	OFF	L			
Н	L	Ŧ		OFF	L	L		Rotation (Recirculate)		
L	Н	L		L	OFF	OFF				
Н	L	H L		Н	OFF	L				
L	Н			OFF	Н	OFF				
Н	L		Н	OFF	OFF	L	OFF	Lock Protection		
L	Н		L	L	L		OFF	OFF	OFF	

Note 1:  $V_{OSC}$  (H)  $\geq V_{PWM}$ ,  $V_{OSC}$  (L)  $\leq V_{PWM}$ .

## **Ordering Information**



Package	Temperature Range	Part N	umber	Mar	Packing	
		Lead Free	Green	Lead Free	Green	Туре
SSOP-16	-30 to 90°C	AM4961GS-E1	AM4961GS-G1	AM4961GS	AM4961GS-G1	Tube
		AM4961GSTR-E1	AM4961GSTR-G1	AM4961GS	AM4961GS-G1	Tape & Reel
HTSSOP-14		AM4961GH-E1	AM4961GH-G1	AM4961GH	AM4961GH-G1	Tube
		AM4961GHTR-E1	AM4961GHTR-G1	AM4961GH	AM4961GH-G1	Tape & Reel

BCD Semiconductor's Pb-free products, as designated with "E1" suffix in the part number, are RoHS compliant. Products with "G1" suffix are available in green packages.

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#### Absolute Maximum Ratings (Note 2)

Parameter	Symbol	Value		Unit	
Supply Voltage	V <sub>CC</sub>	18		V	
Output Current	I <sub>OUT</sub>	1.0		А	
Output Voltage	V <sub>OUT</sub>	18		V	
HB Output Current	I <sub>HB</sub>	10		mA	
VPWM Input Voltage	V <sub>PWM</sub>	6		V	
RD Output Voltage	V <sub>RD</sub>	18		V	
FG Output Voltage	V <sub>FG</sub>	18		V	
RD Output Current	I <sub>RD</sub>	10		mA	
FG Output Current	I <sub>FG</sub>	10		mA	
	_	SSOP-16	0.8	W	
Power Dissipation (Note 3)	P <sub>D</sub>	HTSSOP-14	1.1	W	
Storage Temperature Range	T <sub>STG</sub>	-55 to 150		°C	
ESD (Human Body Model)	ESD	2000		V	
ESD (Machine Model)	ESD	250		V	

Note 2: Stresses greater than those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "Recommended Operating Conditions" is not implied. Exposure to "Absolute Maximum Ratings" for extended periods may affect device reliability.

Note 3: T<sub>A</sub>=25°C, no external heatsink.

#### **Recommended Operating Conditions**

Parameter	Symbol	Min	Тур	Max	Unit
Supply Voltage	V <sub>CC</sub>	3.5	12	16	V
Hall Input Voltage + (Note 4)	V <sub>IN+</sub>	0.2		3	V
Hall Input Voltage - (Note 4)	V <sub>IN-</sub>	0.2		3	V
Ambient Temperature	T <sub>A</sub>	-30		90	°C

Note 4: Hall input voltage range includes the amplitude of signal.



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## **Electrical Characteristics**

(V<sub>CC</sub>=12V,  $T_A$ =25°C, unless otherwise specified.)

Parameter	Symbol	Conditions	Min	Тур	Max	Unit	
Quiescent Current	I <sub>Q1</sub>	Lock Off	10.2	15	18.76	mA	
Quiescent Current	I <sub>Q2</sub>	Lock On	5.38	8	10.55		
VREF Voltage	V <sub>REF</sub>	I <sub>REF</sub> =5mA	5.8	6	6.2	V	
Output Saturation Voltage at High Side	V <sub>SATH</sub>	I <sub>SOURCE</sub> =200mA		1.0	1.17	V	
Output Saturation Voltage at Low Side	V <sub>SATL</sub>	I <sub>SINK</sub> =200mA		0.2	0.3	V	
COSC Frequency	$\mathbf{f}_{OSC}$	C <sub>OSC</sub> =100pF	18	25	32	kHz	
COSC High Level Voltage	V <sub>OSCH</sub>		3.45	3.6	3.75	V	
COSC Low Level Voltage	V <sub>OSCL</sub>		1.83	1.95	2.07	V	
Hall Input Hysteresis	$V_{\rm HYS}$			±10	±20	mV	
Hall Bias Voltage	$V_{HB}$	I <sub>HB</sub> =5mA	1.1	1.25	1.4	V	
CT High Level Voltage	V <sub>CTH</sub>		3.55	3.7	3.88	V	
CT Low Level Voltage	V <sub>CTL</sub>		1.55	1.7	1.85	V	
CT Charge Current	I <sub>CHG</sub>		1.5	2	2.85	μΑ	
CT Discharge Current	I <sub>DHG</sub>		0.14	0.2	0.285	μΑ	
CT Charge and Discharge Ratio	R <sub>CD</sub>	I <sub>CHG</sub> /I <sub>DHG</sub>	8.5	10	14.5		
FG Output Low Level Voltage	V <sub>FGL</sub>	I <sub>FG</sub> =5mA		0.2	0.3	V	
FG Leakage Current	I <sub>LFG</sub>	V <sub>FG</sub> =12V			30	μΑ	
RD Output Low Level Voltage	V <sub>RDL</sub>	I <sub>RD</sub> =5mA		0.2	0.3	V	
RD Leakage Current	I <sub>LRD</sub>	V <sub>RD</sub> =12V			30	μΑ	



## SINGLE PHASE FULL WAVE DIRECT PWM MOTOR DRIVER

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## **Typical Performance Characteristics**

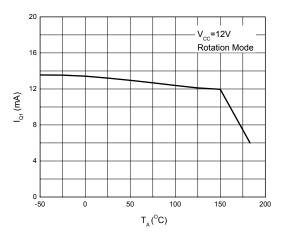


Figure 4.Quiescent Current vs. Ambient Temperature

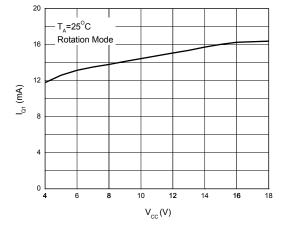


Figure 5. Quiescent Current vs. Supply Voltage

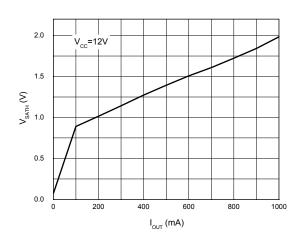


Figure 6. Output Saturation Voltage (High) vs. Output Current

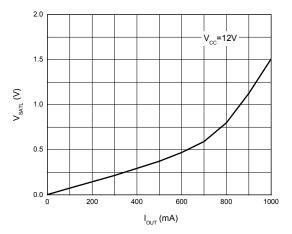


Figure 7. Output Saturation Voltage (Low) vs. Output Current

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**Typical Performance Characteristics (Continued)** 

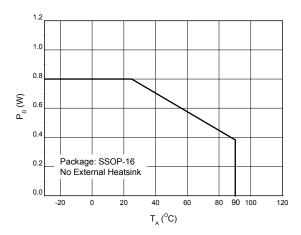


Figure 8. Power Dissipation vs. Ambient Temperature

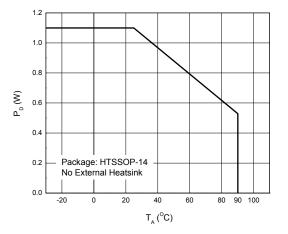


Figure 9. Power Dissipation vs. Ambient Temperature



#### SINGLE PHASE FULL WAVE DIRECT PWM MOTOR DRIVER

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#### **Operating Diagram**

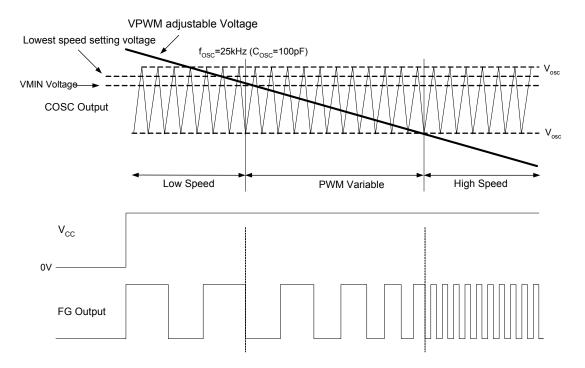


Figure 10. Operating Diagram of AM4961 (Note 5)

#### Note 5:

#### 1. Low Speed Setting Mode

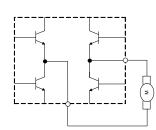
When VPWM voltage is higher than VMIN pin voltage, motor speed is settable by VMIN pin voltage. The minimum drive duty cycle is settable by comparing COSC oscillating voltage and VMIN pin voltage.

VPWM voltage is decided by variation of PWM duty.

#### 2. Variable Speed Setting Mode

When VPWM voltage is lower than VMIN pin voltage, PWM control system works by comparing VPWM voltage and COSC voltage. If VPWM voltage is higher, the ON duty cycle of the upper side transistors will be minimized and motor speed becomes lower. Vice versa.

3. Full Speed Rotation Mode



At a certain PWN duty, when VPWM voltage is lower than the low side of COSC output voltage, the motor will run at full speed.

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#### **Typical Application**

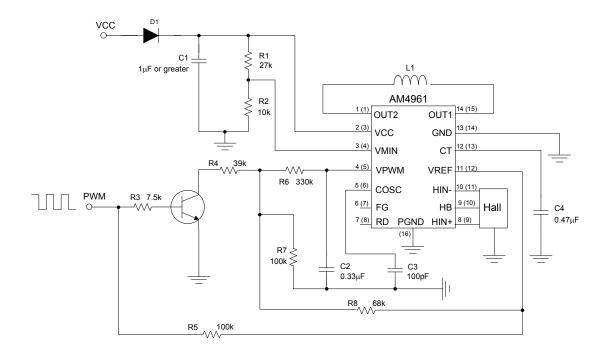


Figure 11. Typical Application of AM4961 (Note 6)

Note 6:

\*1. Ground Line Layout

PGND is connected to motor supply stage and GND is connected to control stage. All ground lines from control stage are connected to GND.

\*2. Stability of Power Supply

C1 is employed to stabilize  $V_{CC}$ . Its capacitance is no less than  $1\mu F$ .

\*3. Hall Input

To avoid noise, the shortest line is recommended to connect with Hall stage which has about 20mV hysteresis. Thus, the ideal Hall input is 50mV or over.

\*4. COSC Capacitor

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When  $C_{CP}$  is 100pF, the COSC frequency will be 25kHz.

\*5. FG Output

FG output terminal is open collector output which varies with phase change.

\*6. RD Output

RD output terminal is open collector output. It is low at rotation mode and high when stopped.

\*7. HB Pin

This pin is available to output a 1.25V Hall bias voltage.

\*8. VMIN Pin

If this pin is disused, connect it directly with VPWM, the minimum duty cycle will be 10%.



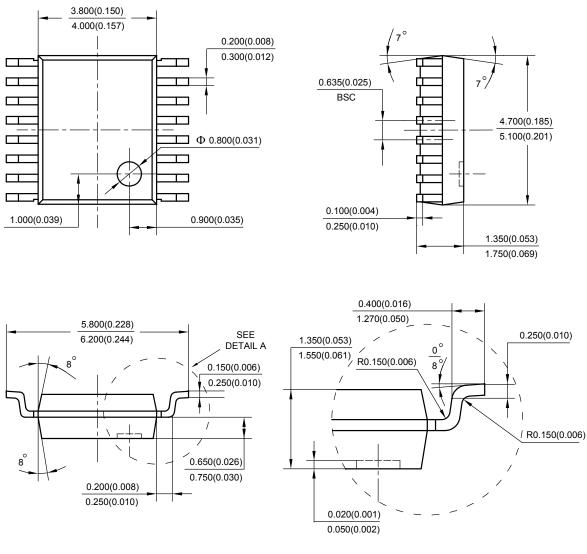
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#### **Mechanical Dimensions**

SSOP-16

Unit: mm(inch)



Note: Eject hole, oriented hole and mold mark is optional.

DETAIL A

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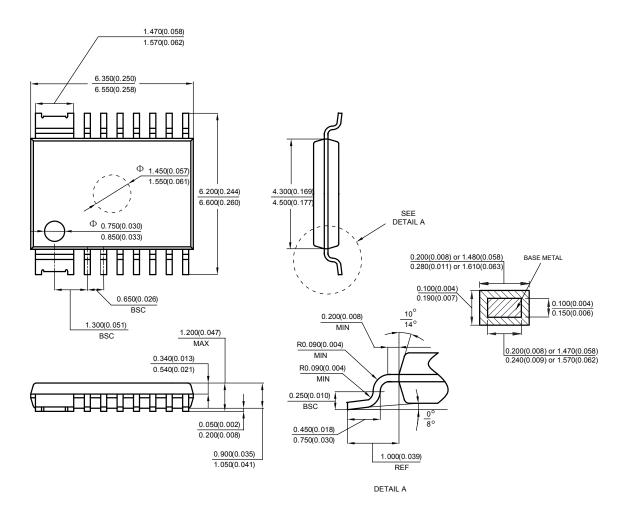
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SINGLE PHASE FULL WAVE DIRECT PWM MOTOR DRIVER

Mechanical Dimensions (Continued)

HTSSOP-14

Unit: mm(inch)



Note: Eject hole, oriented hole and mold mark is optional.

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