## VS-31DQ09, VS-31DQ09-M3, VS-31DQ10, VS-31DQ10-M3

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### **Vishay Semiconductors**

ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CO	VALUES	UNITS		
Maximum forward voltage drop See fig. 1	V <sub>FM</sub> <sup>(1)</sup>	3 A	T <sub>1</sub> = 25 °C	0.85	V	
		6 A	1j=23 0	0.97		
		3 A	T, = 125 °C	0.69		
		6 A	1j = 125 C	0.80		
Maximum reverse leakage current See fig. 4	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	$V_{\rm B}$ = Rated $V_{\rm B}$	1	mA	
		T <sub>J</sub> = 125 °C	$v_{\rm R} = naleu v_{\rm R}$	3		
Typical junction capacitance	C <sub>T</sub>	$V_R$ = 5 $V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		110	pF	
Typical series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body		9.0	nH	
Maximum voltage rate of charge	dV/dt	Rated V <sub>R</sub> 10 000			V/µs	

#### Note

<sup>(1)</sup> Pulse width < 300  $\mu$ s, duty cycle < 2 %

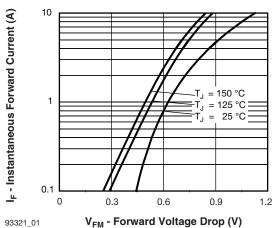
THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range	T <sub>J</sub> <sup>(1)</sup> , T <sub>Stg</sub>		- 40 to 150	°C	
Maximum thermal resistance, junction to ambient	R <sub>thJA</sub>	DC operation Without cooling fin	80	°C/W	
Typical thermal resistance, junction to lead	R <sub>thJL</sub>	DC operation	15		
Approximate weight			1.2	g	
			0.042	oz.	
		Constitute C 16		31DQ09	
Marking device		Case style C-16	31DQ10		

#### Note

(1)  $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$  thermal runaway condition for a diode on its own heatsink

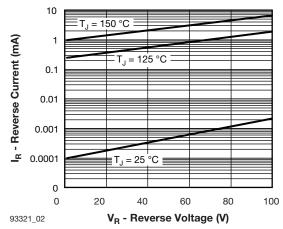
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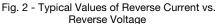
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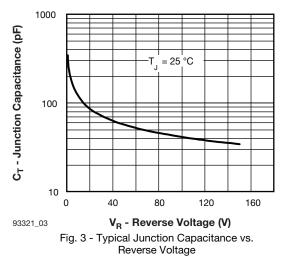


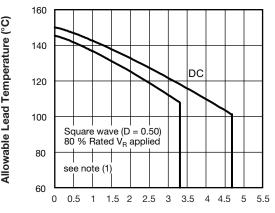
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Fig. 1 - Maximum Forward Voltage Drop Characteristics









93321\_04 **I<sub>F(AV)</sub> - Average Forward Current (A)** Fig. 4 - Maximum Allowable Lead Temperature vs. Average Forward Current

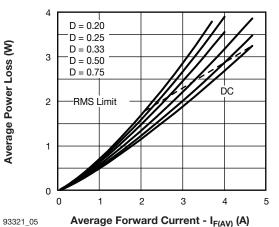


Fig. 5 - Forward Power Loss Characteristics

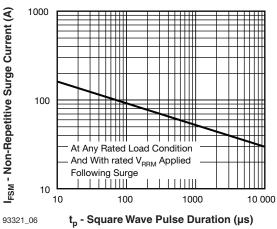


Fig. 6 - Maximum Non-Repetitive Surge Current

#### Note

<sup>(1)</sup> Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJL}$ ;

Pd = Forward power loss =  $I_{F(AV)} \times V_{FM}$  at ( $I_{F(AV)}/D$ ) (see fig. 6); Pd<sub>REV</sub> = Inverse power loss =  $V_{R1} \times I_R$  (1 - D);  $I_R$  at  $V_{R1}$  = 80 % rated  $V_R$ 

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**Vishay Semiconductors** 

### **ORDERING INFORMATION TABLE**

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VISHA

Device code	VS-	31	D	Q	10	TR	-M3	
		2	3	4	5	6	7	1
	1       -         2       -         3       -         4       -         5       -         6       -         7       -	31 = D = 1 Q = 10 = • TR • No Envi	Curren DO-201 Schottky Voltage = Tape ne = Bu ronmen	•	, 3.3 A e ries el packa	ge	complia	_ 09 = 90 ∨ 10 = 100 ∨
			<ul> <li>None = Lead (Pb)-free and RoHS compliant</li> <li>-M3 = Halogen-free, RoHS compliant, and terminations lead (Pb)-free</li> </ul>					

ORDERING INFORMATION (Example)					
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION		
VS-31DQ09	500	500	Bulk		
VS-31DQ09TR	1200	1200	Tape and reel		
VS-31DQ09-M3	500	500	Bulk		
VS-31DQ09TR-M3	1200	1200	Tape and reel		
VS-31DQ10	500	500	Bulk		
VS-31DQ10TR	1200	1200	Tape and reel		
VS-31DQ10-M3	500	500	Bulk		
VS-31DQ10TR-M3	1200	1200	Tape and reel		

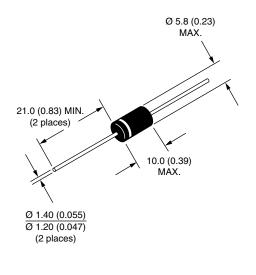
LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95242			
Part marking information	www.vishay.com/doc?95304			
Packaging information	www.vishay.com/doc?95338			
SPICE model	www.vishay.com/doc?95300			

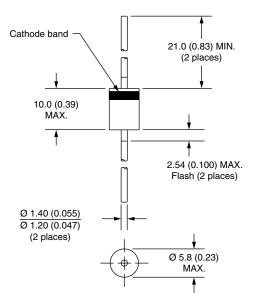




Axial DO-201AD (C-16)

### **DIMENSIONS** in millimeters (inches)





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