

# VS-25TTS08-M3, VS-25TTS12-M3

Vishay Semiconductors

ABSOLUTE MAXIMUM RATINGS	5						
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES		UNITS	
PARAMETER			TYP.	MAX.	UNITS		
Maximum average on-state current	I <sub>T(AV)</sub>	$T_{\rm C} = 93 ^{\circ}{\rm C}$ , 180° conduc	tion half sine wave	16			
Maximum RMS on-state current	I <sub>RMS</sub>			2	25	А	
Maximum peak, one-cycle,	1	10 ms sine pulse, rated	/ <sub>RRM</sub> applied	2	70	^	
non-repetitive surge current	I <sub>TSM</sub>	10 ms sine pulse, no volt	age reapplied	3	20	1	
Maximum I <sup>2</sup> t for fusing	l <sup>2</sup> t	10 ms sine pulse, rated	/ <sub>RRM</sub> applied	365		A <sup>2</sup> s	
Maximum I-t for fusing	I <sup>2</sup> t	10 ms sine pulse, no volt	5	15 A-S	A-S		
Maximum I <sup>2</sup> $\sqrt{t}$ for fusing	l²√t	t = 0.1 to 10 ms, no voltage reapplied			52	A²√s	
Maximum on-state voltage drop	V <sub>TM</sub>	16 A, T <sub>J</sub> = 25 °C		1.25		V	
On-state slope resistance	r <sub>t</sub>			12.0		2.0	mΩ
Threshold voltage	V <sub>T(TO)</sub>	T <sub>J</sub> = 125 °C		1.0		V	
	1 /1			0	.5		
Maximum reverse and direct leakage current	I <sub>RM</sub> /I <sub>DM</sub>	T <sub>J</sub> = 125 °C	$V_{R} = Rated V_{RRM}/V_{DRM}$	1	0		
Holding current	Ι <sub>Η</sub>	Anode supply = 6 V, resistive load, initial $I_T$ = 1 A, $T_J$ = 25 °C		-	150	mA	
Maximum latching current	١L	Anode supply = 6 V, resistive load, $T_J$ = 25 °C			00		
Maximum rate of rise of off-state voltage	dV/dt	$T_J = T_J max.$ , linear to 80 °C, $V_{DRM} = R_g - k = Open$			00	V/µs	
Maximum rate of rise of turned-on current	dl/dt			1:	50	A/µs	

TRIGGERING							
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Maximum peak gate power	P <sub>GM</sub>		8.0				
Maximum average gate power	P <sub>G(AV)</sub>		2.0	W			
Maximum peak positive gate current	+ I <sub>GM</sub>		1.5	А			
Maximum peak negative gate voltage	- V <sub>GM</sub>		10	V			
	er l <sub>GT</sub>	Anode supply = 6 V, resistive load, $T_J = -10 \text{ °C}$	60				
Maximum required DC gate current to trigger		Anode supply = 6 V, resistive load, $T_J = 25 \text{ °C}$	45	mA			
		Anode supply = 6 V, resistive load, $T_J = 125 \text{ °C}$	20				
		Anode supply = 6 V, resistive load, $T_J = -10 \degree C$	2.5				
Maximum required DC gate voltage to trigger	V <sub>GT</sub>	Anode supply = 6 V, resistive load, $T_J = 25 \ ^{\circ}C$	2.0	v			
		Anode supply = 6 V, resistive load, $T_J = 125 \text{ °C}$	1.0	V			
Maximum DC gate voltage not to trigger	$V_{GD}$	T = 125 °C V Botod volue	0.25				
Maximum DC gate current not to trigger	I <sub>GD</sub>	T <sub>J</sub> = 125 °C, V <sub>DRM</sub> = Rated value	2.0	mA			

SWITCHING								
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS				
Typical turn-on time	t <sub>gt</sub>	T <sub>J</sub> = 25 °C	0.9					
Typical reverse recovery time	t <sub>rr</sub>	T. = 125 °C	4	μs				
Typical turn-off time	t <sub>q</sub>	ij = 125 C	110					

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THERMAL AND MECHANICAL SPECIFICATIONS								
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Maximum junction and storage temperature range		T <sub>J</sub> , T <sub>Stg</sub>		-40 to +125	°C			
Maximum thermal resistance, junction to case		R <sub>thJC</sub>	DC operation	1.1				
Maximum thermal resistance, junction to ambient		R <sub>thJA</sub>		62	°C/W			
Typical thermal resistance, case to heatsink		R <sub>thCS</sub>	Mounting surface, smooth and greased	0.5				
Approximate weight				2	g			
Approximate weight				0.07	oz.			
Mounting torque –	minimum			6 (5)	kgf ⋅ cm			
	maximum			12 (10)	(lbf ⋅ in)			
Marking daviaa			Case style 21 TO 220AP	25TTS08				
Marking device			Case style 3L TO-220AB	25TTS12				

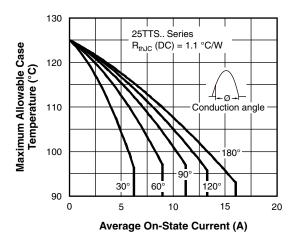


Fig. 1 - Current Rating Characteristics

R<sub>thJC</sub> (DC) = 1.1 °C/W

80

20

-ø-i

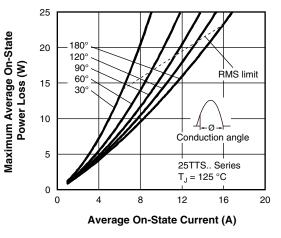
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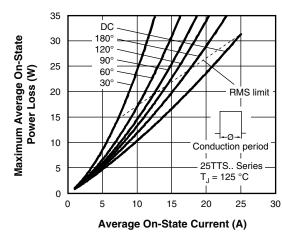
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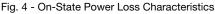
Conduction period

25TTS. Series









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130

120

110

100

90

80

0

30

5

60

10

90°

15

Average On-State Current (A)

Fig. 2 - Current Rating Characteristics

Maximum Allowable Case Temperature (°C)

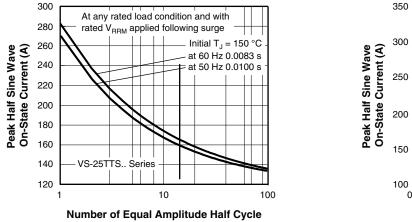
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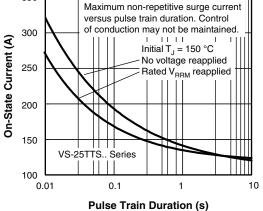
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Current Pulses (N)

Fig. 5 - Maximum Non-Repetitive Surge Current





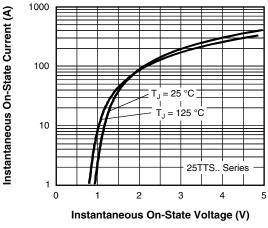


Fig. 7 - On-State Voltage Drop Characteristics

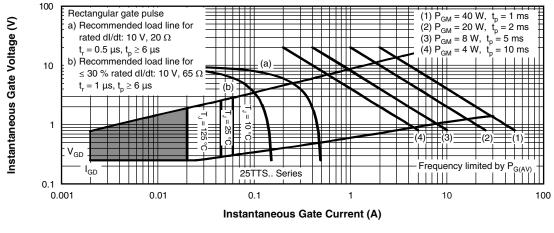


Fig. 8 - Gate Characteristics



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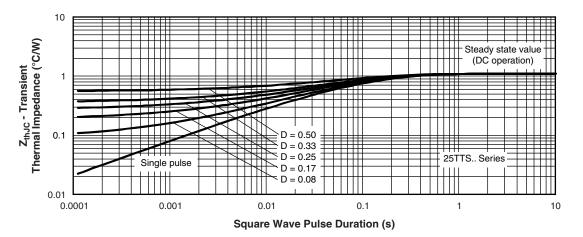


Fig. 9 - Thermal Impedance Z<sub>thJC</sub> Characteristics

### **ORDERING INFORMATION TABLE**

www.vishay.com

Device code	VS-	25	т	т	s	12	-M3	
		(2)	(3)	4	(5)	(6)	$\overline{7}$	1
		C	C	Ċ	C	C	Ċ	
	1	- Visl	nay Sen	niconduc	ctors pro	duct		
	2	- Cur	rent rati	ng (25 =	25 A)			
	3	- Circ	uit conf	iguratior	n:			
		Т =	single tl	nyristor				
	4	- Pac	kage:					
		T =	TO-220	AB				
	5	- Тур	e of silio	con:				
				d recove	ery rectit	ier		
	6	- Volt	age rati	ng				8 = 800 V 2 = 1200 V
	7	- Env	ronmen	tal digit:			12	1200 V
		-M3	= halog	en-free,	RoHS-o	complia	nt, and f	terminations

ORDERING INFORMATION (Example)							
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION				
VS-25TTS08-M3	50	1000	Antistatic plastic tubes				
VS-25TTS12-M3	50	1000	Antistatic plastic tubes				

LINKS TO RELATED DOCUMENTS						
Dimensions	www.vishay.com/doc?96154					
Part marking information	www.vishay.com/doc?95028					

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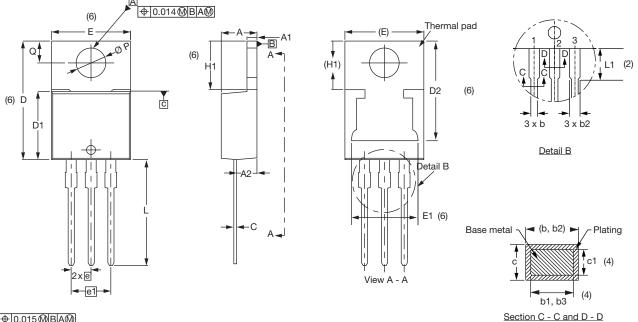
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## **3L TO-220AB**

#### **DIMENSIONS** in millimeters and inches



⊕0.015@BA@





SYMBOL	MILLIM	IETERS	INC	HES	NOTES
STNIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.25	4.65	0.167	0.183	
A1	1.14	1.40	0.045	0.055	
A2	2.50	2.92	0.098	0.115	
b	0.69	1.01	0.027	0.040	
b1	0.38	0.97	0.015	0.038	4
b2	1.20	1.73	0.047	0.068	
b3	1.14	1.73	0.045	0.068	4
С	0.36	0.61	0.014	0.024	
c1	0.36	0.56	0.014	0.022	4
D	14.85	15.35	0.585	0.604	3
D1	8.38	9.02	0.330	0.355	

Conforms to JEDEC <sup>®</sup>	outline	<b>TO-220AB</b>

SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STWDUL	MIN.	MAX.	MIN.	MAX.	NOTES
D2	11.68	13.30	0.460	0.524	6, 7
Ш	10.11	10.51	0.398	0.414	3, 6
E1	6.86	8.89	0.270	0.350	6
е	2.41	2.67	0.095	0.105	
e1	4.88	5.28	0.192	0.208	
H1	6.09	6.48	0.240	0.255	6
L	13.52	14.02	0.532	0.552	
L1	3.32	3.82	0.131	0.150	2
ØР	3.54	3.91	0.139	0.154	
Q	2.60	3.00	0.102	0.118	

Notes

 $^{(1)}\,$  Dimensioning and tolerancing as per ASME Y14.5M-1994

<sup>(2)</sup> Lead dimension and finish uncontrolled in L1

(3) Dimension D, D1, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body

<sup>(4)</sup> Dimension b1, b3, and c1 apply to base metal only

(5) Controlling dimensions: inches

<sup>(6)</sup> Thermal pad contour optional within dimensions E, H1, D2, and E1

<sup>(7)</sup> Outline conforms to JEDEC<sup>®</sup> TO-220, except D2

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