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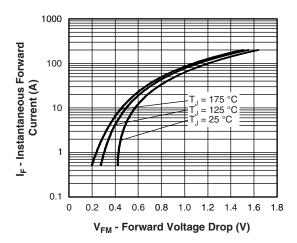
#### **Vishay Semiconductors**

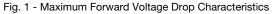
| ELECTRICAL SPECIFICATIONS                  |                                |                                   |                                       |       |    |  |  |
|--|--------------------------------|-----------------------------------|---------------------------------------|-------|----|--|--|
| PARAMETER                                  | SYMBOL                         | TEST CO                           | VALUES                                | UNITS |    |  |  |
|  | V <sub>FM</sub> <sup>(1)</sup> | 10 A                              | T.I = 25 °C                           | 0.57  |    |  |  |
| Maximum forward voltage drop<br>See fig. 1 |                                | 20 A                              | $1_{\rm J} = 25$ C                    | 0.67  | v  |  |  |
|  |                                | 10 A                              | T.I = 125 °C                          | 0.49  |    |  |  |
|  |                                | 20 A                              | 1j=125 0                              | 0.61  |    |  |  |
| Maximum reverse leakage current            | I <sub>RM</sub> <sup>(1)</sup> | T <sub>J</sub> = 25 °C            | $V_{\rm B}$ = Rated $V_{\rm B}$       | 2     | mA |  |  |
| See fig. 2                                 |                                | T <sub>J</sub> = 125 °C           | V <sub>R</sub> = naleu V <sub>R</sub> | 15    | ША |  |  |
| Maximum junction capacitance               | CT                             | $V_R = 5 V_{DC}$ (test signal ran | 900                                   | pF    |    |  |  |
| Typical series inductance                  | L <sub>S</sub>                 | Measured lead to lead 5 r         | 8.0                                   | nH    |    |  |  |
| Maximum voltage rate of change             | dV/dt                          | Rated V <sub>R</sub>              | 10 000                                | V/µs  |    |  |  |

Note

 $^{(1)}\,$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

| THERMAL - MECHANICAL SPECIFICATIONS            |             |                                   |  |                      |            |  |  |  |
|--|-------------|-----------------------------------|--|----------------------|------------|--|--|--|
| PARAMETER                                      |             | SYMBOL                            | TEST CONDITIONS                          | VALUES               | UNITS      |  |  |  |
| Maximum junction and storage temperature range |             | T <sub>J</sub> , T <sub>Stg</sub> |  | -55 to 175           | °C         |  |  |  |
| Maximum thermal resistance, junction to case   |             | R <sub>thJC</sub>                 | DC operation<br>See fig. 4               | 2.0                  |            |  |  |  |
| Typical thermal resistance, case to heatsink   |             | R <sub>thCS</sub>                 | Mounting surface, smooth and greased     | 0.50                 | - °C/W     |  |  |  |
| Approvimente vuoight                           | · · · · · · |                                   |  | 2                    | g          |  |  |  |
| Approximate weight                             |             |                                   |  | 0.07                 | oz.        |  |  |  |
| Mounting torque                                | minimum     |                                   |  | 6 (5)                | kgf · cm   |  |  |  |
| Mounting torque                                | maximum     |                                   |  | 12 (10)              | (lbf ⋅ in) |  |  |  |
| Marking device                                 |             |                                   | Case style D <sup>2</sup> PAK (TO-263AB) | 10TQ035S<br>10TQ045S |            |  |  |  |





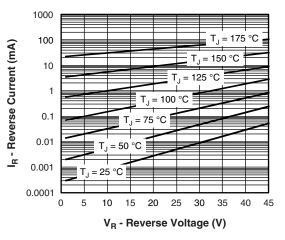


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

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|-----------|-----------|
|           |           |

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## VS-10TQ035S-M3, VS-10TQ045S-M3

**Vishay Semiconductors** 

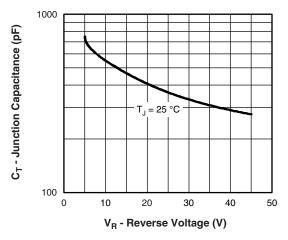


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

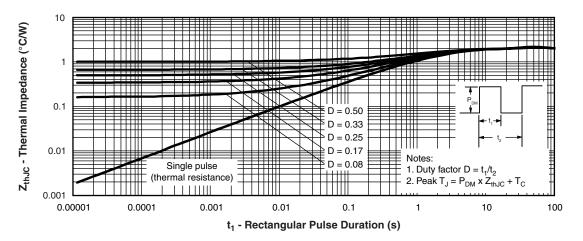
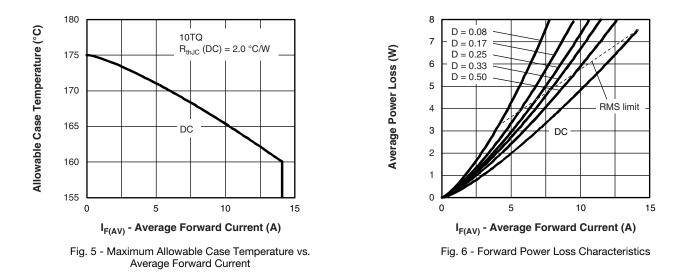


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics



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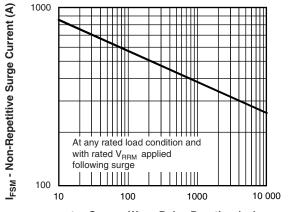
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## VS-10TQ035S-M3, VS-10TQ045S-M3

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 $t_{p}$  - Square Wave Pulse Duration (µs)



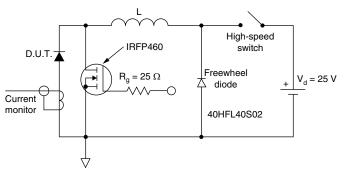
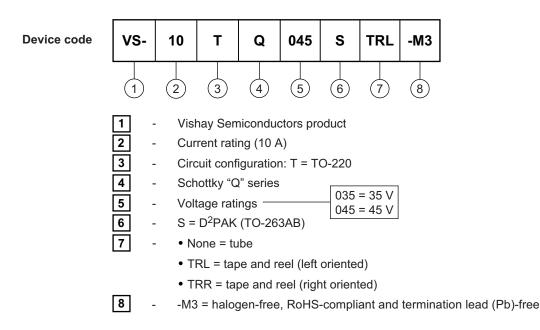


Fig. 8 - Unclamped Inductive Test Circuit

#### **ORDERING INFORMATION TABLE**



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# VS-10TQ035S-M3, VS-10TQ045S-M3

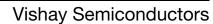
## Vishay Semiconductors

| ORDERING INFORMATION |                  |                        |                          |  |  |  |  |  |
|----------------------|------------------|------------------------|--------------------------|--|--|--|--|--|
| PREFERRED P/N        | QUANTITY PER T/R | MINIMUM ORDER QUANTITY | PACKAGING DESCRIPTION    |  |  |  |  |  |
| VS-10TQ035S-M3       | 50               | 1000                   | Antistatic plastic tubes |  |  |  |  |  |
| VS-10TQ035STRR-M3    | 800              | 800                    | 13" diameter reel        |  |  |  |  |  |
| VS-10TQ035STRL-M3    | 800              | 800                    | 13" diameter reel        |  |  |  |  |  |
| VS-10TQ045S-M3       | 50               | 1000                   | Antistatic plastic tubes |  |  |  |  |  |
| VS-10TQ045STRR-M3    | 800              | 800                    | 13" diameter reel        |  |  |  |  |  |
| VS-10TQ045STRL-M3    | 800              | 800                    | 13" diameter reel        |  |  |  |  |  |

| LINKS TO RELATED DOCUMENTS |                          |  |  |  |  |
|----------------------------|--------------------------|--|--|--|--|
| Dimensions                 | www.vishay.com/doc?96164 |  |  |  |  |
| Part marking information   | www.vishay.com/doc?95444 |  |  |  |  |
| Packaging information      | www.vishay.com/doc?96424 |  |  |  |  |

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# **Outline Dimensions**

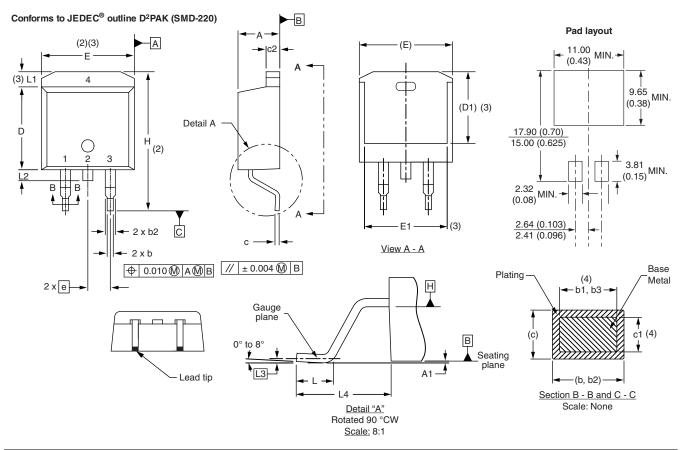


D<sup>2</sup>PAK

#### **DIMENSIONS** in millimeters and inches

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ISHA



| SYMBOL | MILLIM | ILLIMETERS INCHES |       | NOTES SYMBO | SYMBOL | MILLIMETERS |        | INCHES             |       | NOTES |       |       |
|--------|--------|-------------------|-------|-------------|--------|-------------|--------|--------------------|-------|-------|-------|-------|
| STMBOL | MIN.   | MAX.              | MIN.  | MAX.        | NOTES  | NOTES       | STMBOL | MIN.               | MAX.  | MIN.  | MAX.  | NOTES |
| А      | 4.06   | 4.83              | 0.160 | 0.190       |        |             | D1     | 6.86               | 8.00  | 0.270 | 0.315 | 3     |
| A1     | 0.00   | 0.254             | 0.000 | 0.010       |        |             | E      | 9.65               | 10.67 | 0.380 | 0.420 | 2, 3  |
| b      | 0.51   | 0.99              | 0.020 | 0.039       |        |             | E1     | 7.90               | 8.80  | 0.311 | 0.346 | 3     |
| b1     | 0.51   | 0.89              | 0.020 | 0.035       | 4      |             | е      | 2.54 BSC 0.100 BSC |       | ) BSC |       |       |
| b2     | 1.14   | 1.78              | 0.045 | 0.070       |        |             | Н      | 14.61              | 15.88 | 0.575 | 0.625 |       |
| b3     | 1.14   | 1.73              | 0.045 | 0.068       | 4      |             | L      | 1.78               | 2.79  | 0.070 | 0.110 |       |
| С      | 0.38   | 0.74              | 0.015 | 0.029       |        |             | L1     | -                  | 1.65  | -     | 0.066 | 3     |
| c1     | 0.38   | 0.58              | 0.015 | 0.023       | 4      |             | L2     | 1.27               | 1.78  | 0.050 | 0.070 |       |
| c2     | 1.14   | 1.65              | 0.045 | 0.065       |        |             | L3     | 0.25 BSC 0.010 BSC |       |       |       |       |
| D      | 8.51   | 9.65              | 0.335 | 0.380       | 2      |             | L4     | 4.78               | 5.28  | 0.188 | 0.208 |       |

#### Notes

<sup>(1)</sup> Dimensioning and tolerancing per ASME Y14.5 M-1994

<sup>(2)</sup> Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

<sup>(3)</sup> Thermal pad contour optional within dimension E, L1, D1 and E1

(4) Dimension b1 and c1 apply to base metal only

<sup>(5)</sup> Datum A and B to be determined at datum plane H

<sup>(6)</sup> Controlling dimension: inch

<sup>(7)</sup> Outline conforms to JEDEC<sup>®</sup> outline TO-263AB

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