TISP4xxxJ3BJ Overvoltage Protector Series

Absolute Maximum Ratings, TA = 25 °C (Unless Otherwise Noted)

Rating	Symbol	Value	Unit	
*4070J3BJ *4080J3BJ *4095J3BJ *4115J3BJ *4115J3BJ *4145J3BJ *4145J3BJ *4165J3BJ *4180J3BJ *4200J3BJ *4219J3BJ *4250J3BJ *4250J3BJ *4350J3BJ *4395J3BJ	V_{DRM}	±58 ±65 ±75 ±90 ±100 ±120 ±135 ±145 ±155 ±180 ±190 ±220 ±275 ±320	V	
Non-repetitive peak impulse current (see Notes 1 and 2) 2/10 μs (GR-1089-CORE, 2/10 μs voltage wave shape) 8/20 μs (IEC 61000-4-5, combination wave generator, 1.2/50 μsvoltage wave shape) 10/160 μs (TIA-968-A, 10/160 μs voltage wave shape) 4/250 μs (ITU-T K.20/21, 10/700 μs voltage waveshape, simultaneous) 5/310 μs (ITU-T K.20/21, 10/700 μs voltage wave shape, single) 5/320 μs (TIA-968-A, 9/720 μs voltage waveshape, single) 10/560 μs (TIA-968-A, 10/560 μs voltage wave shape) 10/1000 μs (GR-1089-CORE, 10/1000 μs voltage wave shape)	I _{PPSM}	±1000 ±800 ±400 ±370 ±350 ±350 ±250 ±200	А	
Non-repetitive peak on-state current (see Notes 1 and 2) 20 ms, 50 Hz (full sine wave)	I _{TSM}	50	А	
Initial rate of rise of on-state current. Linear current ramp. Maximum ramp value < 50 A	di _T /dt	800	A/µs	
Junction temperature	TJ	-40 to +150	°C	
Storage temperature range	T _{stg}	-65 to +150	°C	

- NOTES: 1. Initially the device must be in thermal equilibrium with T_J = 25 °C.
 - 2. These non-repetitive rated currents are peak values of either polarity. The surge may be repeated after the device returns to its initial conditions.

Electrical Characteristics, T_A = 25 °C (Unless Otherwise Noted)

Parameter Test Conditions				Min	Тур	Max	Unit
I _{DRM}	Repetitive peak off-state current	$V_D = V_{DRM}$	T _A = 25 °C T _A = 85 °C			±5 ±10	μΑ
V _(BO)	AC Breakover voltage	dv/dt = ± 250 V/ms, R _{SOURCE} = 300 Ω	'4070J3BJ '4080J3BJ '4095J3BJ '4115J3BJ '4125J3BJ '4145J3BJ '4165J3BJ '4180J3BJ '4200J3BJ '4219J3BJ '4250J3BJ '4290J3BJ '4350J3BJ			±70 ±80 ±95 ±115 ±125 ±145 ±165 ±200 ±219 ±250 ±290 ±350 ±395	V

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Users should verify actual device performance in their specific applications.

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TISP4xxxJ3BJ Overvoltage Protector Series

Electrical Characteristics, T_A = 25 °C (Unless Otherwise Noted)

	Parameter	Test Conditions		Min	Тур	Max	Unit
			'4070J3BJ			±77	
V _(BO) Ramp breal		'4080J3BJ			±88		
			'4095J3BJ			±104	
			'4115J3BJ			±125	
			'4125J3BJ			±135	
	Ramp breakover voltage	dv/dt ≤ ±1000 V/μs, Linear voltage ramp,	'4145J3BJ			±156	
		Maximum ramp value = ±500 V	'4165J3BJ			±177	V
(50)	,	di/dt = ±20 A/µs, Linear current ramp,	'4180J3BJ			±192	
		Maximum ramp value = ±10 A	'4200J3BJ '4219J3BJ			±212 ±231	
			4250J3BJ			±263	
			4290J3BJ			±303	
	4350J3BJ			±364			
			'4395J3BJ			±409	
I _(BO) Brea			'4070J3BJ thru '4115J3BJ			±900	
	Breakover current	$dv/dt = \pm 250 \text{ V/ms}, R_{SOURCE} = 300 \Omega$	'4125J3BJ thru '4219J3BJ			±800	mA
			'4250J3BJ thru '4395J3BJ			±600	
I _H	Holding current	$I_T = \pm 5 \text{ A}, \text{ di/dt} = \pm 30 \text{ mA/ms}$		±150		±600	mA
dv/dt	Critical rate of rise of	Linear voltage ramp		±5			kV/µs
aviat	off-state voltage	Maximum ramp value < 0.85V _{DRM}		10			ΚΨ/μΟ
I _D	Off-state current	$V_D = \pm 50 \text{ V}$	$T_A = 85 ^{\circ}C$			±10	μΑ
		'4070J3BJ thru '4115J3BJ		195	235		
		$f = 1 \text{ MHz}, V_d = 1 \text{ V rms}, V_D = 0$	'4125J3BJ thru '4219J3BJ		120	145	
			'4250J3BJ thru '4395J3BJ		105	125	
			'4070J3BJ thru '4115J3BJ		180	215	
		$f = 1 \text{ MHz}, V_d = 1 \text{ V rms}, V_D = -1 \text{ V}$	'4125J3BJ thru '4219J3BJ		110	132	
Co			'4250J3BJ thru '4395J3BJ		95	115	
	Off-state capacitance		'4070J3BJ thru '4115J3BJ		165	200	pF
	On-State Capacitance	$f = 1 \text{ MHz}, V_d = 1 \text{ V rms}, V_D = -2 \text{ V}$	'4125J3BJ thru '4219J3BJ		100	120	ρı
			'4250J3BJ thru '4395J3BJ		90	105	
	1		'4070J3BJ thru '4115J3BJ		85	100	
		$f = 1 \text{ MHz}, V_d = 1 \text{ V rms}, V_D = -50 \text{ V}$ '4125J3E	'4125J3BJ thru '4219J3BJ		50	60	
			'4250J3BJ thru '4395J3BJ		42	50	
		f = 1 MHz, V _d = 1 V rms, V _D = -100 V	'4125J3BJ thru '4219J3BJ		40	50	
		(see Note 3)	'4250J3BJ thru '4395J3BJ		35	40	

NOTE: 3. To avoid possible clipping, the TISP4125J3BJ is tested with V_D = -98 V.

Thermal Characteristics

	Parameter	Test Conditions	Min	Тур	Max	Unit
$R_{\theta JA}$	Junction to ambient thermal resistance	EIA/JESD51-3 PCB, $I_T = I_{TSM(1000)}$ (see Note 4)			90	°C/W

NOTE: 4. EIA/JESD51-2 environment and PCB has standard footprint dimensions connected with 5 A rated printed wiring track widths.

Parameter Measurement Information

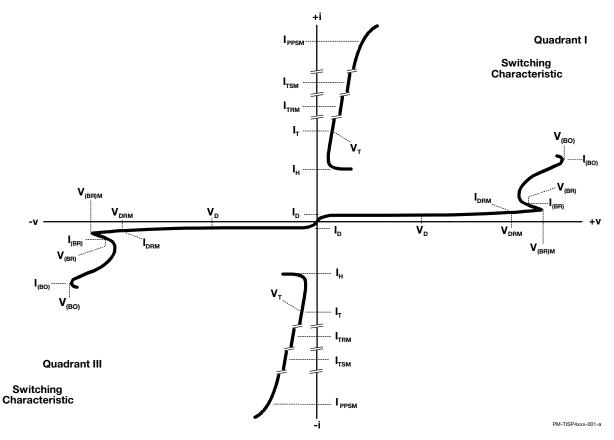


Figure 1. Voltage-Current Characteristic for T and R Terminals All Measurements are Referenced to the R Terminal

Typical Characteristics

OFF-STATE CURRENT JUNCTION TEMPERATURE TC4JAG 100 $V_D = \pm 50 \text{ V}$ 10 ||_p| - Off-State Current - μΑ 0.001 -25 0 50 75 100 125 150 T₁ - Junction Temperature - °C

Figure 2.

NORMALIZED HOLDING CURRENT

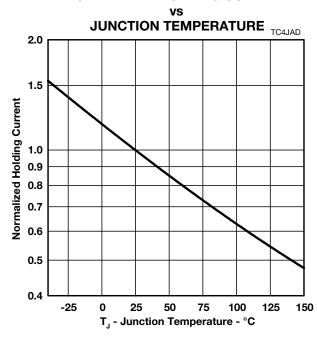


Figure 4.

NORMALIZED BREAKOVER VOLTAGE

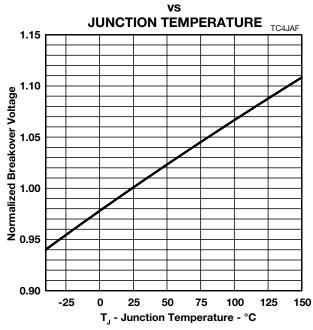


Figure 3.

NORMALIZED CAPACITANCE

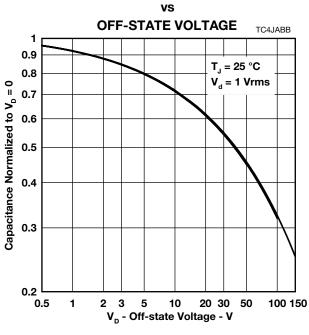


Figure 5.

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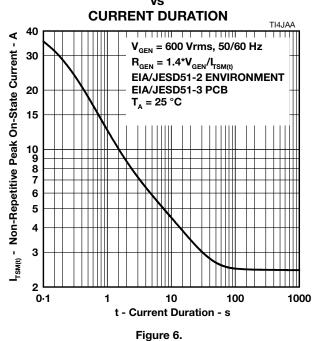
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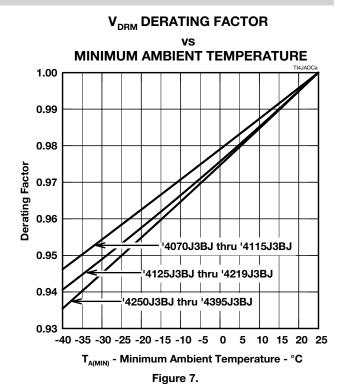
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Rating and Thermal Characteristics

NON-REPETITIVE PEAK ON-STATE CURRENT





Applications Information

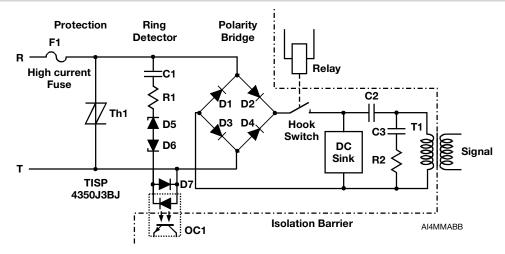


Figure 8. Typical Application Circuit

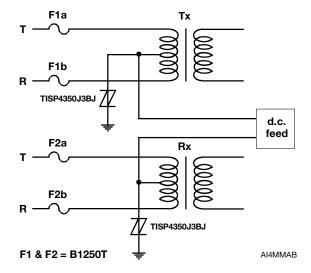


Figure 9. Typical Application Circuit

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