

1. Pin Descriptions

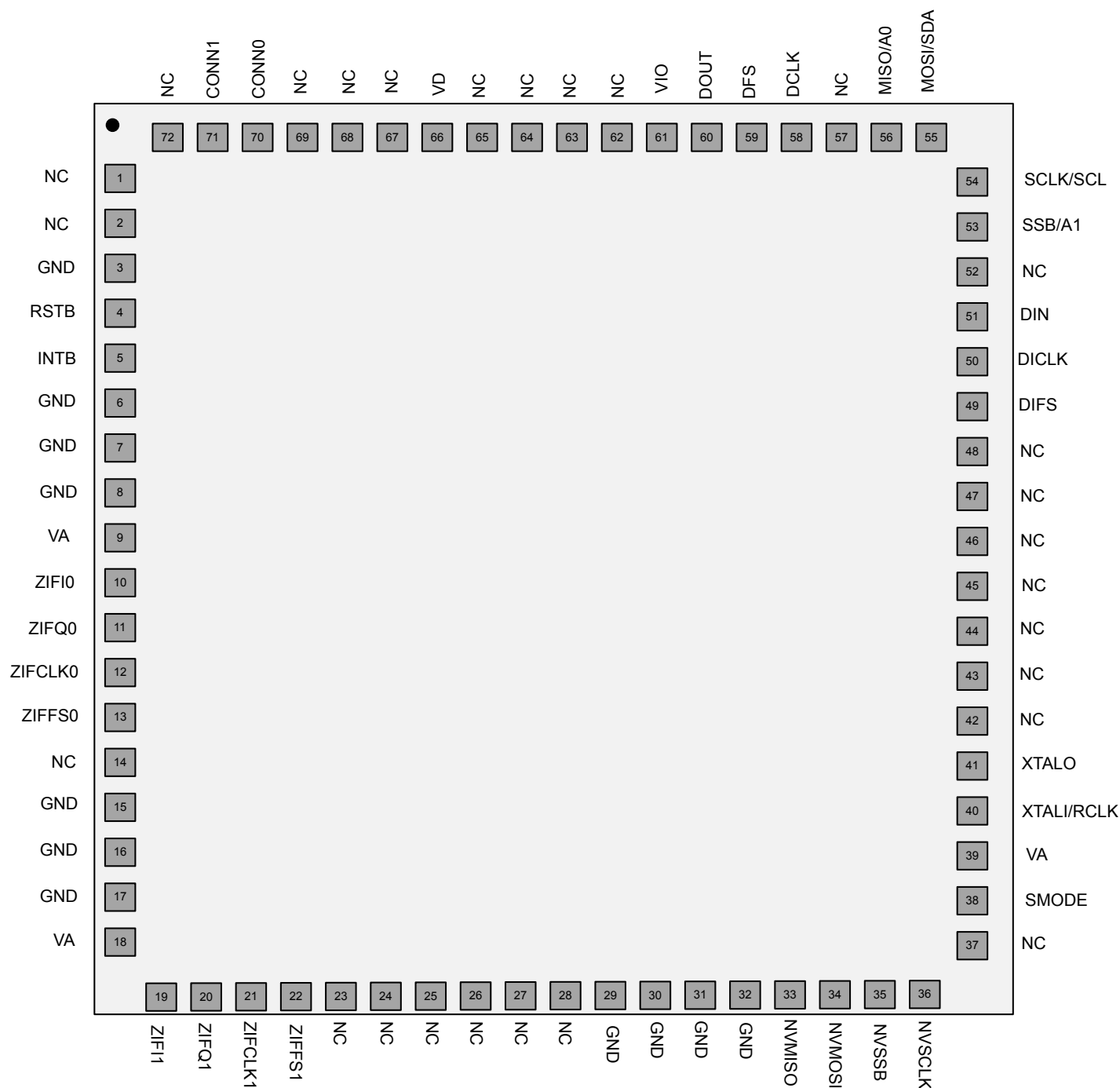


Figure 1.1. Si4692 Pinout Diagram

2. Package Outline

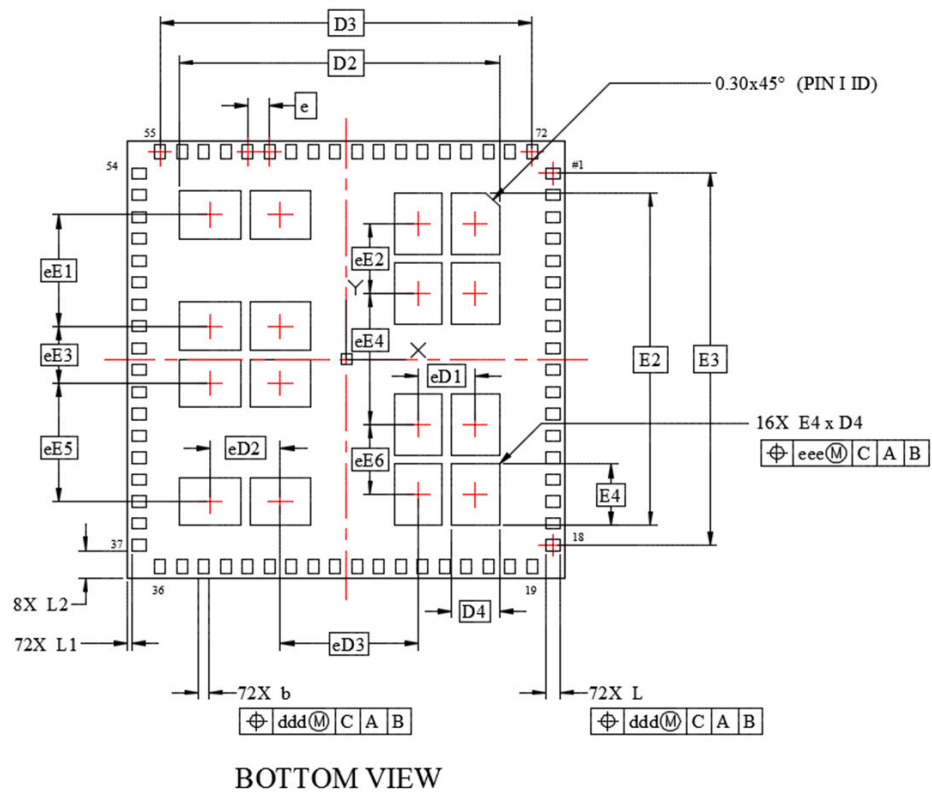
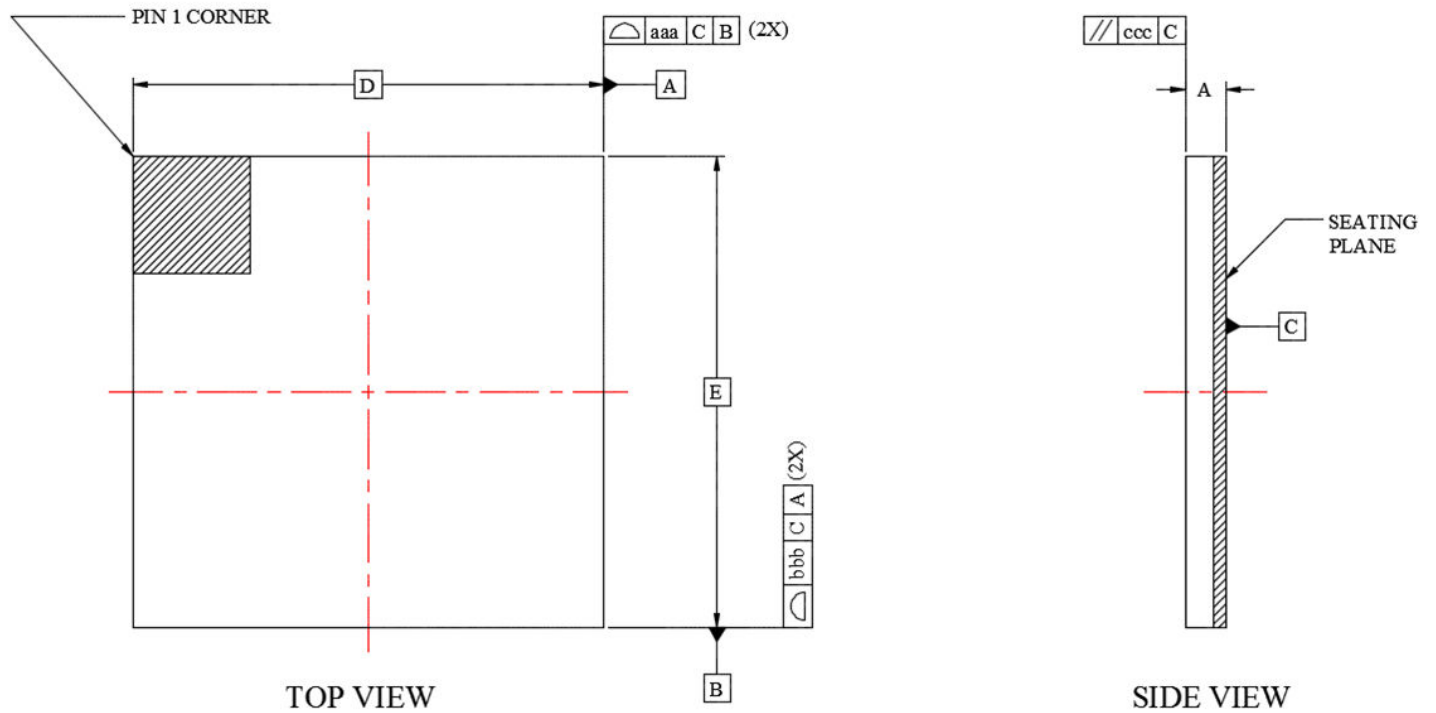


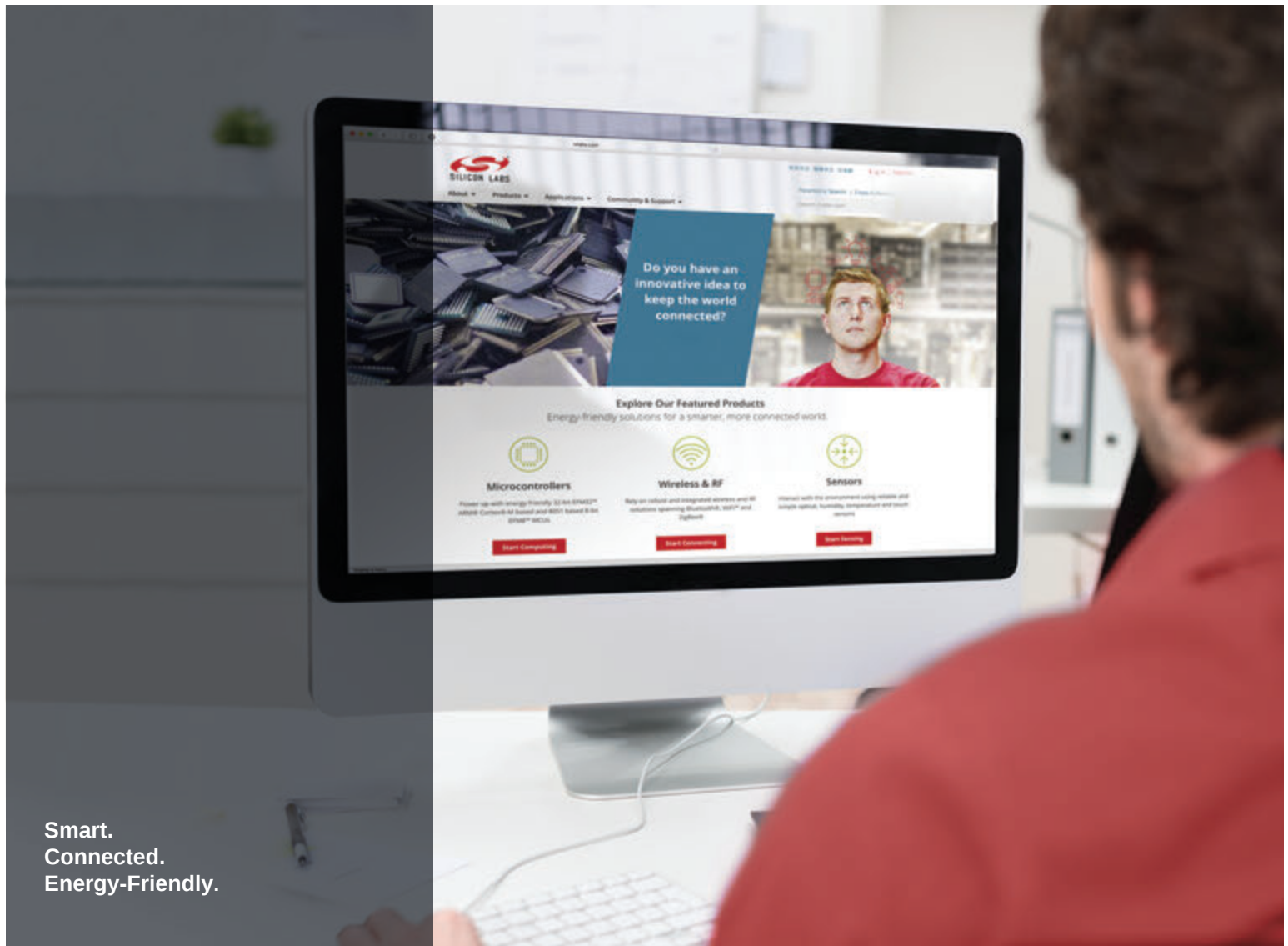
Figure 2.2. 10 x 10 mm 72-Pin LGA

Table 2.1. Package Dimensions

Dimension	Min	Nom	Max
A	1.00	1.08	1.20
b	0.15	0.25	0.35
D	10.0 BSC		
D2	7.20	7.30	7.40
D3	8.50 BSC		
D4	1.00	1.10	1.20
e	0.50 BSC		
E	10.0 BSC		
E2	7.50	7.60	7.70
E3	8.50 BSC		
E4	1.30	1.40	1.50
L	0.225	0.325	0.425
L1	0.05	0.10	0.15
L2	0.575	0.625	0.675
eD1	1.30 BSC		
eD2	1.60 BSC		
eD3	3.15 BSC		
eE1	2.55 BSC		
eE2	1.60 BSC		
eE3	1.30 BSC		
eE4	3.00 BSC		
eE5	2.70 BSC		
eE6	1.60 BSC		
aaa	0.10		
bbb	0.10		
ccc	0.10		
ddd	0.10		
eee	0.10		

Note:

1. All dimensions shown are in millimeters (mm) unless otherwise noted.
2. Dimensioning and Tolerancing per ANSI Y14.5M-1994.
3. This drawing conforms to the JEDEC Solid State Outline MO-220.
4. Recommended card reflow profile is per the JEDEC/IPC J-STD-020 specification for Small Body Components.

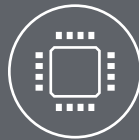


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Silicon Laboratories Inc.
400 West Cesar Chavez
Austin, TX 78701
USA

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