

# RB751S40

## ELECTRICAL CHARACTERISTICS ( $T_A = 25^\circ\text{C}$ unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit
Reverse Breakdown Voltage ( $I_R = 10\ \mu\text{A}$ )	$V_{(BR)R}$	30	–	–	V
Total Capacitance ( $V_R = 1.0\ \text{V}$ , $f = 1.0\ \text{MHz}$ )	$C_T$	–	2.0	2.5	pF
Reverse Leakage ( $V_R = 30\ \text{V}$ )	$I_R$	–	300	500	nAdc
Forward Voltage ( $I_F = 1.0\ \text{mA}$ )	$V_F$	–	0.28	0.37	Vdc

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

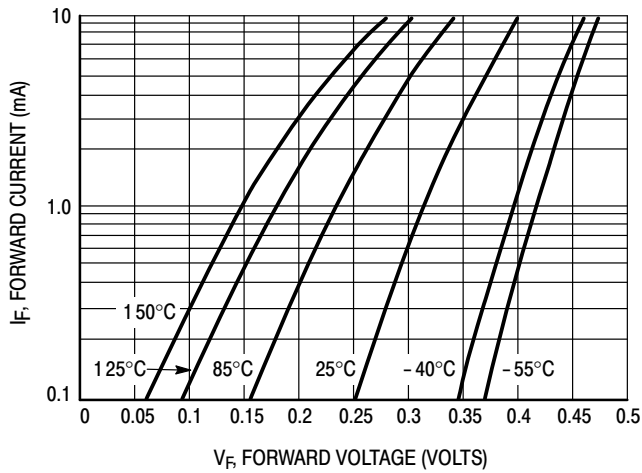


Figure 1. Typical Forward Voltage

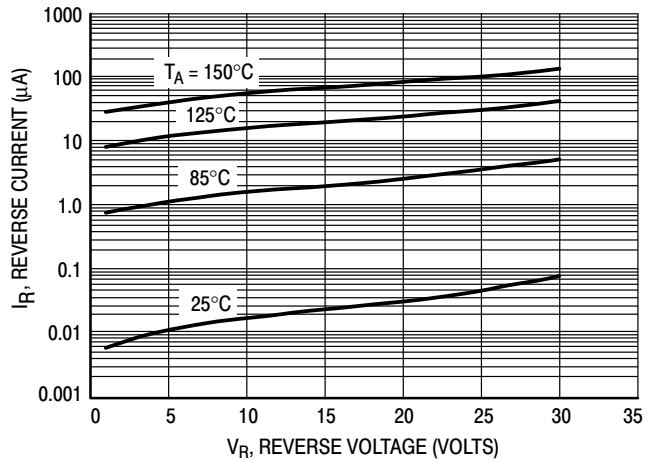


Figure 2. Reverse Current versus Reverse Voltage

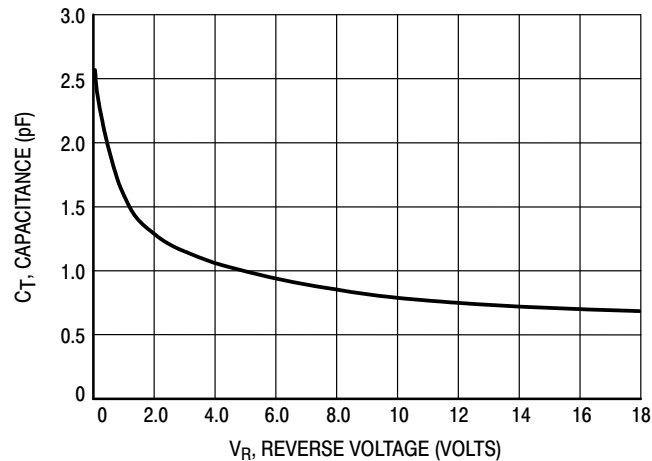
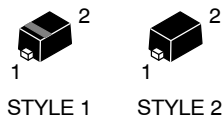


Figure 3. Typical Capacitance

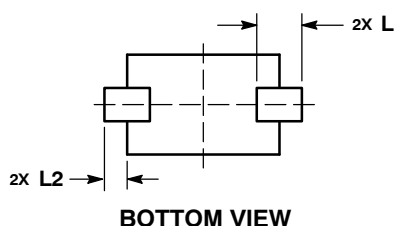
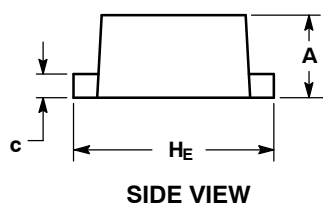
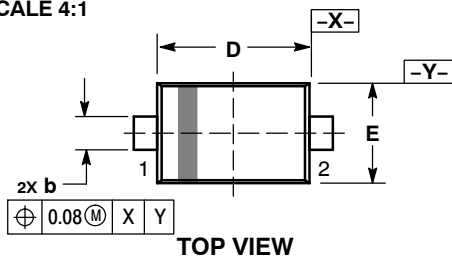
# MECHANICAL CASE OUTLINE

## PACKAGE DIMENSIONS

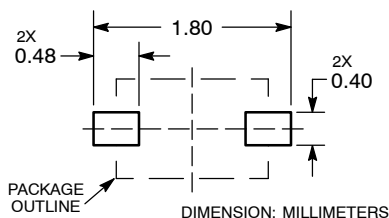
ON Semiconductor®



SCALE 4:1



### RECOMMENDED SOLDERING FOOTPRINT\*



**SOD-523**  
CASE 502-01  
ISSUE E

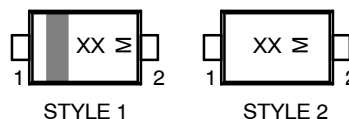
DATE 28 SEP 2010

#### NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.
4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS.

DIM	MILLIMETERS		
	MIN	NOM	MAX
A	0.50	0.60	0.70
b	0.25	0.30	0.35
c	0.07	0.14	0.20
D	1.10	1.20	1.30
E	0.70	0.80	0.90
H E	1.50	1.60	1.70
L	0.30 REF		
L2	0.15	0.20	0.25

### GENERIC MARKING DIAGRAM\*



XX = Specific Device Code  
M Date Code

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present.

STYLE 1:  
PIN 1. CATHODE (POLARITY BAND)  
2. ANODE


STYLE 2:  
NO POLARITY

\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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STATUS:	ON SEMICONDUCTOR STANDARD	
NEW STANDARD:		
DESCRIPTION:	SOD-523	PAGE 1 OF 2

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