

Function Tables

PI90LV031A

Enables		Input	Outputs	
EN	$\overline{\text{EN}}$	D _{IN}	D _{OUT+}	D _{OUT-}
H	X	H	H	L
H	X	L	L	H
X	L	H	H	L
X	L	L	L	H
L	H	X	Z	Z

PI90LV027A

Input	Outputs	
D _{IN}	D _{OUT+}	D _{OUT-}
H	H	L
L	L	H

PI90LV017A

Input	Outputs	
D _{IN}	D _{OUT+}	D _{OUT-}
H	H	L
L	L	H

Pin Descriptions

Name	Description
D _{IN}	TTL/CMOS driver input pins
D _{O+}	Non-inverting driver output pins
D _{O-}	Inverting driver output pins
GND	Ground pin
V _{CC}	Positive power supply pin, +3.3V ±10%

Recommended Operating Conditions

	Min.	Typ.	Max.	Units
Supply Voltage (V _{CC})	+3.0	+3.3	+3.6	V
Operating Free Air Temperature	-40	+25	+85	°C

Absolute Maximum Ratings (see Note 1, Page 4)

Supply Voltage (V _{CC})	-0.3V to +4.0V
Input Voltage (D _{IN})	-0.3V to (V _{CC} +0.3V)
Enable Input Voltage (EN, EN*)	-0.3V to (V _{CC} +0.3V)
Output Voltage (D _{OUT+} , D _{OUT-})	-0.3V to +3.9V
Short Circuit Duration (D _{OUT+} , D _{OUT-})	Continuous
S Package	750mW
Derate S Package	8.5mW/°C above +25°C
Storage Temperature Range	-65°C to +150°C
Lead Temperature Range Soldering (4s)	+260°C
Maximum Junction Temperature	+150°C
ESD Rating	≥6kV

Note:

Stresses greater than those listed under Absolute Maximum Ratings may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

Electrical Characteristics

Over supply voltage and operating temperature ranges, unless otherwise specified. (Notes 2,3,4)

Symbol	Parameter	Conditions	Pin	Min.	Typ.	Max.	Units
V _{OD1}	Differential Output Voltage	R _L = 100Ω (Fig.1)	D _{OUT-} D _{OUT+}	250	350	450	mV
ΔV _{OD1}	Change in Magnitude of V _{OD1} for Complementary Output States				4	35	ImVl
V _{OS}	Offset Voltage			1.125	1.25	1.375	V
ΔV _{OS}	Change in Magnitude of V _{OS} for Complementary Output States				5	25	ImVl
V _{OH}	Output Voltage High				1.38	1.6	V
V _{OL}	Output Voltage Low			0.90	1.03		
V _{IH}	Input Voltage High	V _{IN} = V _{CC} or 2.5V	D _{IN} EN EN*	2.0		V _{CC}	μA
V _{IL}	Input Voltage Low			GND		0.8	
I _{IH}	Input Current			-10	±1	+10	μA
I _{IL}	Input Current			-10	±1	+10	
V _{CL}	Input Clamp Voltage			-1.5	-0.8		V
I _{OS}	Output Short Circuit Current	ENABLED, ⁽¹⁰⁾ D _{IN} = V _{CC} , D _{OUT+} = 0V or D _{IN} = GND, D _{OUT-} = 0V	D _{OUT-} D _{OUT+}		-6.0	-9.0	mA
I _{OSD}	Differential Output Short Circuit Current	ENABLED, V _{OD} = 0V ⁽¹⁰⁾			-6.0	-9.0	
I _{OFF}	Power-off Leakage	V _{OUT} = 0V or 3.6V, V _{CC} = 0V or Open		-20	±1	+20	μA
I _{IOZ}	Output Three-State Current	EN = 0.8V and EN* = 2.0V V _{OUT} = 0V or V _{CC}		-10	±1	+10	
I _{CC}	No Load Supply Current Drivers Enabled	D _{IN} = V _{CC} or GND	V _{CC}		5.0	8.0	mA
I _{ICL}	Loaded Supply Current Drivers Enabled	R _L = 100Ω All Channels, D _{IN} = V _{CC} or GND (all inputs)			23	30	
I _{CCZ}	No Load Supply Current Drivers Disabled	D _{IN} = V _{CC} or GND, EN = GND, EN* = V _{CC}			2.6	6.0	

Switching Characteristics

$V_{CC} = +3.3V \pm 10\%$, $T_A = -40^\circ C$ to $+85^\circ C$ (Notes 3,9,11)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Units
t_{PHLD}	Differential Propagation Delay High to Low	$R_L = 100\Omega$, $C_L = 10pF$ (Figures 2 & 3)	0.8	1.18	2.0	ns
t_{PLHD}	Differential Propagation Delay Low to High		0.8	1.25	2.0	
t_{SKD1}	Differential Pulse Skew $ t_{PHLD} - t_{PLHD} $ ⁽⁵⁾		0	0.07	0.4	
t_{SKD2}	Channel-to-Channel Skew ⁽⁶⁾		0	0.1	0.5	
t_{SKD3}	Differential Part-to-Part Skew ⁽⁷⁾		0		1.0	
t_{SKD4}	Differential Part-to-Part Skew ⁽⁸⁾		0		1.2	
t_{TLH}	Rise Time			0.38	1.5	
t_{THL}	Fall Time			0.40	1.5	
t_{PHZ}	Disable Time High to Z	$R_L = 100\Omega$, $C_L = 10pF$ (Figures 4 & 5)			5	ns
t_{PLZ}	Disable Time Low to Z				5	
t_{PZH}	Enable Time Z to High				7	
t_{PZL}	Enable Time Z to Low				7	
t_{MAX}	Maximum Operating Frequency ⁽¹³⁾		200	250		MHz

Notes:

- “Absolute Maximum Ratings” are those values beyond which the safety of the device cannot be guaranteed. They are not meant to imply that the devices should be operated at these limits. The table of “Electrical Characteristics” specifies conditions of device operation.
- Current into device pins is defined as positive. Current out of device pins is defined as negative. All voltages are referenced to ground except: V_{OD1} and ΔV_{OD1} .
- All typicals are given for: $V_{CC} = +3.3V$, $T_A = +25^\circ C$.
- The PI90LV031A/PI90LV027A/PI90LV017A are current mode devices and only functions within datasheet specifications when a resistive load is applied to the driver outputs typical range is (90 Ω to 110 Ω).
- t_{SKD1} , $|t_{PHLD} - t_{PLHD}|$ is the magnitude difference in differential propagation delay time between the positive going edge and the negative going edge of the same channel.
- t_{SKD2} is the Differential Channel-to-Channel Skew of any event on the same device.
- t_{SKD3} , Differential Part-to-Part Skew, is defined as the difference between the minimum and maximum specified differential propagation delays. This specification applies to devices at the same V_{CC} and with $5^\circ C$ of each other within the operating temperature range.
- t_{SKD4} , Part-to-Part Skew, is the differential Channel-to-Channel skew of any event between devices. This specification applies to devices over recommended operating temperature and voltage ranges, and across process distribution. t_{SKD4} is defined as $|Max - Min|$ differential propagation delay.
- Generator waveform for all tests unless otherwise specified: $f = 1$ MHz, $Z_O = 50\Omega$, $t_r \leq 1ns$, and $t_f \leq 1ns$.
- Output short circuit current (I_{OS}) is specified as magnitude only, minus sign indicates direction only.
- C_L includes probe and jig capacitance.
- All input voltages are for one channel unless otherwise specified. Other inputs are set to GND.
- f_{MAX} generator input conditions: $t_R = t_F < 1ns$, (0% to 100%), 50% duty cycle, 0V to 3V.
Output Criteria: duty cycle = 45%/55%, $V_{OD} > 250mV$, all channels switching.

Parameter Measurement Information

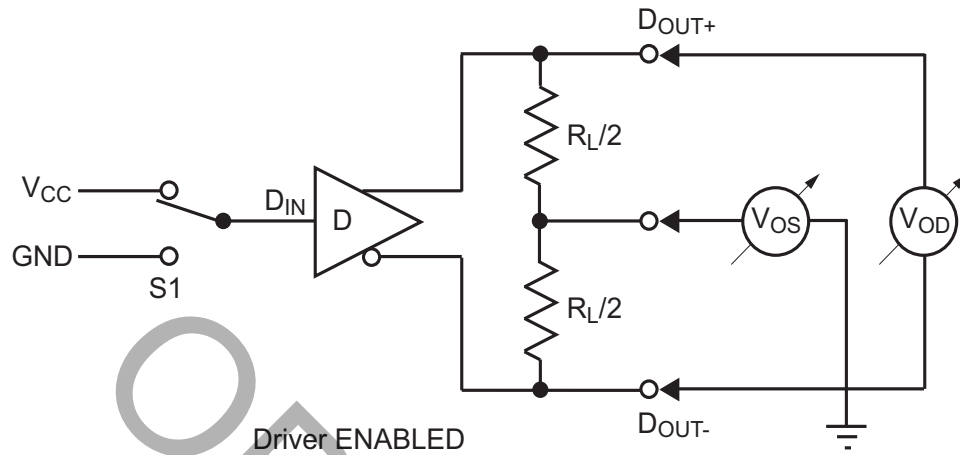


Figure 1. Driver VOD and VOS Test Circuit

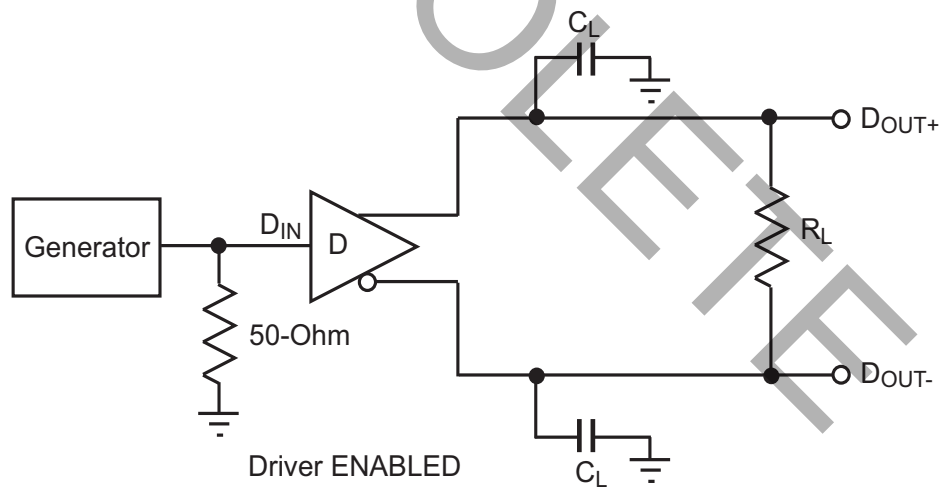


Figure 2. Driver Propagation Delay and Transition Time Test Circuit

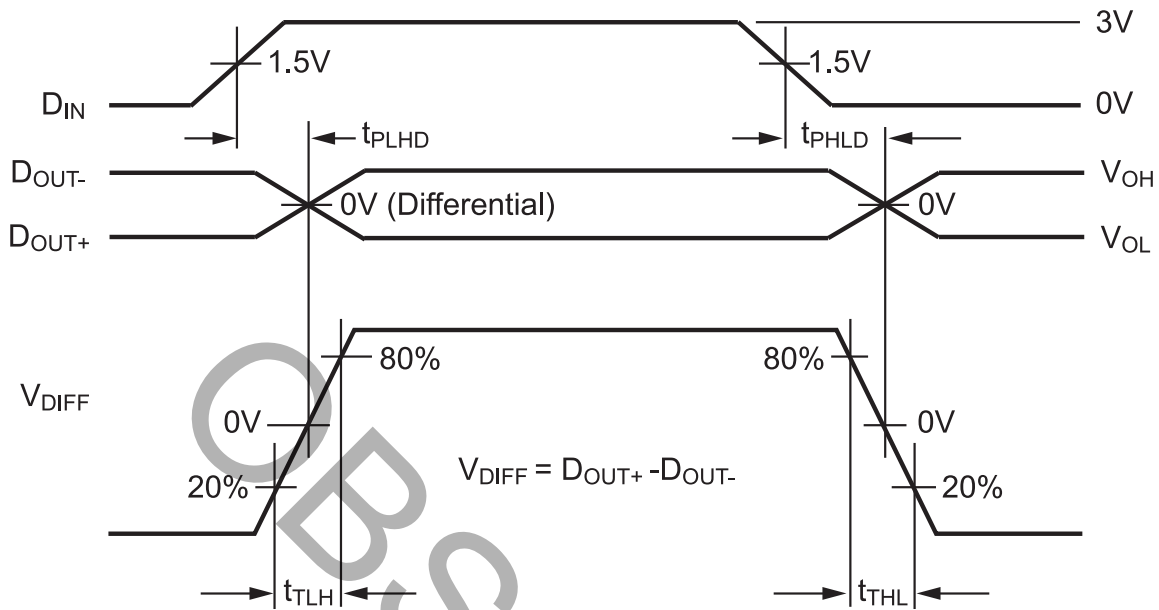


Figure 3. Driver Propagation Delay and Transition Time Waveforms

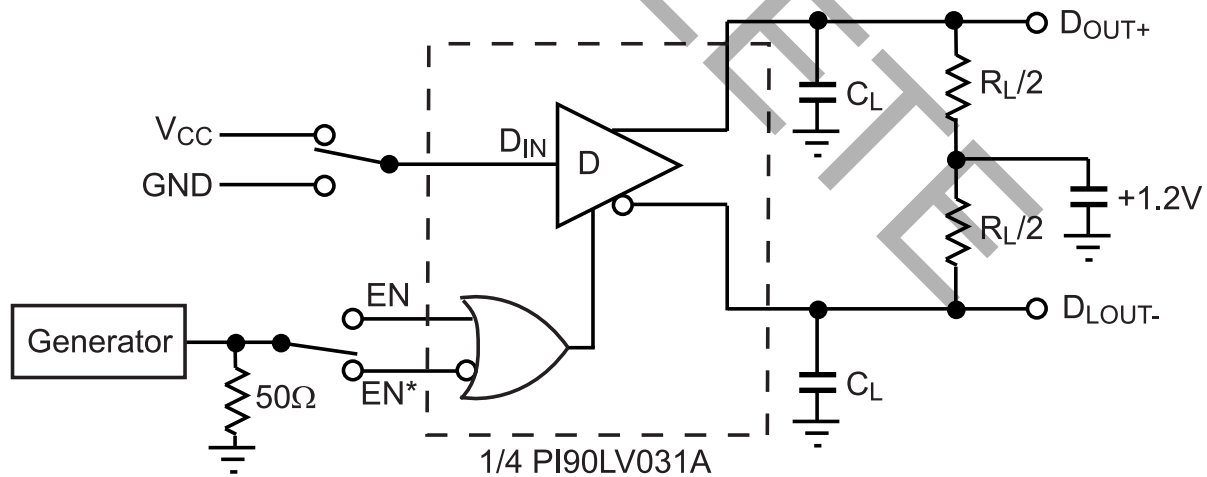


Figure 4. Driver Three-State Delay Test Circuit

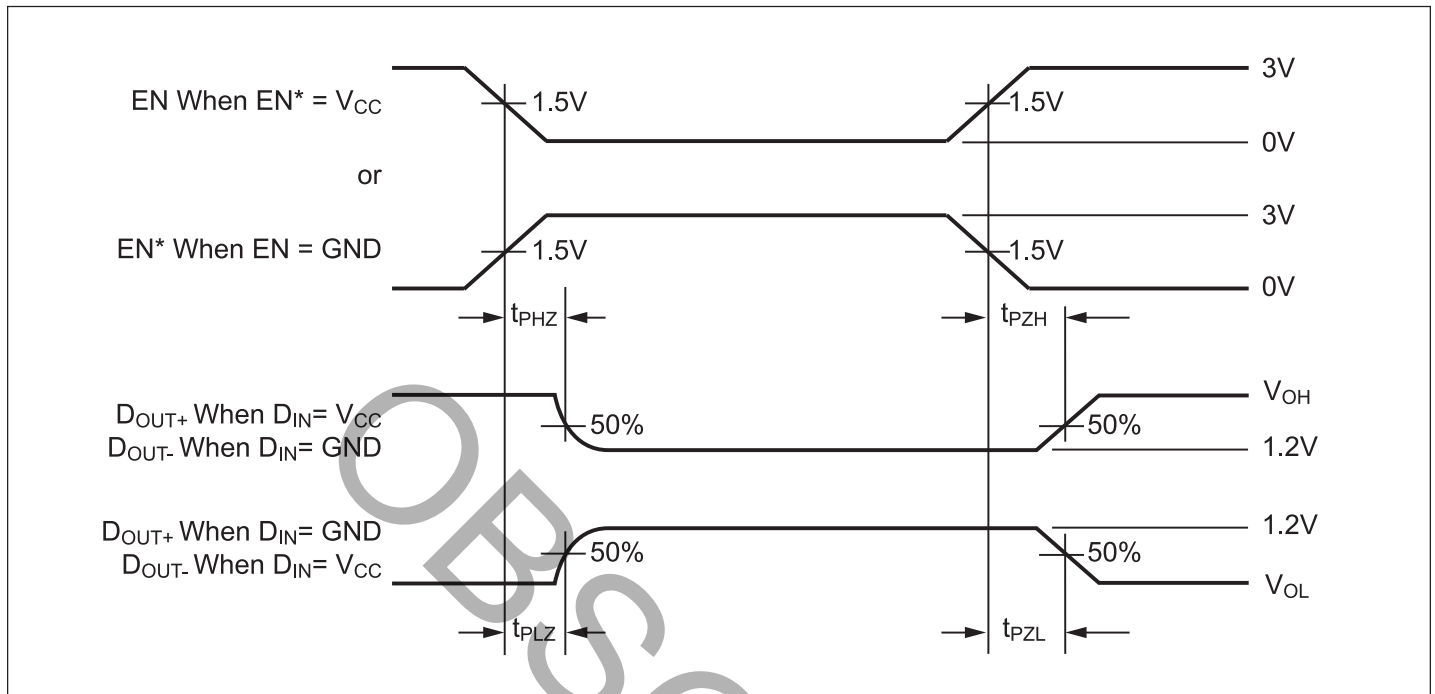


Figure 5. Driver 3-State Delay Waveform

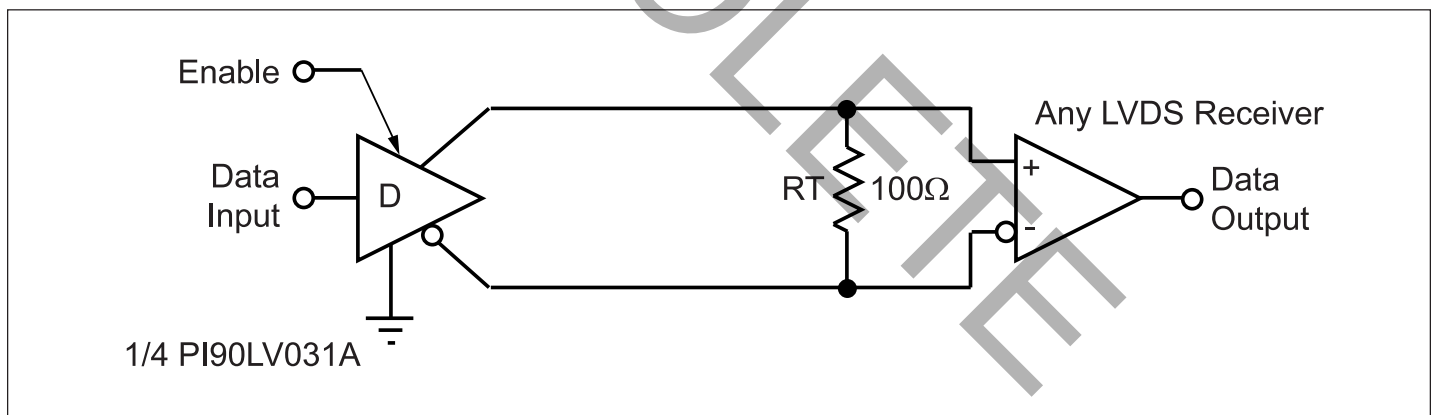
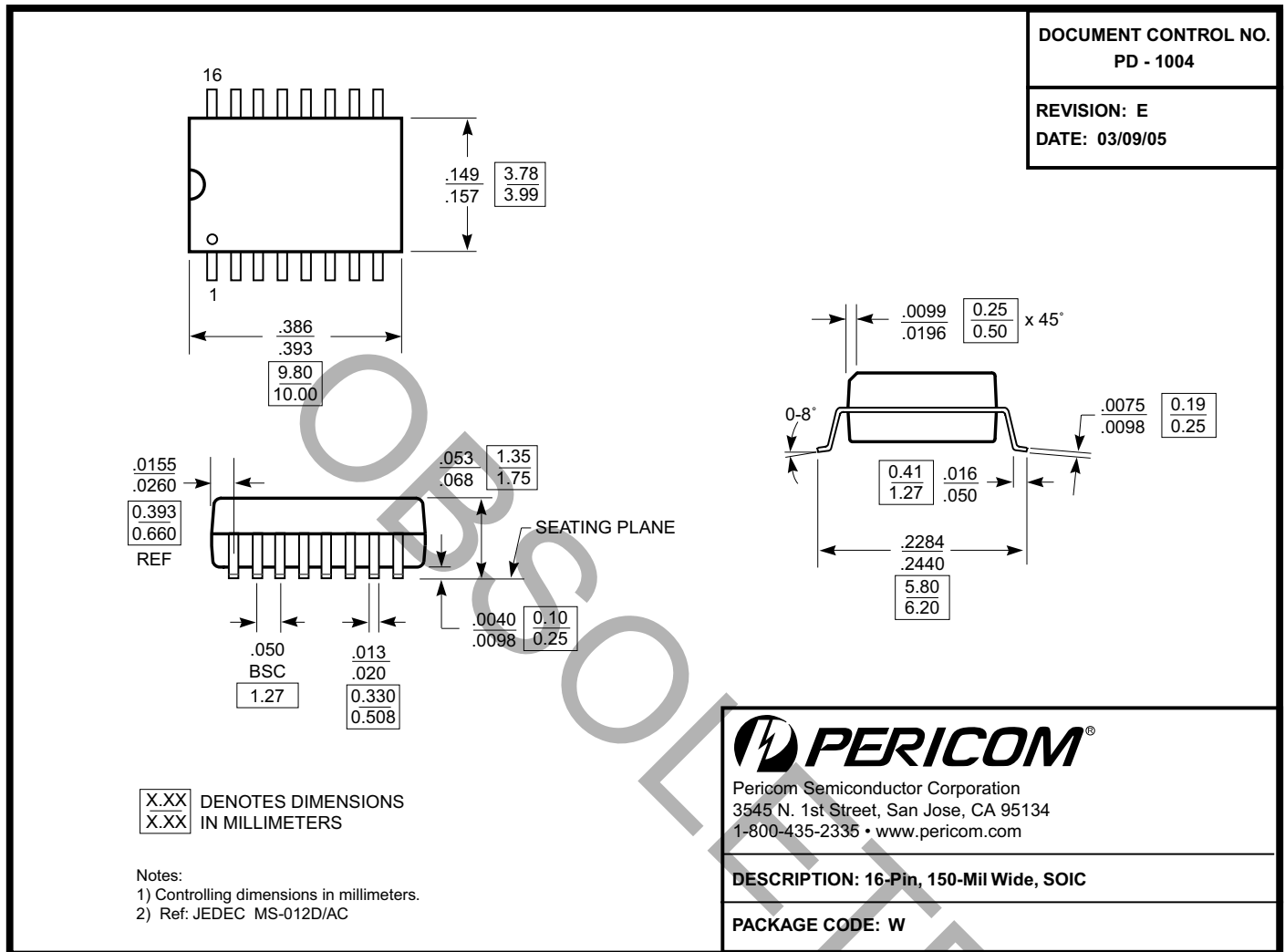


Figure 6. Point-to-Point Application

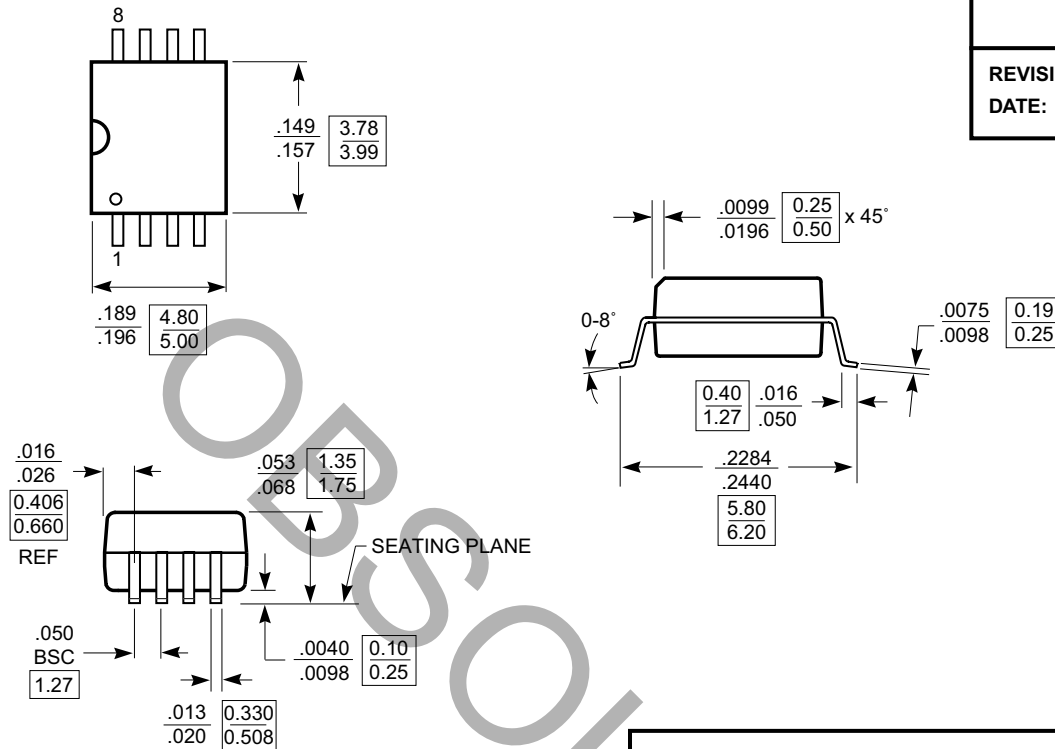
Packaging Mechanical: 16-Pin SOIC (W)



Packaging Mechanical: 8-Pin SOIC (W)

DOCUMENT CONTROL NO.
PD - 1001

REVISION: F
DATE: 03/09/05



X.XX DENOTES DIMENSIONS
X.XX IN MILLIMETERS

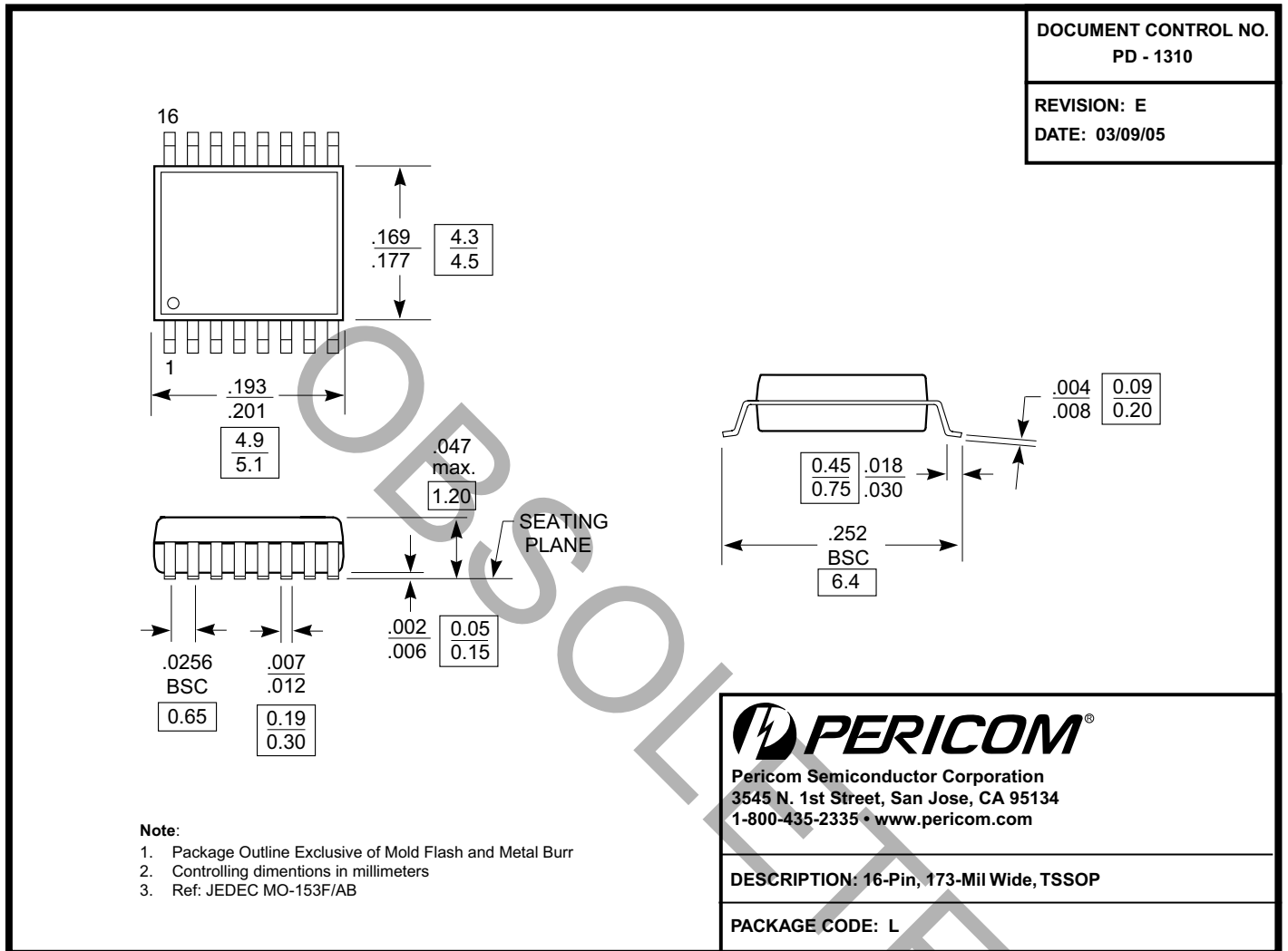
Notes:
1) Controlling dimensions in millimeters.
2) Ref: JEDEC MS-012D/AA



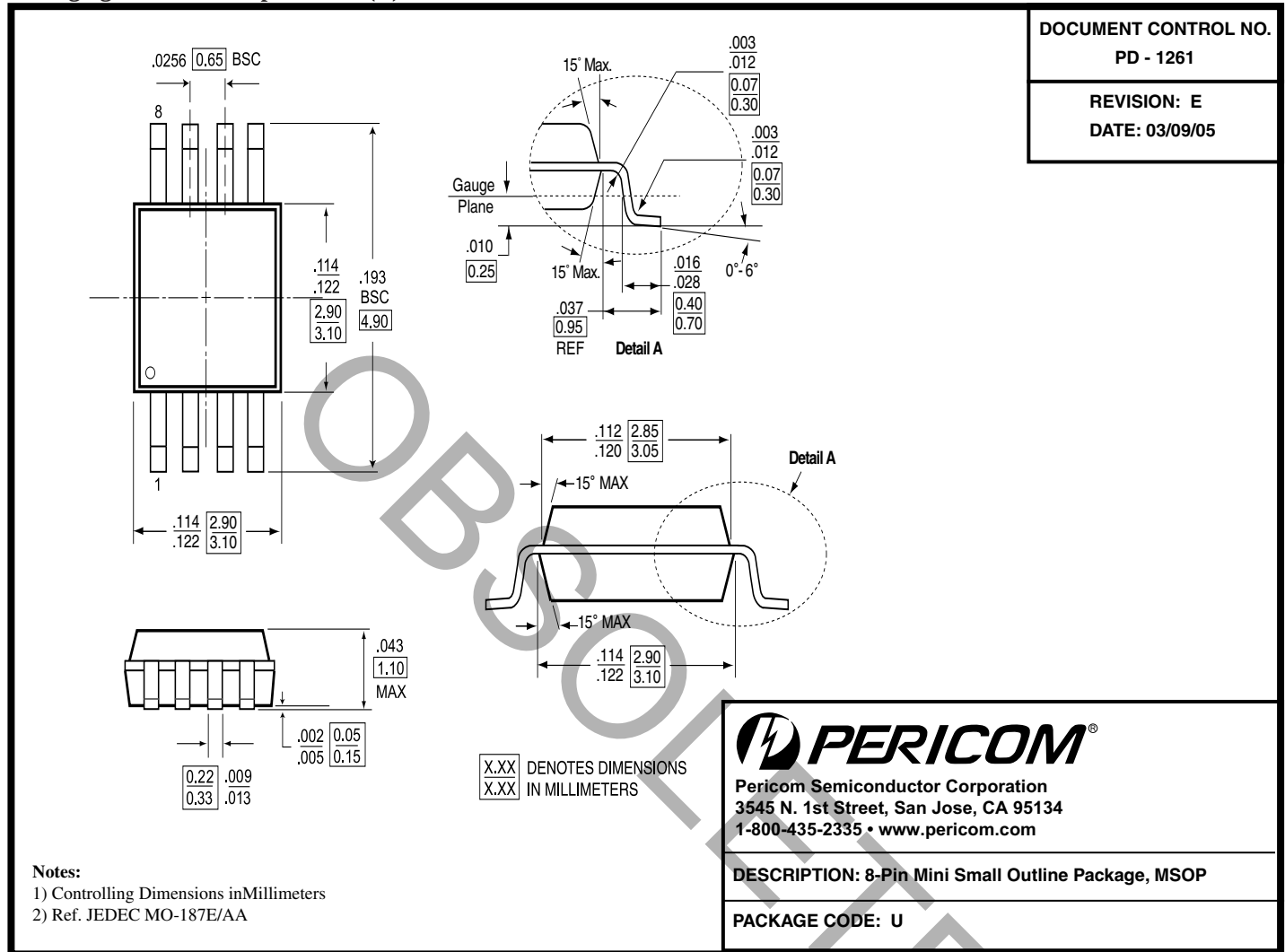
Pericom Semiconductor Corporation
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1-800-435-2335 • www.pericom.com

DESCRIPTION: 8-Pin, 150-Mil Wide, SOIC

PACKAGE CODE: W

Packaging Mechanical: 16-Pin TSSOP (L)


Packaging Mechanical: 8-pin MSOP (U)



Ordering Information

Ordering Code	Package Code	Package Description
PI90LV017AWE	W	Pb-free & Green, 8-pin SOIC
PI90LV017AUE	U	Pb-free & Green, 8-pin MSOP
PI90LV027AWE	W	Pb-free & Green, 8-pin SOIC
PI90LV027AUE	U	Pb-free & Green, 8-pin MSOP
PI90LV031AWE	W	Pb-free & Green, 16-pin SOIC
PI90LV031ALE	L	Pb-free & Green, 16-pin TSSOP

Notes:

- Thermal characteristics can be found on the company web site at www.pericom.com/packaging/