LB1830MC

Allowable Operating Ranges at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
Supply voltage	V _{CC} , V _S		3.0 to 9.0	V
Input high level voltage	VIH		2.0 to 9.0	V
Input low level voltage	V _{IL}		-0.3 to +0.3	V
Control voltage	VC		1.0 to 6.0	V

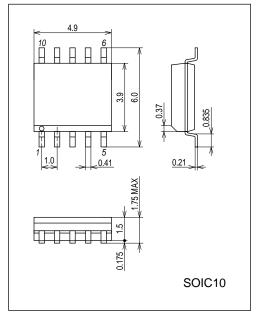
Electrical Characteristics at Ta = 25°C, $V_{CC} = 6V$

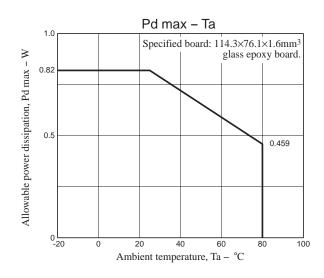
Down ston	Completel	Conditions	Ratings			1.114	
Parameter	Symbol	Conditions	min	typ	max	Unit	
Current drain	I _{CC} 0	$IN1 = IN2 = Vm = 0V$, $V_C = Vref$ at standby mode		0.1	10	μΑ	
	I _{CC} 1	Forward/reverse, control, load OPEN		2	3	mA	
	I _{CC} 2	Forward/reverse, saturation, load OPEN		3	5	mA	
	I _{CC} 3	Braking, load OPEN		5	8	mA	
Output saturation voltage	Vsat1	I _O = 40mA (upper side + lower side)		0.2	0.3	V	
	Vsat2	I _O = 80mA (upper side + lower side)		0.4	0.6	V	
Reference voltage	Vref	I _{Vref} = 1mA	1.85	2.0	2.15	V	
Voltage characteristics of output voltage	ΔV _O -Line	$V_O = 5V$, $V_{CC} = 5.5$ to 9V, $I_O = 40$ mA			80	mA	
Current characteristics of output voltage	ΔV _O -Load	$V_O = 5V$, $V_{CC} = 6V$, $I_O = 10$ to 80mA			50	mA	
Input current	I _{IN}	V _{IN} = 5V		90	150	μΑ	
Output voltage	VO	V _C = 2V	2.3×V _C		2.5×V _C	V	

Package Dimensions

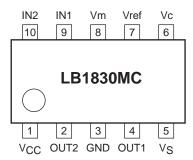
unit : mm (typ)

3426A

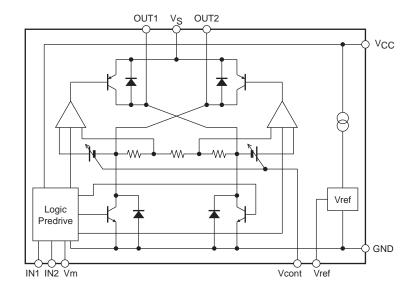




Pin Assignment



Block Diagram



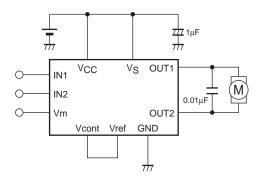
- The output voltage (voltage between output pins) Vo during drive with constant voltage is set as follows: $V_O = (V_C \text{ pin input voltage}) \times 2.4 \text{ (typical)}$
- There is no hierarchical relationship among voltages; VCC (control supply voltage), VS (motor supply voltage), IN1/IN2 (input signal voltage).

Truth Table

Input			Output		Mada				
IN1	IN2	Vm	OUT1	OUT2	Mode				
L	L	L	OFF	OFF	Standby				
Н	L	L	Н	Ы	Forward (Regulated)				
Н	L	Н	Н	Ш	Forward (Saturation)				
L	Н	L	L	Н	Reverse (Regulated)				
L	Н	Н	L	Н	Reverse (Saturation)				
Н	Н	*	L	L	Brake				

 $^{^{\}star}$ when in saturation mode, V_{C} = VS available.

Application Circuit Example



ON Semiconductor and the ON logo are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of SCILLC's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equa