Custom Engineered Solutions for Tomorrow

A Global Leader in the Design, Development, and Manufacture of Sensor and Magnetic Components

Series Datasheet – KT Reed Relays

www.standexmeder.com

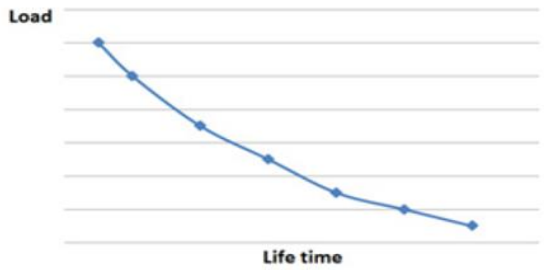
Coil Data		Coil Voltage (nom.)	Coil Resistance (typ.)	Pull-In Voltage (max.)	Drop-Out Voltage (min.)	Nominal Coil Power (typ.)
Contact Form	Switch Model					
Unit		VDC	Ohm	VDC	VDC	mW
1A	85	05	80	3.5	0.6	313
		12	475	8.4	1.4	303
		24	1,800	16	2.9	320
The Pull-In / Drop-Out Voltage and Coil Resistance will change at rate of 0.4% per °C.						

Relay Data		Unit
Dielectric Strength Coil/Contact (min.) according to EN60255-5	7	kVDC
Insulation Resistance Coil/Contact (typ.) Rh<45%, 200V Test Voltage	10^{13}	Ohm
Capacitance Coil/Contact (typ.) @ 10 kHz	1.2	pF
Shock Resistance (max.) 1/2 sine wave duration 11ms	30	g
Vibration Resistance (max.)	20	g
Operating Temperature	-40 to 100	°C
Storage Temperature	-40 to 125	°C
Soldering Temperature (max.) 5 sec. max.	260	°C
Washability	Fully Sealed	

KT Reed Relay

Life Test Data

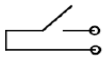
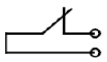
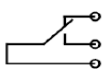
*Load increase reduces life expectancy of Reed Switches



Handling & Assembly Instructions

- Switching inductive and/or capacitive loads create voltage and/or current peaks, which may damage the relay. Protective circuits need to be used.
- External magnetic fields needs to be taken into consideration, including a too high packing density. This may influence the relays' electrical characteristics.
- Mechanical shock impacts e.g. dropping the relays may cause immediate or post-installation failure.
- Wave soldering: maximum 260°/5 seconds.
- Reflow soldering: Recommendations given by the soldering paste manufacturer need to be considered as well as the temperature limits of other components/processes.

Glossary Contact Form

Form A	NO = Normally Open Contacts SPST = Single Pole Single Throw	
Form B	NC = Normally Closed Contacts SPST = Single Pole Single Throw	
Form C	Changeover SPDT = Single Pole Double Throw	

REACH COMPLIANT

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USA: +1.866.782.6339 | salesusa@standexmeder.com
Europe: +49.7731.8399.0 | info@standexmeder.com
Asia: +86.21.37820625 | salesasia@standexmeder.com

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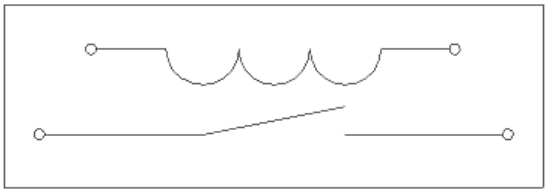
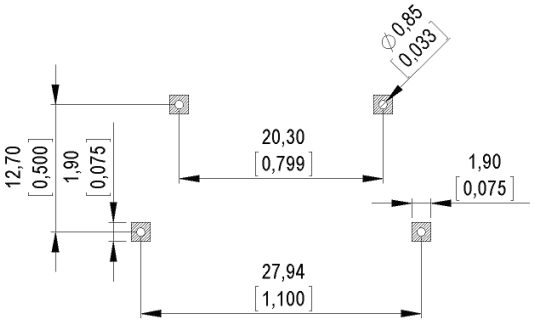
Layout

Top View



Pad-Layout SMD



Pad-Layout THT