### International **TOR** Rectifier

#### **Qualification Information**<sup>+</sup>

|             |                      |                                                                                                                                                                               | Automotive<br>(per AEC-Q100 <sup>††</sup> ) |  |  |
|-------------|----------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---------------------------------------------|--|--|
| Qualificati | on Level             | Comments: This family of ICs has passed an Automotive qualification. IR's Industrial and Consumer qualification level is granted by extension of the higher Automotive level. |                                             |  |  |
|             |                      | DPAK-3L                                                                                                                                                                       | MSL1, 260°C<br>(per IPC/JEDEC J-STD-020)    |  |  |
| Moisture    | Sensitivity Level    | D2PAK-3L                                                                                                                                                                      | MSL1, 260°C<br>(per IPC/JEDEC J-STD-020)    |  |  |
|             |                      | TO220-5L Not applicable                                                                                                                                                       |                                             |  |  |
|             | Machine Model        | Class M4 (+/-450V)<br>(per AEC-Q100-003)                                                                                                                                      |                                             |  |  |
| ESD         | Human Body Model     |                                                                                                                                                                               | Class H2 (+/-2500V)<br>(per AEC-Q100-002)   |  |  |
|             | Charged Device Model | Class C4 (+/-1000V)<br>(per AEC-Q100-011)                                                                                                                                     |                                             |  |  |
| IC Latch-U  | Jp Test              | Class II, Level A<br>(per AEC-Q100-004)                                                                                                                                       |                                             |  |  |
| RoHS Cor    | mpliant              |                                                                                                                                                                               | Yes                                         |  |  |

† †† Qualification standards can be found at International Rectifier's web site http://www.irf.com/

Exceptions to AEC-Q100 requirements are noted in the qualification report.

#### **Absolute Maximum Ratings**

Absolute maximum ratings indicate sustained limits beyond which damage to the device may occur. (Tj= -40°C..150°C, Vcc=6..36V unless otherwise specified).

| Symbol    | Parameter                                                            | Min. | Max. | Units |
|-----------|----------------------------------------------------------------------|------|------|-------|
| Vds       | Maximum drain to source voltage                                      | -0.3 | 36   | V     |
| Vds cont. | Maximum continuous drain to source voltage                           | -    | 28   | V     |
| Vin       | Maximum input voltage                                                | -0.3 | 6    | V     |
| Isd cont. | Max. diode continuous current (limited by thermal dissipation)       | —    | 4.5  | А     |
|           | Maximum power dissipation (internally limited by thermal protection) |      |      |       |
| Pd        | Rth=5°C/W IPS1021                                                    | _    | 25   | W     |
| Fu        | Rth=40°C/W AUIPS1021S 1" sqr. footprint                              | _    | 3.1  |       |
|           | Rth=50°C/W AUIPS1021R 1" sqr. footprint                              | _    | 2.5  |       |
| Tj max.   | Max. storage & operating temperature junction temperature            |      | 150  | °C    |

### **Thermal Characteristics**

| Symbol | Parameter                                                                            | Тур. | Max. | Units |
|--------|--------------------------------------------------------------------------------------|------|------|-------|
| Rth1   | Thermal resistance junction to ambient AUIPS1021 TO-220 free air                     | 50   | _    |       |
| Rth2   | Thermal resistance junction to case IPS1021 TO-220                                   | 2.6  | _    |       |
| Rth1   | Thermal resistance junction to ambient IPS1021S D <sup>2</sup> Pak std. footprint    | 60   | _    |       |
| Rth2   | Thermal resistance junction to ambient IPS1021S D <sup>2</sup> Pak 1" sqr. footprint | 40   | _    | °C/W  |
| Rth3   | Thermal resistance junction to case IPS1021S D <sup>2</sup> Pak                      | 2.6  | _    | 0/11  |
| Rth1   | Thermal resistance junction to ambient IPS1021R D-Pak std. footprint                 | 70   | _    |       |
| Rth2   | Thermal resistance junction to ambient IPS1021R D-Pak 1" sqr. footprint              | 50   | _    |       |
| Rth3   | Thermal resistance junction to case IPS1021R D-Pak                                   | 2.6  | _    |       |

### **Recommended Operating Conditions**

These values are given for a quick design. For operation outside these conditions, please consult the application notes.

| Symbol     | Parameter                                                           | Min. | Max. | Units |
|------------|---------------------------------------------------------------------|------|------|-------|
| VIH        | High level input voltage                                            | 4.5  | 5.5  |       |
| VIL        | Low level input voltage                                             | 0    | 0.5  |       |
| lds        | Continuous drain current, Tambient=85°C, Tj=125°C, Vin=5V           |      |      |       |
|            | Rth=5°C/W AUIPS1021                                                 | _    | 13.5 | A     |
|            | Rth=40°C/W AUIPS1021S 1" sqr. footprint                             | _    | 4.8  |       |
|            | Rth=50°C/W AUIPS1021R 1" sqr. footprint                             | -    | 4.3  |       |
| Rin        | Recommended resistor in series with IN pin to generate a diagnostic | 0.5  | 10   | kΩ    |
| Max L      | Max. recommended load inductance (including line inductance) (1)    | _    | 20   | μH    |
| Max F      | Max. frequency (switching losses = conduction losses)               | _    | 500  | Hz    |
| Max t rise | Max. input rising time                                              |      | 1    | μs    |

(1) Higher inductance is possible if maximum load current is limited - see figure 11

#### **Static Electrical Characteristics**

Tj= -40..150°C, Vcc=6..28V (unless otherwise specified), typical value are given for Tj=25°C

| Symbol    | Parameter                        | Min. | Тур. | Max. | Units  | Test Conditions  |
|-----------|----------------------------------|------|------|------|--------|------------------|
| Rds(on)   | ON state resistance Tj=25°C      | -    | 20   | 25   | mΩ     | Vin=5V. Ids=8A   |
|           | ON state resistance Tj=150°C (2) | -    | 38   | 48   | 1115.2 | VIII=3V, IUS=0A  |
| ldss1     | Drain to source leakage current  | _    | 0.1  | 2    | μA     | Vcc=14V, Tj=25°C |
| ldss2     | Drain to source leakage current  | _    | 0.2  | 4    | μΑ     | Vcc=28V, Tj=25°C |
| V clamp1  | Drain to source clamp voltage 1  | 36   | 39   |      |        | Id=20mA          |
| V clamp2  | Drain to source clamp voltage 2  | -    | 39   | 42   | V      | Id=2A            |
| Vin clamp | IN to source pin clamp voltage   | 5.5  | 6.5  | 7.5  | v      | lin=1mA          |
| Vth       | Input threshold voltage          | —    | 1.7  | —    |        | Id=10mA          |

### **Switching Electrical Characteristics**

Vcc=14V, Resistive load=1.5Ω, Rinput=0Ω, Vin=5V, Tj=25°C

| Symbol     | Parameter                  | Min. | Тур. | Max. | Units | Test Conditions |
|------------|----------------------------|------|------|------|-------|-----------------|
| Tdon       | Turn-on delay time to 20%  | 10   | 30   | 100  |       |                 |
| Tr         | Rise time 20% to 80%       | 10   | 30   | 60   |       | See figure 2    |
| Tdoff      | Turn-off delay time to 80% | 40   | 150  | 400  | μs    | See ligule 2    |
| Tf         | Fall time 80% to 20%       | 15   | 30   | 60   |       |                 |
| Eon + Eoff | Turn on and off energy     |      | 2    |      | mJ    |                 |

#### **Protection Characteristics**

Tj= -40..150°C, Vcc=6..28V (unless otherwise specified), typical value are given for Tj=25°C

| Symbol | Parameter                                                    | Min.   | Тур. | Max. | Units | Test Conditions |
|--------|--------------------------------------------------------------|--------|------|------|-------|-----------------|
| Tsd    | Over temperature threshold                                   | 150(2) | 165  | —    | °C    | See figure 1    |
| lsd    | Over current threshold                                       | 20     | 45   | 58   | Α     | See figure 1    |
| OV     | Over voltage protection ( not active when the device is ON ) | 34     | 37   | —    | V     |                 |
| Vreset | IN protection reset threshold                                | —      | 1.7  | —    | V     |                 |
| Treset | Time to reset protection                                     | 15(2)  | 50   | 200  | μs    | Vin=0V          |

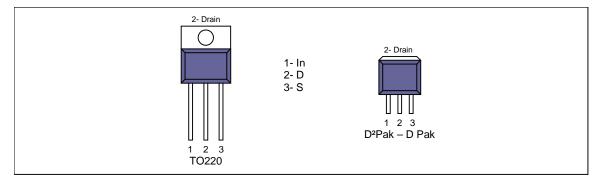
#### Diagnostic

Tj= -40..150°C, Vcc=6..28V (unless otherwise specified), typical value are given for Tj=25°C

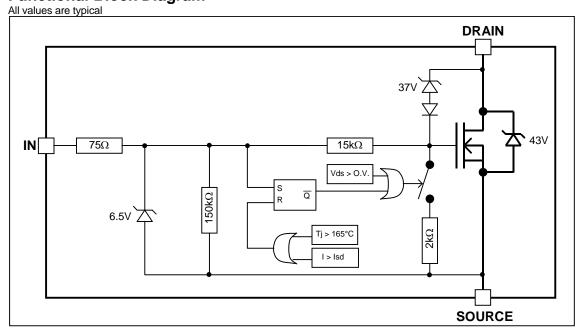
| Symbol   | Parameter                                                   | Min. | Тур. | Max. | Units | Test Conditions |
|----------|-------------------------------------------------------------|------|------|------|-------|-----------------|
| lin, on  | ON state IN positive current                                | 15   | 32   | 70   |       | Vin=5V          |
| lin, off | OFF state IN positive current<br>(after protection latched) | 150  | 230  | 350  | μA    | Vin=5V          |

(2) Guaranteed by design

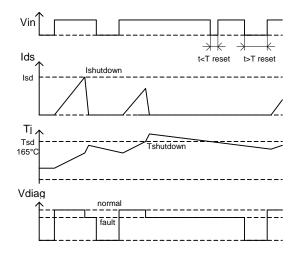
## Lead Assignments



### **Functional Block Diagram**



## AUIPS1021(S)(R)



All curves are typical values. Operating in the shaded area is not recommended.

Figure 1 – Timing diagram

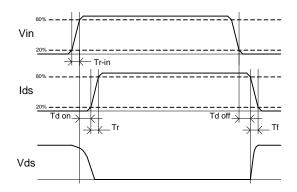


Figure 2 – IN rise time & switching definitions

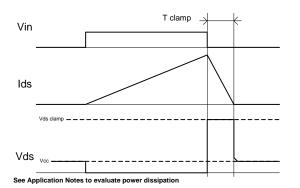


Figure 3 – Active clamp waveforms

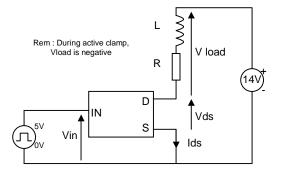


Figure 4 – Active clamp test circuit

## International **TOR** Rectifier

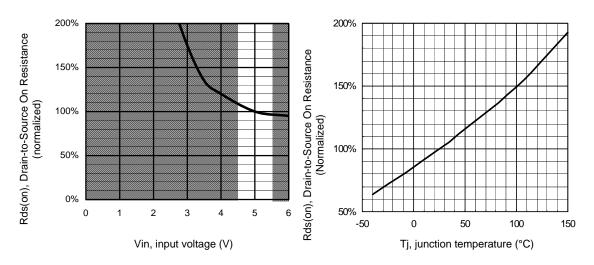
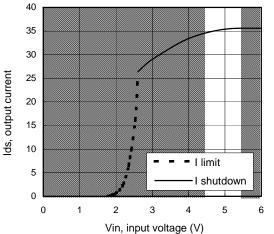


Figure 5 – Normalized Rds(on) (%) Vs Input voltage (V)



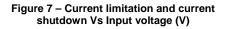
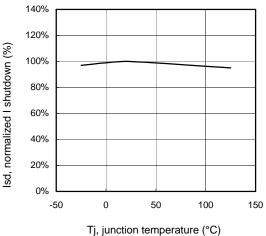
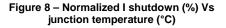
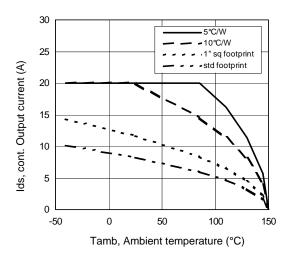


Figure 6 - Normalized Rds(on) (%) Vs Tj (°C)





## AUIPS1021(S)(R)



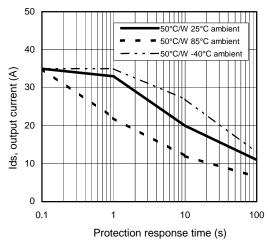
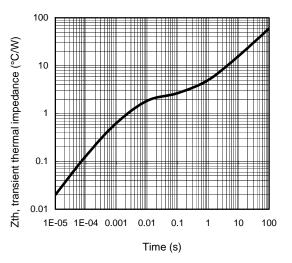


Figure 10 – Ids (A) Vs over temperature at (A) protection response time (s)



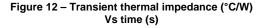
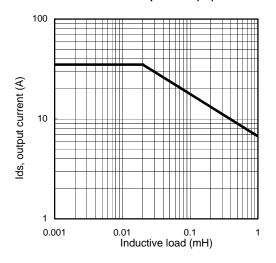
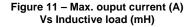
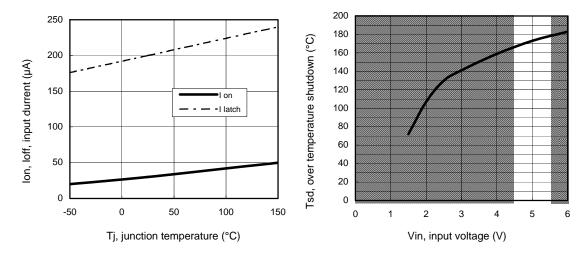


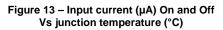
Figure 9 – Max. continuous output current (A) Vs Ambient temperature (°C)

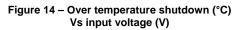


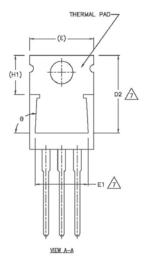


# International

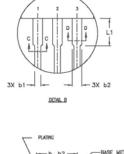




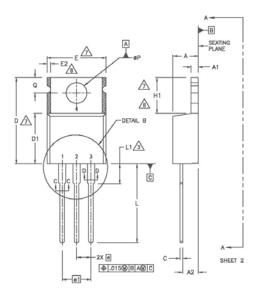




## Case Outline - TO-220 AB - Automotive Q100 PbF qualified



BASE METAL b. b2 c1 b1, b3 SECTION C-C & D-D

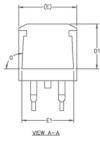


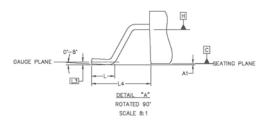
|        |        | DIMEN     | ISIONS |            |       |  |
|--------|--------|-----------|--------|------------|-------|--|
| SYMBOL | MILLIM | ETERS     | INC    | INCHES     |       |  |
|        | MIN.   | MAX.      | MIN.   | MAX.       | NOTES |  |
| A      | 3.56   | 4.82      | .140   | .190       |       |  |
| A1     | 0.51   | 1.40      | .020   | .055       |       |  |
| A2     | 2.04   | 2.92      | .080   | .115       |       |  |
| b      | 0.38   | 1.01      | .015   | .040       |       |  |
| b1     | 0.38   | 0.96      | .015   | .038       | 5     |  |
| b2     | 1.15   | 1.77      | .045   | .070       |       |  |
| b3     | 1.15   | 1.73      | .045   | .068       |       |  |
| c      | 0.36   | 0.61      | .014   | .024       |       |  |
| c1     | 0.36   | 0.56      | .014   | .022       | 5     |  |
|        |        |           |        |            |       |  |
| D      | 14.22  | 16.51     | .560   | .650       | 4     |  |
| D1     | 8.38   | 9.02      | .330   | .355       |       |  |
| D2     | 12.19  | 12.88     | .480   | .507       | 7     |  |
| E      | 9.66   | 10.66     | .380   | .420       | 4,7   |  |
| E1     | 8.38   | 8.89      | .330   | .350       | 7     |  |
| e      | 2.54   | BSC<br>08 | .100   | BSC<br>BSC | 1     |  |
| e1     | 5.     | 80        | .200   | BSC        |       |  |
| H1     | 5.85   | 6.55      | .230   | .270       | 7,8   |  |
| L      | 12.70  | 14.73     | .500   | .580       |       |  |
| L1     | -      | 6.35      | -      | .250       | 3     |  |
| øP     | 3.54   | 4.08      | .139   | .161       |       |  |
| Q      | 2.54   | 3.42      | .100   | .135       |       |  |
| ø      | 90"-   | -93       | 90*    | -93*       | 1     |  |
|        |        |           |        |            |       |  |

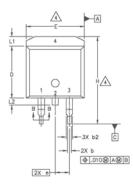
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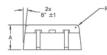
- DIMENSIONING AND TOLERANCING PER ASME Y14.5 M- 1994. 1
- 2 DIMENSIONS ARE SHOWN IN INCHES [MILLIMETERS].
- 3 LEAD DIMENSION AND FINISH UNCONTROLLED IN L1.
- DIMENSION D & E DO NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED .005" (0.127) PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTERMOST EXTREMES OF THE PLASTIC BODY. 4
- DIMENSION b1 & c1 APPLY TO BASE METAL ONLY. CONTROLLING DIMENSION : INCHES. 5
- 6
- THERMAL PAD CONTOUR OPTIONAL WITHIN DIMENSIONS E,H1,D2 & E1 7 8
- DIMENSION E2 X H1 DEFINE A ZONE WHERE STAMPING AND SINGULATION IRREGULARITIES ARE ALLOWED.
- 9 LEADS AND DRAIN ARE PLATED WITH 100% Sn

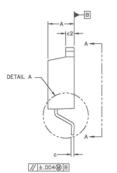
## Case Outline - D<sup>2</sup>Pak (SMD-220) - Automotive Q100 PbF MSL1 qualified



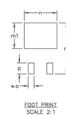












| S<br>Y |        | N<br>O<br>T |      |        |   |  |
|--------|--------|-------------|------|--------|---|--|
| B      | MILLIM | ETERS       | INC  | INCHES |   |  |
| L      | MIN.   | MAX.        | MIN. | MAX.   | Ē |  |
| A      | 4.06   | 4.83        | .160 | .190   |   |  |
| A1     | 0.00   | 0.254       | .000 | .010   |   |  |
| ь      | 0.51   | 0.99        | .020 | .039   |   |  |
| b1     | 0.51   | 0.89        | .020 | .035   | 4 |  |
| b2     | 1.14   | 1.78        | .045 | .070   |   |  |
| c      | 0.38   | 0.74        | .015 | .029   |   |  |
| c1     | 0.38   | 0.58        | .015 | .023   | 4 |  |
| c2     | 1.14   | 1.65        | .045 | .065   |   |  |
| D      | 8.51   | 9.65        | .335 | .380   | 3 |  |
| D1     | 6.86   |             | .270 |        |   |  |
| E      | 9.65   | 10.67       | .380 | .420   | 3 |  |
| E1     | 6.22   |             | .245 |        |   |  |
| e      | 2.54   | BSC         | .100 | BSC    |   |  |
| н      | 14.61  | 15.88       | .575 | .625   |   |  |
| L      | 1.78   | 2.79        | .070 | .110   |   |  |
| L1     |        | 1.65        |      | .065   |   |  |
| L2     | 1.27   | 1.78        | .050 | .070   |   |  |
| L3     | 0.25   | BSC         | .010 | BSC    |   |  |
| L4     | 4.78   | 5.28        | .188 | .208   |   |  |
| m      | 17.78  |             | .700 |        |   |  |
| m1     | 8.89   |             | .350 |        |   |  |
| n      | 11.43  |             | .450 |        |   |  |
| 0      | 2.08   |             | .082 |        |   |  |
| P      | 3.81   |             | .150 |        |   |  |
| R      | 0.51   | 0.71        | .020 | .028   |   |  |
| θ      | 90*    | 93*         | 90*  | 93.    |   |  |
|        |        |             |      |        |   |  |

NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994

2. DIMENSIONS ARE SHOWN IN MILLIMETERS [INCHES].

3. DIMENSION D & E DO NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED 0.127 [.005"] PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTMOST EXTREMES OF THE PLASTIC BODY.

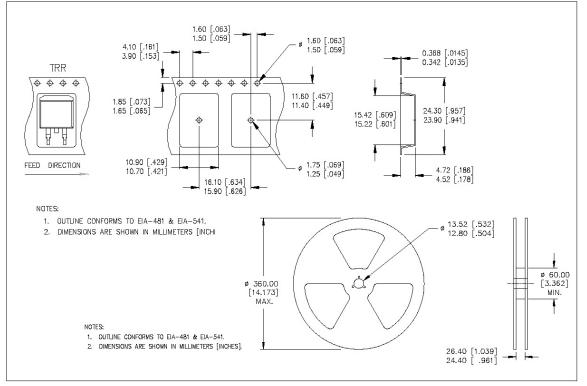
4. DIMENSION 61 AND c1 APPLY TO BASE METAL ONLY.

5. CONTROLLING DIMENSION: INCH.

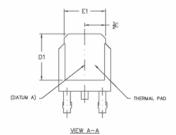
6. LEADS & DRAIN CONTACT ARE PLATED : 100% Sn

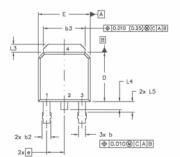
## International

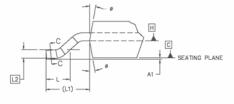
## Tape & Reel - D<sup>2</sup>Pak (SMD220)



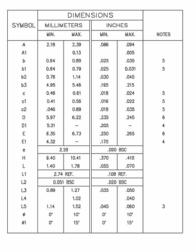
### Case Outline - D-Pak - Automotive Q100 PbF MSL1 qualified

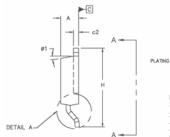












SECTION C-C

603

NOTES:

DIMENSIONING AND TOLERANCING PER ASME Y14.5 M- 1994. DIMENSIONS ARE SHOWN IN INCHES [MILLIMETERS]. LEAD DIMENSION UNCONTROLLED IN L5 1.0

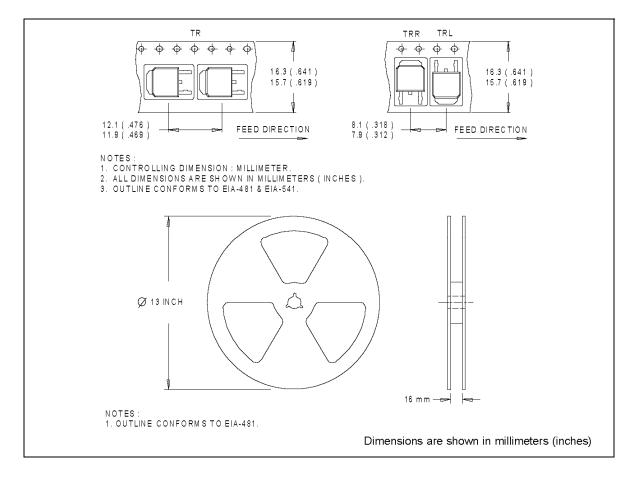
PLATING METAL

c1

- 2.0
- 3.0
- 4.0
- DIMENSION DI AND EI ESTABLISH A MINIMUM MOUNTING SURFACE FOR THERMAL PAD. SECTION C-C DIMENSIONS APPLY TO THE FLAT SECTION OF THE LEAD BETWEEN .005 [0.127] AND 5.0 .010 [0.2540 FROM THE LEAD TIP. 6.0
  - Dimension D & E DO NOT INCLUDE WOLD FLASH. WOLD FLASH SHALL NOT EXCEED .005" (0.127) PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTERMOST EXTERMES OF THE PLASTIC BODY.
- 7.0 OUTLINE CONFORMS TO JEDEC OUTLINE TO-252AA.
- 8.0 LEADS AND DRAIN ARE PLTED WITH 100% Sn

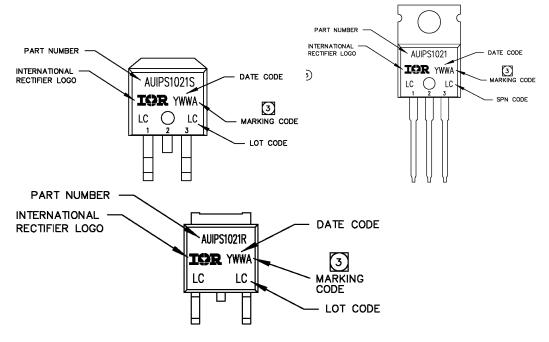
AUIPS1021(S)(R)

#### Tape & Reel - D-Pak



## AUIPS1021(S)(R)

## **Part Marking Information**



## **Ordering Information**

| Base Part Number   | Deckson Torres     | Standard Pack       | O multite Devit Neverland |                      |
|--------------------|--------------------|---------------------|---------------------------|----------------------|
| base i art itumber | Package Type       | Form                | Quantity                  | Complete Part Number |
|                    | TO220 –<br>5Leads  | Tube                | 50                        | AUIPS1021            |
|                    | D2-Pak-5-<br>Leads | Tube                | 50                        | AUIPS1021S           |
|                    |                    | Tape and reel left  | 800                       | AUIPS1021STRL        |
| AUIPS1021          |                    | Tape and reel right | 800                       | AUIPS1021STRR        |
|                    |                    | Tube                | 75                        | AUIPS1021R           |
|                    | D-Pak-5-Lead       | Tape and reel       | 2000                      | AUIPS1021RTR         |
|                    | D-Fak-J-Leau       | Tape and reel left  | 3000                      | AUIPS1021RTRL        |
|                    |                    | Tape and reel right | 3000                      | AUIPS1021RTRR        |

## International

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For technical support, please contact IR's Technical Assistance Center http://www.irf.com/technical-info/

WORLD HEADQUARTERS:

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## **Revision History**

| Revision | Date                              | Notes/Changes             |
|----------|-----------------------------------|---------------------------|
| D        | November, 24 <sup>th</sup> , 2010 | AU release                |
| D1       | December, 7th                     | Remove ESD section page 3 |
| D2       | December, 9 <sup>th</sup> 2010    | Update qual page 2        |
| E        | February, 8th 2011                | Update Vclamp page 1      |
| F        | February, 28th 2011               | Update Max rating         |
|          |                                   |                           |
|          |                                   |                           |
|          |                                   |                           |
|          |                                   |                           |