Preferred Device

General Purpose Transistors

PNP Silicon

Features

• Pb-Free Packages are Available*

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector – Emitter Voltage	V _{CEO}	40	Vdc
Collector – Base Voltage	V _{CBO}	40	Vdc
Emitter – Base Voltage	V _{EBO}	5.0	Vdc
Collector Current – Continuous	Ι _C	600	mAdc
Total Device Dissipation @ $T_A = 25^{\circ}C$ Derate above 25°C	P _D	625 5.0	mW mW/ºC
Total Device Dissipation @ $T_C = 25^{\circ}C$ Derate above 25°C	PD	1.5 12	W mW/°C
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-55 to +150	°C

THERMAL CHARACTERISTICS

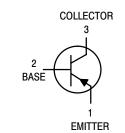
Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Ambient	R_{\thetaJA}	200	°C/W
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	83.3	°C/W

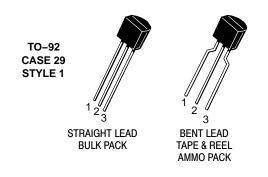
Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.



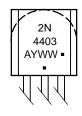
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MARKING DIAGRAM



2N4403= Device CodeA= Assembly LocationY= YearWW= Work Week•= Pb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 2 of this data sheet.

Preferred devices are recommended choices for future

*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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use and best overall value.

Publication Order Number: 2N4403/D

ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted)

Characteristic			Symbol	Min	Max	Unit
OFF CHARACTERISTICS					•	•
Collector–Emitter Breakdown Voltage (Note 1) $(I_{C} = 1.0 \text{ mAdc}, I_{B} = 0)$		1) $(I_{\rm C} = 1.0 \text{ mAdc}, I_{\rm B} = 0)$	V _{(BR)CEO}	40	-	Vdc
Collector-Base Breakdown	Voltage	$(I_{C} = 0.1 \text{ mAdc}, I_{E} = 0)$	V _{(BR)CBO}	40	-	Vdc
Emitter-Base Breakdown Vo	oltage	$(I_{E} = 0.1 \text{ mAdc}, I_{C} = 0)$	V _{(BR)EBO}	5.0	_	Vdc
Base Cutoff Current		$(V_{CE} = 35 \text{ Vdc}, V_{EB} = 0.4 \text{ Vdc})$	I _{BEV}	_	0.1	μAdc
Collector Cutoff Current (VC		(V _{CE} = 35 Vdc, V _{EB} = 0.4 Vdc)	I _{CEX}	_	0.1	μAdc
ON CHARACTERISTICS						
DC Current Gain		$ \begin{array}{l} (I_C=0.1 \text{ mAdc}, V_{CE}=1.0 \text{ Vdc}) \\ (I_C=1.0 \text{ mAdc}, V_{CE}=1.0 \text{ Vdc}) \\ (I_C=10 \text{ mAdc}, V_{CE}=1.0 \text{ Vdc}) \\ (I_C=150 \text{ mAdc}, V_{CE}=2.0 \text{ Vdc}) (\text{Note 1}) \\ (I_C=500 \text{ mAdc}, V_{CE}=2.0 \text{ Vdc}) (\text{Note 1}) \end{array} $	h _{FE}	30 60 100 100 20	_ _ 300 _	-
Collector-Emitter Saturation	Voltage (Note 1) $(I_{C} = 150 \text{ mAdc}, I_{B} = 15 \text{ mAdc})$ $(I_{C} = 500 \text{ mAdc}, I_{B} = 50 \text{ mAdc})$	V _{CE(sat)}		0.4 0.75	Vdc
$ \begin{array}{ll} \text{Base-Emitter Saturation Voltage (Note 1)} & (I_C = 150 \text{ mAdc}, I_B = 15 \text{ mAdc}) \\ (I_C = 500 \text{ mAdc}, I_B = 50 \text{ mAdc}) \end{array} $		V _{BE(sat)}	0.75 -	0.95 1.3	Vdc	
SMALL-SIGNAL CHARACT	FERISTICS					
Current-Gain - Bandwidth F	Product	$(I_{C} = 20 \text{ mAdc}, V_{CE} = 10 \text{ Vdc}, f = 100 \text{ MHz})$	f _T	200	_	MHz
Collector-Base Capacitance		(V _{CB} = 10 Vdc, I _E = 0, f = 1.0 MHz)	C _{cb}	_	8.5	pF
Emitter-Base Capacitance		$(V_{EB} = 0.5 \text{ Vdc}, I_{C} = 0, f = 1.0 \text{ MHz})$	C _{eb}	_	30	pF
Input Impedance (I _C		$(I_{C} = 1.0 \text{ mAdc}, V_{CE} = 10 \text{ Vdc}, f = 1.0 \text{ kHz})$	h _{ie}	1.5 k	15 k	Ω
Voltage Feedback Ratio $(I_C = 1)$		$(I_{C} = 1.0 \text{ mAdc}, V_{CE} = 10 \text{ Vdc}, f = 1.0 \text{ kHz})$	h _{re}	0.1	8.0	X 10-4
Small–Signal Current Gain (I		$(I_{C} = 1.0 \text{ mAdc}, V_{CE} = 10 \text{ Vdc}, f = 1.0 \text{ kHz})$	h _{fe}	60	500	-
Output Admittance		$(I_{C} = 1.0 \text{ mAdc}, V_{CE} = 10 \text{ Vdc}, f = 1.0 \text{ kHz})$	h _{oe}	1.0	100	μmhos
SWITCHING CHARACTERI	STICS					
Delay Time	$(V_{CC} = 30 \text{ Vdc}, V_{BE} = +2.0 \text{ Vdc}, I_{C} = 150 \text{ mAdc}, I_{B1} = 15 \text{ mAdc})$		t _d	_	15	ns
Rise Time			t _r	_	20	ns
Storage Time	$(V_{CC} = 30 \text{ Vdc}, I_C = 150 \text{ mAdc}, I_{B1} = 15 \text{ mA}, I_{B2} = 15 \text{ mA})$		t _s	_	225	ns
Fall Time			t _f	_	30	ns

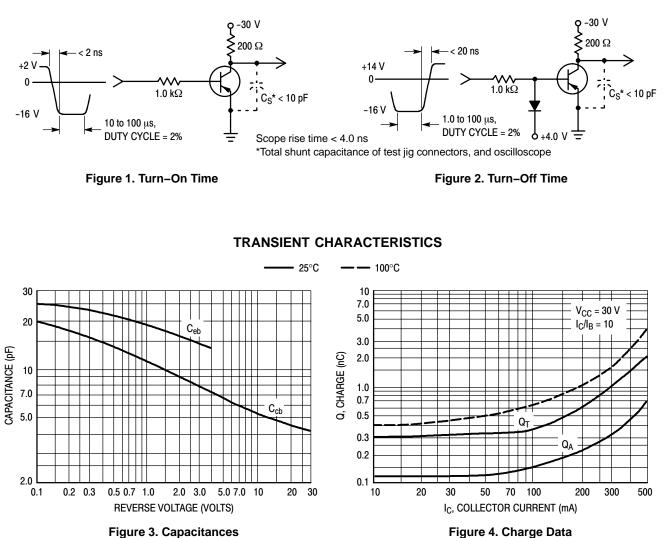
1. Pulse Test: Pulse Width \leq 300 µs, Duty Cycle \leq 2.0%.

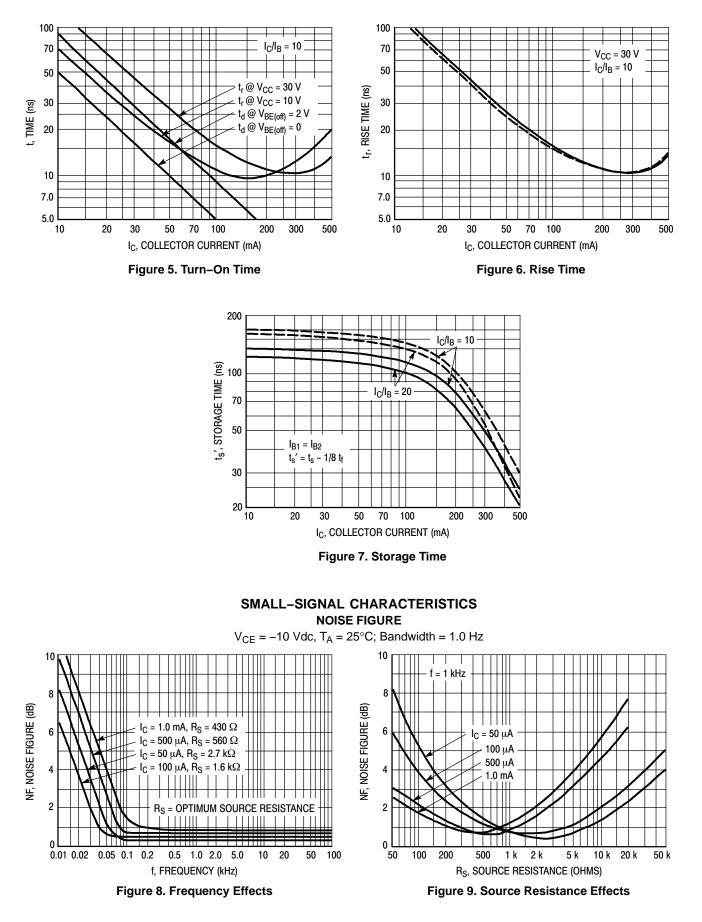
ORDERING INFORMATION

Device	Package	Shipping [†]
2N4403	TO-92	5000 Units / Bulk
2N4403G	TO-92 (Pb-Free)	5000 Units / Bulk
2N4403RLRA	TO-92	2000 / Tape & Reel
2N4403RLRAG	TO-92 (Pb-Free)	2000 / Tape & Reel
2N4403RLRM	TO-92	2000 / Ammo Pack
2N4403RLRMG	TO-92 (Pb-Free)	2000 / Ammo Pack
2N4403RLRPG	TO-92 (Pb-Free)	2000 / Ammo Pack

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

SWITCHING TIME EQUIVALENT TEST CIRCUIT





h PARAMETERS

 $V_{CE} = -10 \text{ Vdc}, \text{ f} = 1.0 \text{ kHz}, \text{ } \text{T}_{\text{A}} = 25^{\circ}\text{C}$

This group of graphs illustrates the relationship between h_{fe} and other "h" parameters for this series of transistors. To obtain these curves, a high–gain and a low–gain unit were

selected from the 2N4403 lines, and the same units were used to develop the correspondingly–numbered curves on each graph.

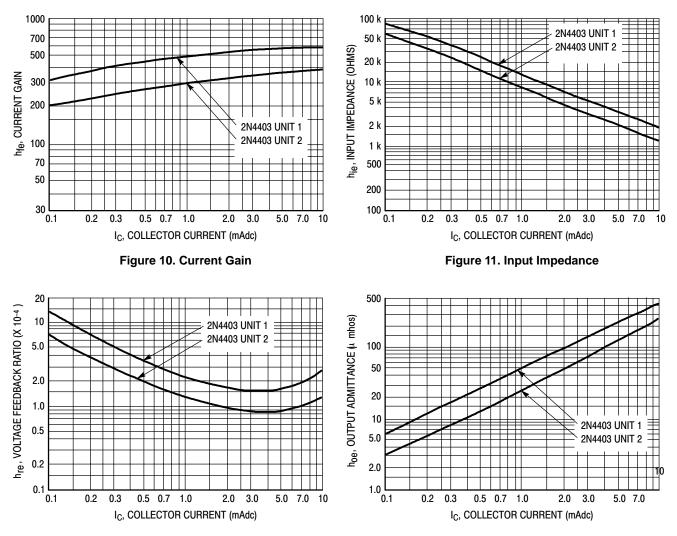
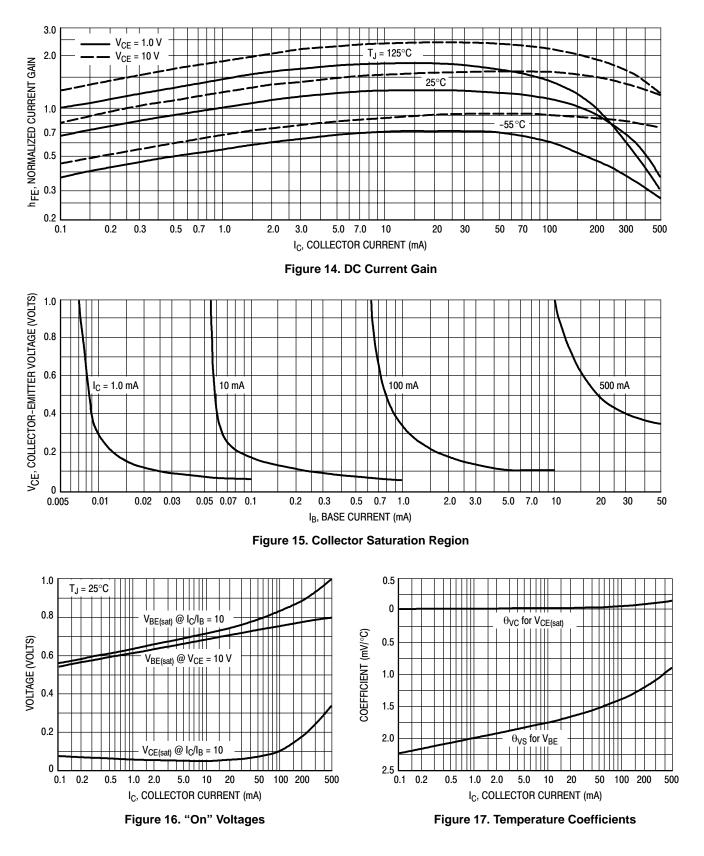


Figure 12. Voltage Feedback Ratio

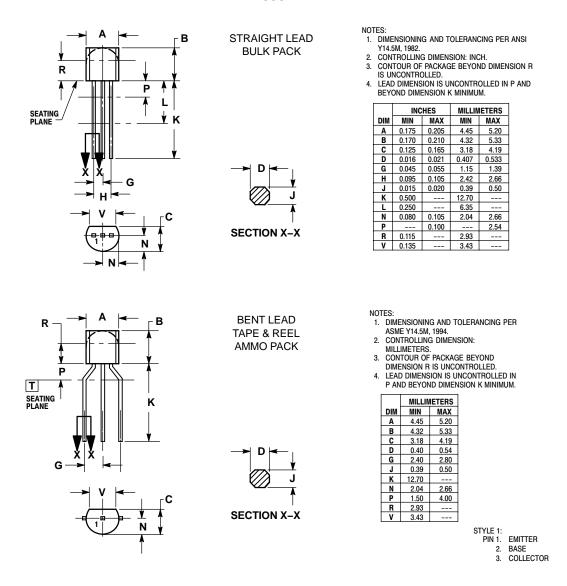
Figure 13. Output Admittance





PACKAGE DIMENSIONS

TO-92 (TO-226) CASE 29-11 ISSUE AM



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