

Fig. 2 - Forward Current Limit vs. Ambient Temperature

BASIC CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	$I_F = 250 \text{ mA}, t_p = 10 \text{ ms}$	V _F	-	1.7	2.0	V
Temperature coefficient of V _F	I _F = 1 mA	TK _{VF}	-	-1.5	-	mV/K
Reverse current	V _R = 5 V	I _R	Not designed for reverse operation			μΑ
Radiant intensity	$I_F = 250 \text{ mA}, t_p = 10 \text{ ms}$	l _e	30	55	90	mW/sr
Radiant power	$I_F = 250 \text{ mA}, t_p = 20 \text{ ms}$	φ _e	-	130	-	mW
Temperature coefficient of ϕ_{e}	I _F = 1 A	TKφ _e	-	-0.5	-	%/K
Angle of half intensity		φ	-	± 60	-	o
Peak wavelength	I _F = 250 mA	λ_{p}	-	850	-	nm
Spectral bandwidth	I _F = 250 mA	Δλ	-	30	-	nm
Temperature coefficient of λ_p	I _F = 250 mA	TKλ _p	-	0.2	-	nm/K
Rise time	I _F = 250 mA	t _r	-	8	-	ns
Fall time	I _F = 250 mA	t _f	-	10	-	ns



BASIC CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

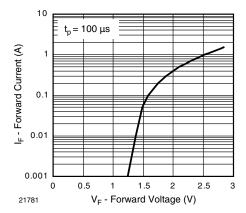


Fig. 3 - Forward Current vs. Forward Voltage

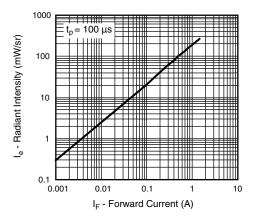


Fig. 4 - Radiant Intensity vs. Forward Current

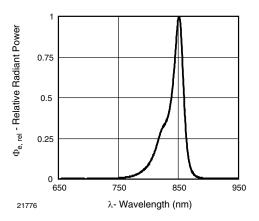


Fig. 5 - Relative Radiant Power vs. Wavelength

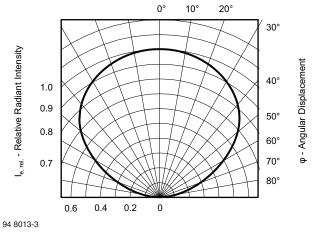
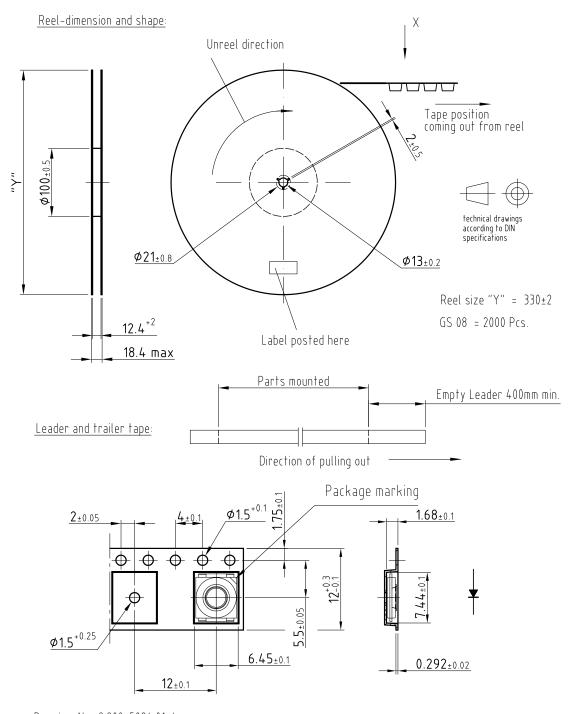


Fig. 6 - Relative Radiant Intensity vs. Angular Displacement



TAPING DIMENSIONS in millimeters



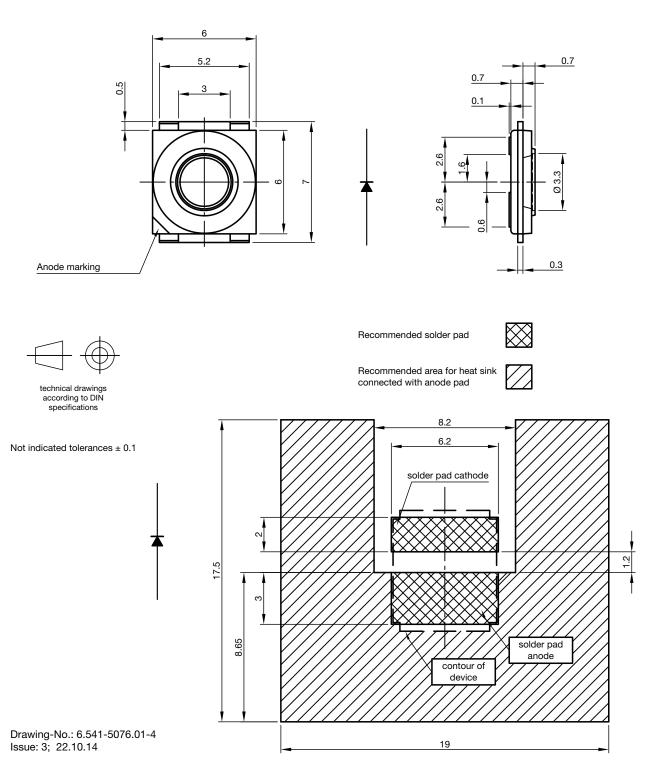
Drawing-No.: 9.800-5094.01-4

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PACKAGE DIMENSIONS in millimeters







SOLDER PROFILE

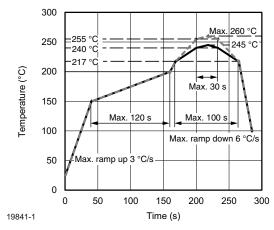


Fig. 7 - Lead (Pb)-free Reflow Solder Profile According to J-STD-020 for Preconditioning According to JEDEC $^\$$, Level 2

Vishay Semiconductors

DRYPACK

Devices are packed in moisture barrier bags (MBB) to prevent the products from moisture absorption during transportation and storage. Each bag contains a desiccant.

FLOOR LIFE

Floor life (time between soldering and removing from MBB) must not exceed the time indicated on MBB label:

Floor life: 1 year

Conditions: T_{amb} < 30 °C, RH < 60 %

Moisture sensitivity level 2, according to J-STD-020B

DRYING

In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-020 or label. Devices taped on reel dry using recommended conditions 192 h at 40 $^{\circ}$ C (+ 5 $^{\circ}$ C), RH < 5 $^{\circ}$ M.

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Vishay

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