

# Vishay Semiconductors

ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CO	VALUES	UNITS		
	V <sub>FM</sub> <sup>(1)</sup>	20 A	T.I = 25 °C	0.60	V	
Maximum forward valtage dran		40 A	1j=25 C	0.78		
Maximum forward voltage drop		20 A	T 105 %O	0.58		
		40 A	T <sub>J</sub> = 125 °C	0.75		
	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C		1	mA	
Maximum instantaneus reverse current		T <sub>J</sub> = 100 °C	Rated DC voltage	50		
		T <sub>J</sub> = 125 °C		95		
Maximum junction capacitance	CT	$V_R$ = 5 $V_{DC}$ , (test signal range 100 kHz to 1 MHz) 25 °C		900	pF	
Typical series inductance	L <sub>S</sub>	Measured from top of terminal to mounting plane		8.0	nH	
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>	10 000	V/µs		

#### Note

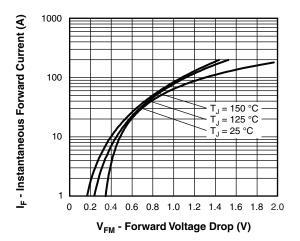
 $^{(1)}$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Maximum junction temperature range	TJ		- 65 to 150	°C		
Maximum storage temperature range	T <sub>Stg</sub>		- 65 to 175	U		
Maximum thermal resistance, junction to case per leg	R <sub>thJC</sub>	DC operation	1.5			
Typical thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, smooth and greased (Only for TO-220)	0.50	°C/W		
Maximum thermal resistance, junction to ambient	R <sub>thJA</sub>	DC operation (For D <sup>2</sup> PAK and TO-262)	50			
Approvimate weight			2	g		
Approximate weight			0.07	oz.		
Mounting torgue minimum		Non-lubricated threads	6 (5)	kgf ⋅ cm		
Mounting torque maximum		Non-Iublicated threads	12 (10)	(lbf · in)		
Marking device		Case style TO-220AB	MBR4	045CT		



# VS-MBR4045CTPbF, VS-MBR4045CT-N3

**Vishay Semiconductors** 





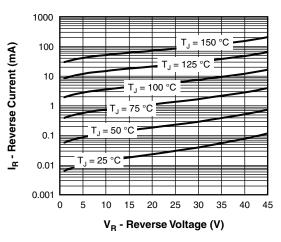


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

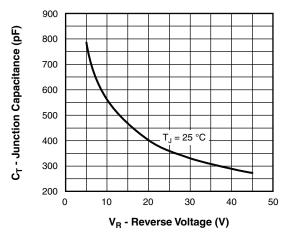
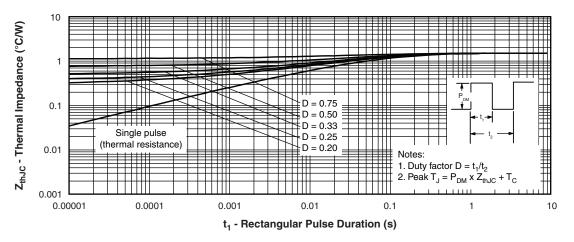


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)





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 3
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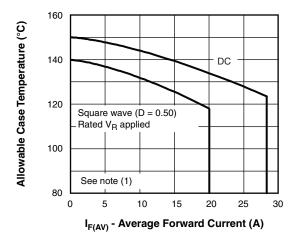
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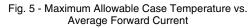
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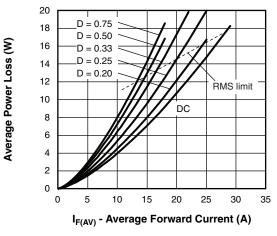


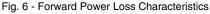
# VS-MBR4045CTPbF, VS-MBR4045CT-N3

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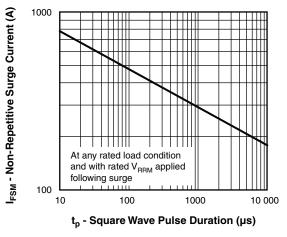


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

### Note



ode	VS-	MBR	40	45	СТ	PbF
	1	2	3	4	5	6
	1 · 2 ·		5	niconduo BR serie		oduct
	3 -	Cur	rent rati	ng (40 =	40 A)	
	4	· Volt	age rati	ng (45 =	45 V)	
	5 -	CT	= Essen	itial part	number	-
	<b>6</b>	- Env	vironmer	ntal digit		

- PbF = Lead (Pb)-free and RoHS compliant
- -N3 = Halogen-free, RoHS compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)						
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION			
VS-MBR4045CTPbF	50	1000	Antistatic plastic tube			
VS-MBR4045CT-N3	50	1000	Antistatic plastic tube			

LINKS TO RELATED DOCUMENTS					
Dimensions www.vishay.com/doc?95222					
Part marking information	TO-220AB PbF	www.vishay.com/doc?95225			
	TO-220AB -N3	www.vishay.com/doc?95028			
SPICE model		www.vishay.com/doc?95296			

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### **ORDERING INFORMATION TABLE**

www.vishay.com

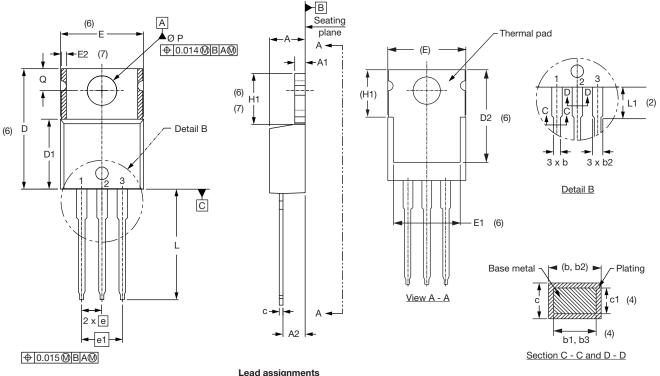
Device cod

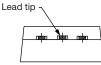


**Vishay Semiconductors** 

**TO-220AB** 

### **DIMENSIONS** in millimeters and inches





ead.	assignments

**Diodes** 

3. - Anode

1. - Anode/open 2. - Cathode

SYMBOL	MILLIN	IETERS	INCHES		NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.25	4.65	0.167	0.183	
A1	1.14	1.40	0.045	0.055	
A2	2.56	2.92	0.101	0.115	
b	0.69	1.01	0.027	0.040	
b1	0.38	0.97	0.015	0.038	4
b2	1.20	1.73	0.047	0.068	
b3	1.14	1.73	0.045	0.068	4
С	0.36	0.61	0.014	0.024	
c1	0.36	0.56	0.014	0.022	4
D	14.85	15.25	0.585	0.600	3
D1	8.38	9.02	0.330	0.355	
D2	11.68	12.88	0.460	0.507	6

#### Notes

- <sup>(1)</sup> Dimensioning and tolerancing as per ASME Y14.5M-1994
- <sup>(2)</sup> Lead dimension and finish uncontrolled in L1
- <sup>(3)</sup> Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed  $0.127 \text{ mm} (0.005^{\circ})$  per side. These dimensions are measured at the outermost extremes of the plastic body
- $^{\left( 4\right) }$  Dimension b1, b3 and c1 apply to base metal only
- (5) Controlling dimensions: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2 and E1

SYMBOL		MILLIMETERS		INC	NOTES	
		MIN.	MAX.	MIN.	MAX.	NOTES
Е		10.11	10.51	0.398	0.414	3, 6
E1		6.86	8.89	0.270	0.350	6
E2		-	0.76	-	0.030	7
е		2.41	2.67	0.095	0.105	
e1		4.88	5.28	0.192	0.208	
H1		6.09	6.48	0.240	0.255	6, 7
L		13.52	14.02	0.532	0.552	
L1		3.32	3.82	0.131	0.150	2
ØΡ	)	3.54	3.73	0.139	0.147	
Q		2.60	3.00	0.102	0.118	
θ		90° to 93°		90° t	o 93°	
θ		90° to 93°		90° t	90° to 93°	

Conforms to JEDEC outline TO-220AB

- $^{(7)}$  Dimensions E2 x H1 define a zone where stamping and singulation irregularities are allowed
- Outline conforms to JEDEC TO-220, except A2 (maximum) and (8) D2 (minimum) where dimensions are derived from the actual package outline

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For technical questions within your region, please contact one of the following: DiodesAmericas@vishay.com, DiodesAsia@vishay.com, DiodesEurope@vishay.com



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