VSC8662

Dual Port 10/100/1000BASE-T PHY and 100BASE-FX/1000BASE-X SerDes with Recovered Clock Outputs

Synchronous Ethernet

- Recovered clock output support for G.8261 and IEEE-1588 Synchronous Ethernet applications, including programmable squelch control
- Patent-pending, fast link fail indication (< 1 ms) to provide an earlier indication of a link failure to critical metro Ethernet traffic and synchronization links

Advanced SerDes

- Dual, high-performance 1.25 Gbps SerDes to maximize receive jitter tolerance and minimize transmit jitter (in comparison to single SerDes architectures)
- Supports 100BASE-FX fiber, 1000BASE-X fiber, and triple-speed copper SFPs over SerDes pins
- Advanced SerDes feature capabilities including transmitter amplitude control, receiver equalization, and link integrity status information
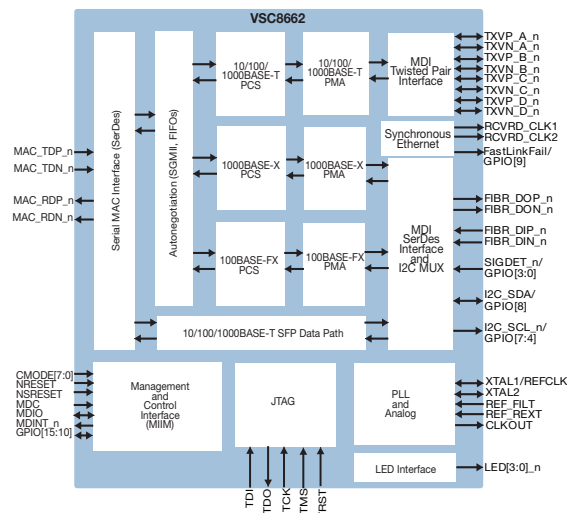
Flexibility

- Integrated quad two-wire serial multiplexer to control SFPs or PoE modules, eliminating the need for an external two-wire serial device for the control and status of SFP or PoE modules
- VeriPHY® cable diagnostics suite provides extensive network cable information such as cable length, termination status, and open/short fault location
- Extensive test features (including near end, far end, connector loopback, and Ethernet packet generator with CRC error-counter) to decrease time-to-market

Wide Range of Support

- Compliant with IEEE 802.3 (10BASE-T, 100BASE-TX, 1000BASE-T, 100BASE-FX, and 1000BASE-X) specifications
- Support for 802.3ah unidirectional transport for 100BASE-FX and 1000BASE-X fiber media
- Support for > 16 kB jumbo frames in all speeds with programmable synchronization FIFOs
- Supports Cisco SGMII v1.7 and 1000BASE-X MACs, IEEE 1149.1 JTAG boundary scan, and IEEE 1149.6 AC-JTAG

Low Power

- Low power consumption with three power savings modes
- ActiPHY™ power management system with built-in intelligence and saving modes

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