



ABSOLUTE MAXIMUM RATINGS <sup>(1)</sup>				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
INPUT				
Reverse voltage		$V_R$	6	V
Forward current		$I_F$	60	mA
Forward surge current	$t_p \leq 10 \mu s$	$I_{FSM}$	1.5	A
Power dissipation		$P_{diss}$	100	mW
Junction temperature		$T_j$	125	°C
OUTPUT				
Collector emitter voltage		$V_{CEO}$	35	V
Emitter collector voltage		$V_{ECO}$	7	V
Collector current		$I_C$	80	mA
Collector peak current	$t_p/T = 0.5, t_p \leq 10 ms$	$I_{CM}$	100	mA
Power dissipation		$P_{diss}$	150	mW
Junction temperature		$T_j$	125	°C
COUPLER				
Isolation test voltage (RMS)		$V_{ISO}$	5000	$V_{RMS}$
Total power dissipation		$P_{tot}$	250	mW
Operating ambient temperature range		$T_{amb}$	- 40 to + 100	°C
Storage temperature range		$T_{stg}$	- 55 to + 125	°C
Soldering temperature <sup>(2)</sup>	2 mm from case, $t \leq 10 s$	$T_{sld}$	260	°C

**Notes**

<sup>(1)</sup>  $T_{amb} = 25 \text{ °C}$ , unless otherwise specified.

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability.

<sup>(2)</sup> Refer to wave profile for soldering conditions for through hole devices.

ELECTRICAL CHARACTERISTICS						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT						
Forward voltage	$I_F = 20 \text{ mA}$	$V_F$		1.15	1.4	V
Junction capacitance	$V_R = 0 \text{ V}, f = 1 \text{ MHz}$	$C_j$		50		pF
OUTPUT						
Collector emitter voltage	$I_C = 1 \text{ mA}$	$V_{CEO}$	32			V
Emitter collector voltage	$I_E = 100 \mu A$	$V_{ECO}$	7			V
Collector emitter cut-off current	$V_{CE} = 10 \text{ V}, I_F = 0, E = 0$	$I_{CEO}$		15	100	nA
COUPLER						
Collector emitter saturation voltage	$I_F = 10 \text{ mA}, I_C = 5 \text{ mA}$	$V_{CEsat}$			1	V
Cut-off frequency	$V_{CE} = 5 \text{ V}, I_F = 10 \text{ mA}, R_L = 100 \Omega$	$f_c$		10		kHz
Coupling capacitance	$f = 1 \text{ MHz}$	$C_k$		0.3		pF

**Note**

$T_{amb} = 25 \text{ °C}$ , unless otherwise specified.

Minimum and maximum values are tested requirements. Typical values are characteristics of the device and are the result of engineering evaluations. Typical values are for information only and are not part of the testing requirements.

CURRENT TRANSFER RATIO							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
$I_C/I_F$	$V_{CE} = 2 \text{ V}, I_F = 1 \text{ mA}$	TCED1100	CTR	600	800		%

MAXIMUM SAFETY RATINGS						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT						
Forward current		$I_F$			130	mA
OUTPUT						
Power dissipation		$P_{diss}$			265	mW
COUPLER						
Rated impulse voltage		$V_{IOTM}$			8	kV
Safety temperature		$T_{si}$			150	°C

## Note

According to DIN EN 60747-5-5 (see figure 1). This optocoupler is suitable for safe electrical isolation only within the safety ratings. Compliance with the safety ratings shall be ensured by means of suitable protective circuits.

INSULATION RATED PARAMETERS						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Partial discharge test voltage - routine test	100 %, $t_{test} = 1$ s	$V_{pd}$	1.6			kV
Partial discharge test voltage - lot test (sample test)	$t_{Tr} = 60$ s, $t_{test} = 10$ s, (see figure 2)	$V_{IOTM}$	8			kV
		$V_{pd}$	1.3			kV
Insulation resistance	$V_{IO} = 500$ V	$R_{IO}$	$10^{12}$			$\Omega$
	$V_{IO} = 500$ V, $T_{amb} = 100$ °C	$R_{IO}$	$10^{11}$			$\Omega$
	$V_{IO} = 500$ V, $T_{amb} = 150$ °C (construction test only)	$R_{IO}$	$10^9$			$\Omega$

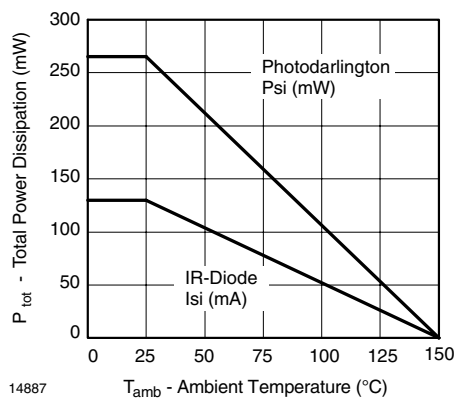


Fig. 1 - Derating Diagram

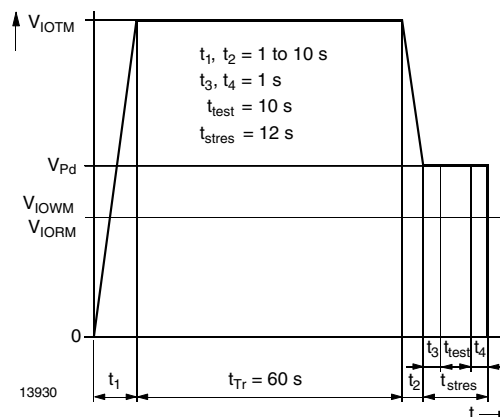


Fig. 2 - Test Pulse Diagram for Sample Test According to DIN EN 60747-5-5/DIN EN 60747-; IEC60747

SWITCHING CHARACTERISTICS						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Rise time	$V_{CC} = 2$ V, $I_C = 10$ mA, $R_L = 100$ $\Omega$ , (see figure 3)	$t_r$		300		$\mu$ s
Fall time	$V_{CC} = 2$ V, $I_C = 10$ mA, $R_L = 100$ $\Omega$ , (see figure 3)	$t_f$		250		$\mu$ s

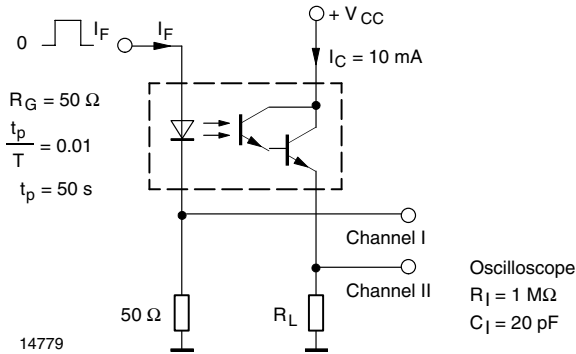


Fig. 3 - Test Circuit, Non-Saturated Operation

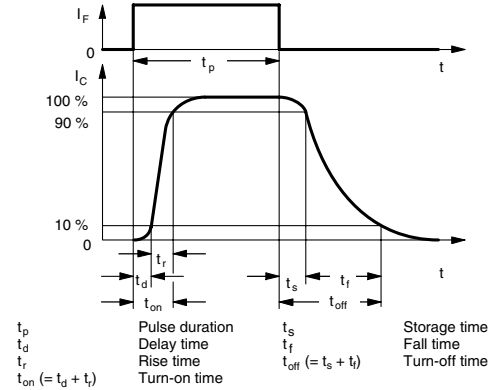


Fig. 4 - Switching Times

### TYPICAL CHARACTERISTICS

$T_{amb} = 25^\circ\text{C}$ , unless otherwise specified

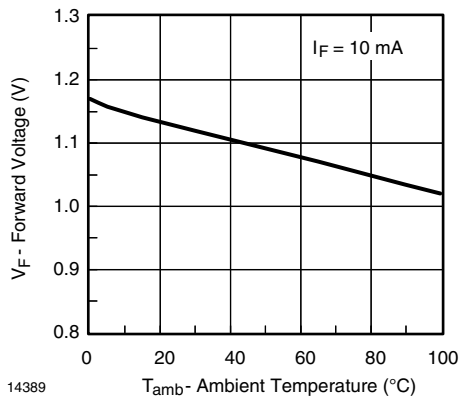


Fig. 5 - Forward Voltage vs. Ambient Temperature

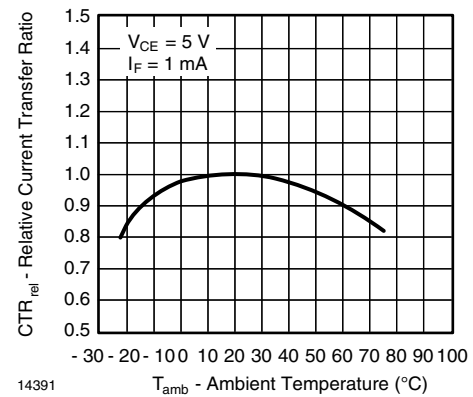


Fig. 7 - Relative Current Transfer Ratio vs. Ambient Temperature

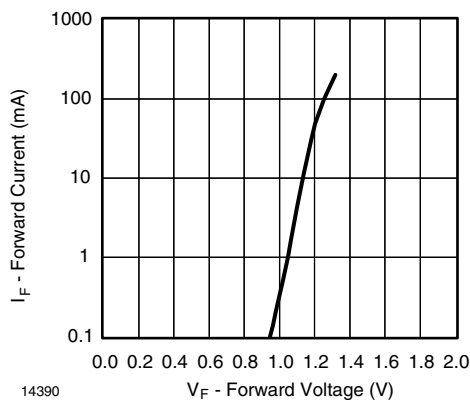


Fig. 6 - Forward Current vs. Forward Voltage

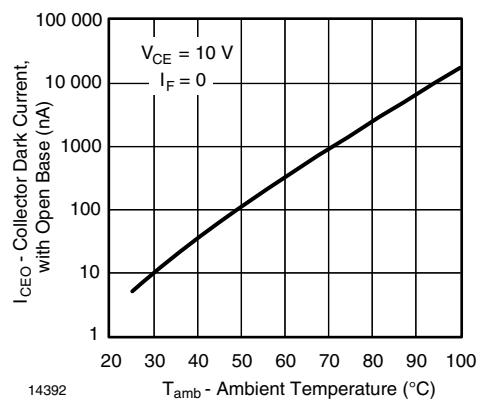


Fig. 8 - Collector Dark Current vs. Ambient Temperature

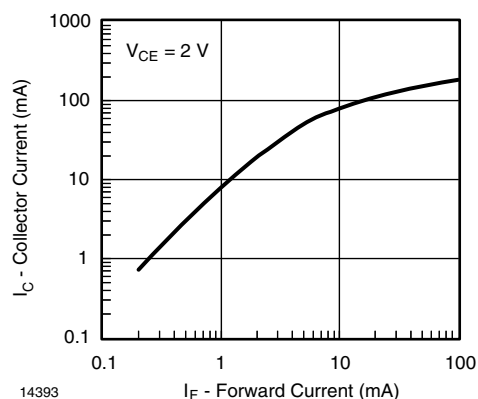


Fig. 9 - Collector Current vs. Forward Current

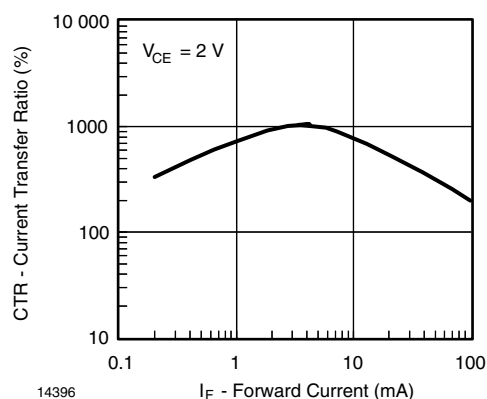


Fig. 12 - Current Transfer Ratio vs. Forward Current

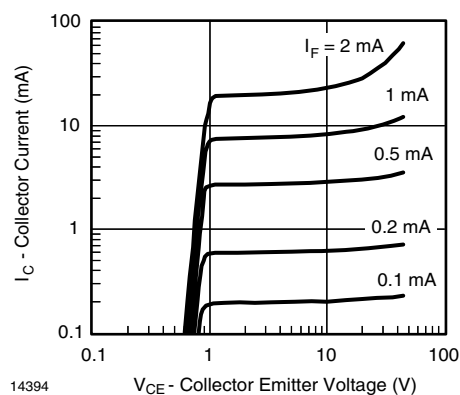


Fig. 10 - Collector Current vs. Collector Emitter Voltage

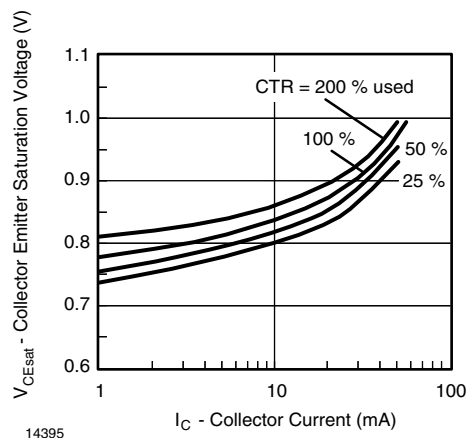
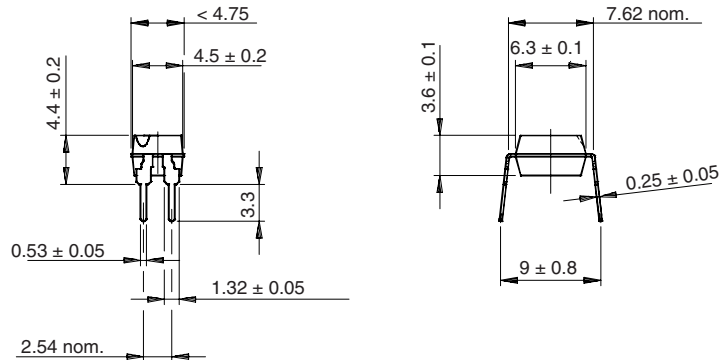
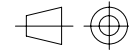
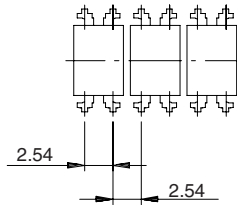
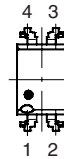


Fig. 11 - Collector Emitter Saturation Voltage vs. Collector Current

**PACKAGE DIMENSIONS** in millimeters

E. g.:  
Special features: endstackable  
to 2.54 mm (0.100") spacing

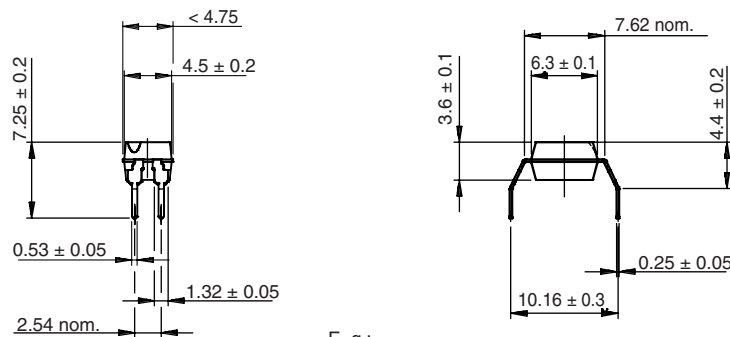
Weight: ca. 0.25 g  
Creepage distance: > 6 mm  
Air path: > 6 mm  
after mounting on PC board



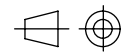
technical drawings  
according to DIN  
specifications

14789

Drawing-No.: 6.544-5302.03-4  
Issue: 5; 20.03.02



E. g.:  
Special features: endstackable  
to 2.54 mm (0.100") spacing



technical drawings  
according to DIN  
specifications

14792

Weight: ca. 0.25 g  
Creepage distance: > 8 mm  
Air path: > 8 mm  
after mounting on PC board

Drawing-No.: 6.544-5303.03-4  
Issue: 4; 20.03.02



## **OZONE DEPLETING SUBSTANCES POLICY STATEMENT**

It is the policy of Vishay Semiconductor GmbH to

1. Meet all present and future national and international statutory requirements.
2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

Vishay Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively.
2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA
3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

We reserve the right to make changes to improve technical design  
and may do so without further notice.

Parameters can vary in different applications. All operating parameters must be validated for each customer application by the customer. Should the buyer use Vishay Semiconductors products for any unintended or unauthorized application, the buyer shall indemnify Vishay Semiconductors against all claims, costs, damages, and expenses, arising out of, directly or indirectly, any claim of personal damage, injury or death associated with such unintended or unauthorized use.

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