TOSHIBA

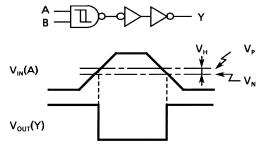
IEC Logic Symbol

卫&	(<u>3)</u> 1Y
	<u>(6)</u> 2Y
	(<u>8)</u> 3Y
	<u>(11)</u> 4Y
	1.8

Truth Table

А	В	Y
L	L	Н
L	Н	Н
Н	L	Н
Н	Н	L

System Diagram, Waveform



Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit
Supply voltage range	V _{CC}	–0.5 to 7	V
DC input voltage	V _{IN}	-0.5 to V _{CC} + 0.5	V
DC output voltage	V _{OUT}	-0.5 to V _{CC} + 0.5	V
Input diode current	I _{IK}	±20	mA
Output diode current	IOK	±20	mA
DC output current	IOUT	±25	mA
DC V _{CC} /ground current	ICC	±50	mA
Power dissipation	PD	500 (DIP) (Note 2)/180 (SOP)	mW
Storage temperature	T _{stg}	–65 to 150	°C

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: 500 mW in the range of Ta = -40 to 65°C. From Ta = 65 to 85°C a derating factor of -10 mW/°C shall be applied until 300 mW.

Operating Ranges (Note)

Characteristics	Characteristics Symbol Rating		Unit
Supply voltage	V _{CC}	2 to 6	V
Input voltage	V _{IN}	0 to V _{CC}	V
Output voltage	V _{OUT}	0 to V _{CC}	V
Operating temperature	T _{opr}	-40 to 85	°C

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either V_{CC} or GND.

Electrical Characteristics

DC Characteristics

Characteristics	Characteristics Symbol		Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit
	,			V _{CC} (V)	Min	Тур.	Max	Min	Max	
				2.0	1.0	1.25	1.50	1.0	1.50	
Positive threshold voltage	VP		—	4.5	2.3	2.70	3.15	2.3	3.15	V
J. J				6.0	3.0	3.50	4.20	3.0	4.20	
				2.0	0.30	0.65	0.9	0.30	0.9	
Negative threshold voltage	V _N		—	4.5	1.13	1.60	2.0	1.13	2.0	V
				6.0	1.50	2.30	2.6	1.50	2.6	
				2.0	0.3	0.6	1.0	0.3	1.0	
Hysteresis output V _H	V _H		—	4.5	0.6	1.1	1.4	0.6	1.4	V
				6.0	0.8	1.2	1.7	0.8	1.7	
		V _{IN} = V _{IH} or V _{IL}		2.0	1.9	2.0	—	1.9	—	
			$I_{OH} = -20 \ \mu A$	4.5	4.4	4.5	—	4.4	—	
High-level output voltage	V _{OH}			6.0	5.9	6.0	_	5.9		V
Ū.			I _{OH} = -4 mA	4.5	4.18	4.31	—	4.13	—	
			$I_{OH} = -5.2 \text{ mA}$	6.0	5.68	5.80	_	5.63	_	
				2.0	—	0.0	0.1	—	0.1	
			$I_{OL} = 20 \ \mu A$	4.5	—	0.0	0.1	—	0.1	
Low-level output voltage	V _{OL}	V _{IN} = V _{IH} or V _{IL}		6.0	_	0.0	0.1	_	0.1	V
			$I_{OL} = 4 \text{ mA}$	4.5	—	0.17	0.26	—	0.33	
			$I_{OL} = 5.2 \text{ mA}$	6.0	_	0.18	0.26	_	0.33	
Input leakage current	IIN	V _{IN} = V _{CC} or GND		6.0	_	_	±0.1		±1.0	μA
Quiescent supply current	ICC	V _{IN} = V _{CC} or	$V_{IN} = V_{CC}$ or GND		_	_	1.0	_	10.0	μA

AC Characteristics (C_L = 15 pF, V_{CC} = 5 V, Ta = 25°C, input: $t_r = t_f = 6$ ns)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Output transition time	t _{TLH}	—	_	4	8	ns
	t _{THL}					
Propagation delay time	t _{pLH}			11	18	ns
	t _{pHL}	—			10	113

AC Characteristics ($C_L = 50 \text{ pF}$, input: $t_r = t_f = 6 \text{ ns}$)

Characteristics Symbo		Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit
	 ,		$V_{CC}(V)$	Min	Тур.	Max	Min	Max	
	+		2.0	_	30	75	_	95	
Output transition time	t _{TLH}	—	4.5	—	8	15	_	19	ns
t _{THL}		6.0	—	7	13	_	16		
	4		2.0	_	42	110		140	
Propagation delay time	t _{pLH}	_	4.5	_	14	22		28	ns
	t _{pHL}		6.0	—	12	19	—	24	
Input capacitance	C _{IN}	_		_	5	10	_	10	pF
Power dissipation capacitance	C _{PD} (Note)	_			29		—	—	pF

Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

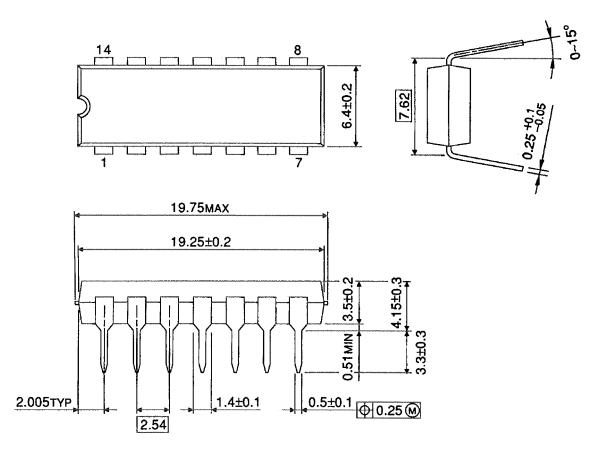
Average operating current can be obtained by the equation:

 I_{CC} (opr) = $C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/4$ (per gate)

Package Dimensions

DIP14-P-300-2.54

Unit : mm



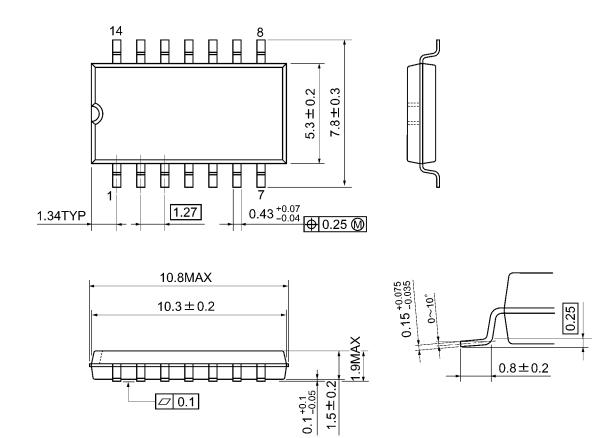
Weight: 0.96 g (typ.)



Package Dimensions

SOP14-P-300-1.27A

Unit: mm



Weight: 0.18 g (typ.)

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