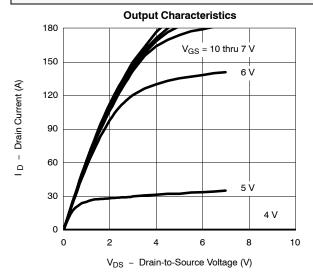


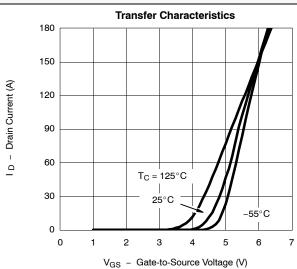
Parameter	Symbol	Test Condition	Min	Тур	Max	Unit
Static	<u></u>				1	1
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	$V_{DS} = 0 \text{ V}, I_{D} = 250 \mu\text{A}$	150			v
Gate-Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \mu A$	2		4	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS}$ = 0 V, $V_{GS}$ = $\pm$ 20 V			± 100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 150 V, V <sub>GS</sub> = 0 V			1	μΑ
		V <sub>DS</sub> = 150 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 125°C			50	
		$V_{DS} = 150 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 175 ^{\circ}\text{C}$			250	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	120			Α
Drain-Source On-State Resistance <sup>a</sup>	r <sub>DS(on)</sub>	$V_{GS} = 10 \text{ V}, I_D = 30 \text{ A}$		0.015	0.019	Ω
		$V_{GS}$ = 10 V, $I_D$ = 30 A, $T_J$ = 125°C			0.038	
		$V_{GS}$ = 10 V, $I_D$ = 30 A, $T_J$ = 175°C			0.050	
Forward Transconductancea	9fs	$V_{DS} = 15 \text{ V}, I_D = 30 \text{ A}$	25			S
Dynamic <sup>b</sup>					•	
Input Capacitance	C <sub>iss</sub>	$V_{GS} = 0 \text{ V}, V_{DS} = 25 \text{ V}, f = 1 \text{ MHz}$		4750		pF
Output Capacitance	C <sub>oss</sub>			530		
Reverse Transfer Capacitance	C <sub>rss</sub>			220		
Total Gate Charge <sup>c</sup>	Qg	$V_{DS} = 75 \text{ V}, \ V_{GS} = 10 \text{ V}, \ I_D = 85 \text{ A}$		76	110	nC
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>			21		
Gate-Drain Charge <sup>c</sup>	Q <sub>gd</sub>			26		
Gate Resistance	R <sub>g</sub>		0.5	1.8	3.0	Ω
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>	$V_{DD}$ = 75 V, $R_L$ = 0.9 $\Omega$ $I_D$ $\cong$ 85 A, $V_{GEN}$ = 10 V, $R_g$ = 2.5 $\Omega$		22	35	- ns
Rise Time <sup>c</sup>	t <sub>r</sub>			170	250	
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>			40	60	
Fall Time <sup>c</sup>	t <sub>f</sub>			170	250	
Source-Drain Diode Ratings an	d Characteristics	s (T <sub>C</sub> = 25°C) <sup>b</sup>	- 1			
Continuous Current	Is			85		
Pulsed Current	I <sub>SM</sub>				180	A
Forward Voltage <sup>a</sup>	V <sub>SD</sub>	I <sub>F</sub> = 85 A, V <sub>GS</sub> = 0 V		1.0	1.5	V
Reverse Recovery Time	t <sub>rr</sub>			130	200	ns
Peak Reverse Recovery Current	I <sub>RM(REC)</sub>	$I_F = 50 \text{ A}, \text{ di/dt} = 100 \text{ A/}\mu\text{s}$		8	12	Α
Reverse Recovery Charge	Q <sub>rr</sub>			0.52	1.2	μC

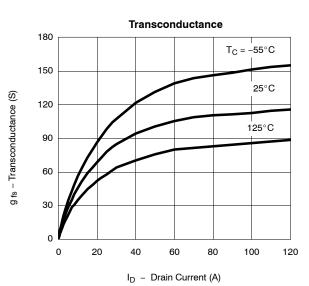
- Notes a. Pulse test; pulse width  $\leq 300~\mu s$ , duty cycle  $\leq 2\%$ . b. Guaranteed by design, not subject to production testing. c. Independent of operating temperature.

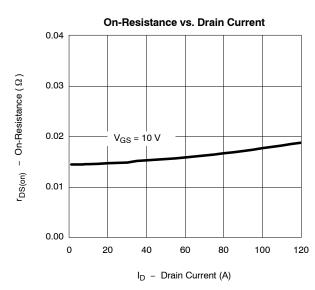


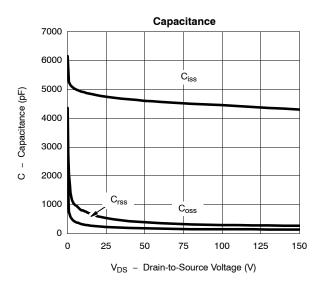
## TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)

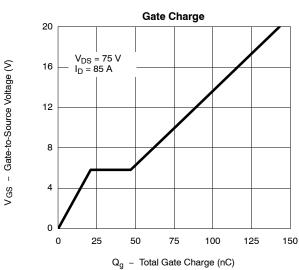






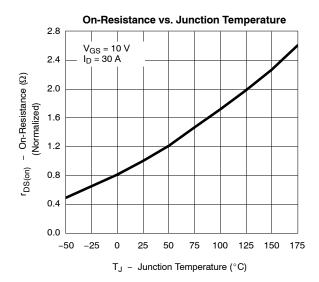


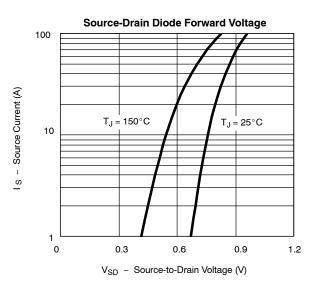


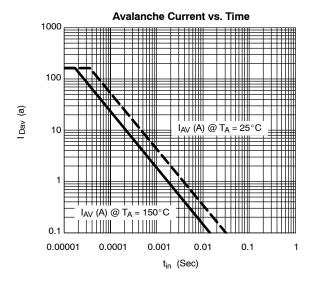


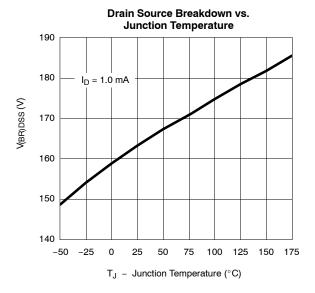


## TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)



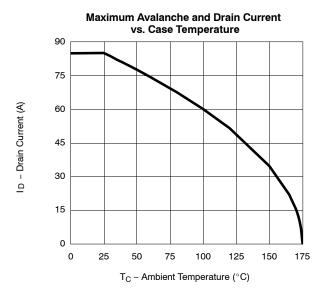


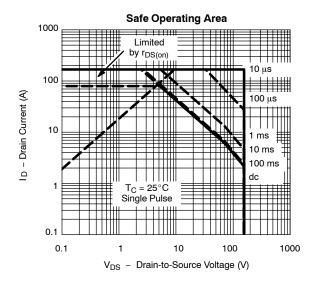


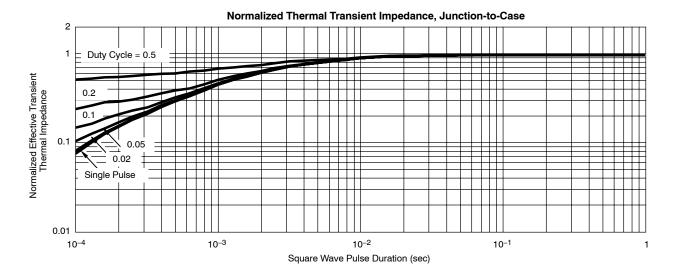




## THERMAL RATINGS









Vishay

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