Characteristics STPS640C-Y

1 Characteristics

Table 2. Absolute ratings (limiting values, per diode)

Symbol	Parameter			Unit	
V_{RRM}	Repetitive peak reverse voltage		40	V	
I _{F(RMS)}	Forward rms current		6	Α	
1	per diode		3	Α	
I _{F(AV)}	Average forward current, δ = 0.5, T_c = 135 °C	per device	6		
I _{FSM}	Surge non repetitive forward current $t_p = 10 \text{ ms sinusoidal}, T_c = 25 \text{ °C}$			Α	
I _{RRM}	Peak repetitive reverse current $t_p = 2 \mu s$, $F = 1 kHz$			Α	
P _{ARM}	Repetitive peak avalanche power $t_p = 1 \mu s$, $T_c = 25 °C$			W	
T _{stg}	Storage temperature range			°C	
T _j	Operating junction temperature			°C	

Table 3. Thermal parameters

Symbol	Parameter	Value	Unit
P	Junction to case per diode	5.5	
R _{th(j-c)}	per device	3	°C/W
R _{th(c)}	coupling	0.5	

When the diodes 1 and 2 are used simultaneously : $\Delta Tj(\mbox{diode 1})$ = P(diode1) x R_{th(j-c)}(Per diode) + P(diode 2) x R_{th(c)}

Table 4. Static electrical characteristics (per diode)

Symbol	Parameter	Test conditions		Min.	Тур	Max.	Unit
ı (1)	I _R ⁽¹⁾ Reverse leakage current	T _j = 25 °C	$V_R = V_{RRM}$	-	-	100	μΑ
'R`		T _j = 125 °C		-	2	10	mA
	Forward voltage drop	T _j = 25 °C	I _F = 3 A	-	-	0.63	. V
V _F ⁽²⁾		T _j = 125 °C		-	0.50	0.57	
VF`		T _j = 25 °C	I _F = 6 A	-	-	0.84	
		T _j = 125 °C		-	0.67	0.72	

^{1.} Pulse test: $t_p = 5$ ms, $\delta < 2\%$

To evaluate the conduction losses use the following equation:

$$P = 0.42 \text{ x } I_{F(AV)} + 0.050 \text{ x } I_{F}^{2}_{(RMS)}$$

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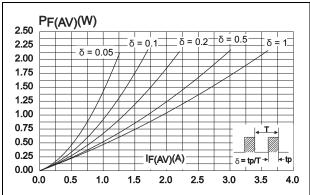
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^{2.} Pulse test: t_p = 380 μ s, δ < 2%

STPS640C-Y Characteristics

Figure 1. Average forward power dissipation versus average forward current (per diode)

Figure 2. Average forward current versus ambient temperature ($\delta = 0.5$, per diode)



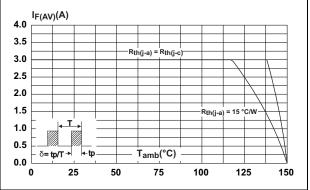
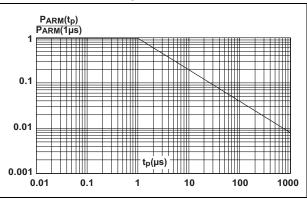


Figure 3. Normalized avalanche power derating versus pulse duration versus junction temperature



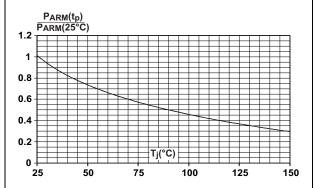
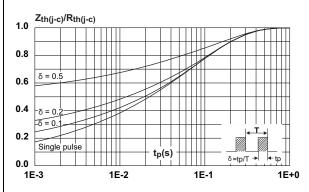
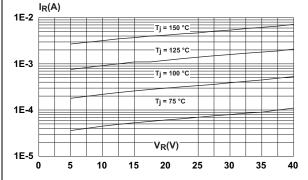


Figure 5. Relative variation of thermal impedance junction to case versus pulse duration

Figure 6. Reverse leakage current vs. reverse voltage applied (typical values, per diode)

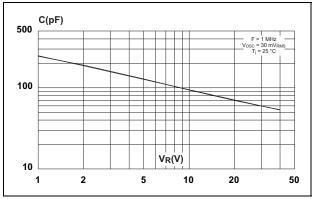




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Figure 7. Junction capacitance vs. reverse voltage applied (typical values, per diode)

Figure 8. Forward voltage drop vs. forward current (per diode)



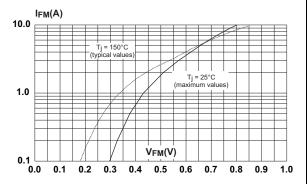
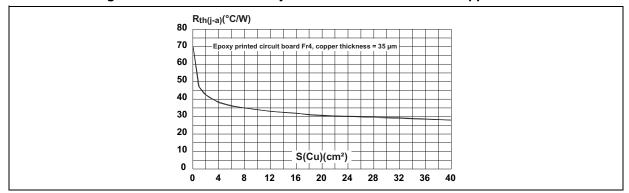


Figure 9. Thermal resistance junction to ambient versus copper surface under tab



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STPS640C-Y Package information

2 Package information

- Epoxy meets UL94,V0
- Lead-free packages

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK[®] packages, depending on their level of environmental compliance. ECOPACK[®] specifications, grade definitions and product status are available at: www.st.com. ECOPACK[®] is an ST trademark.

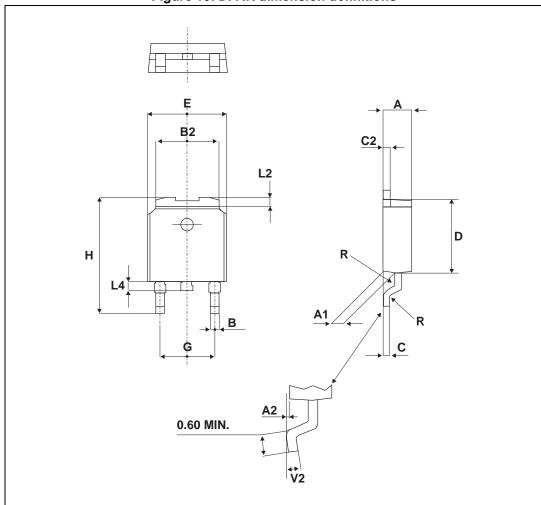


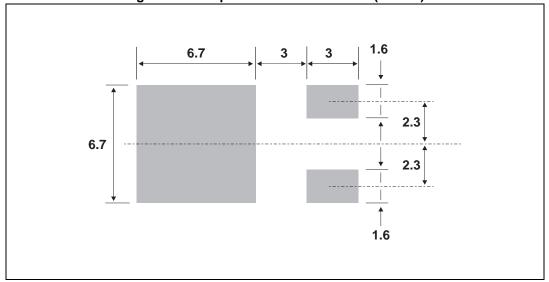
Figure 10. DPAK dimension definitions

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Table 5. DPAK dimension values

			Dimens	ions			
Ref.	Millimeters			Inches			
	Min.	Тур.	Max.	Min.	Тур.	Max.	
Α	2.20		2.40	0.086		0.094	
A1	0.90		1.10	0.035		0.043	
A2	0.03		0.23	0.001		0.009	
В	0.64		0.90	0.025		0.035	
B2	5.20		5.40	0.204		0.212	
С	0.45		0.60	0.017		0.023	
C2	0.48		0.60	0.018		0.023	
D	6.00		6.20	0.236		0.244	
E	6.40		6.60	0.251		0.259	
G	4.40		4.60	0.173		0.181	
Н	9.35		10.10	0.368		0.397	
L2		0.80 typ.			0.031 typ.		
L4	0.60		1.00	0.023		0.039	
V2	0°		8°	0°		8°	

Figure 11. Footprint dimensions in mm (inches)



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3 Ordering information

Table 6. Ordering information

Order code	Marking	Package	Weight	Base qty	Delivery mode
STPS640CBY-TR	STPS640CY	DPAK	0.3 g	2500	Tape and reel

4 Revision history

Table 7. Revision history

Date	Revision	Changes
6-Nov-2013	1	First issue
04-Dec-2013	2	Properties changed from preliminary data to production data.



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