

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM}	50	V
Average Rectified Output Current	lo	10	A
Non-Repetitive Peak Forward Surge Current 8.3ms	I _{FSM}	300	А

Thermal Characteristics

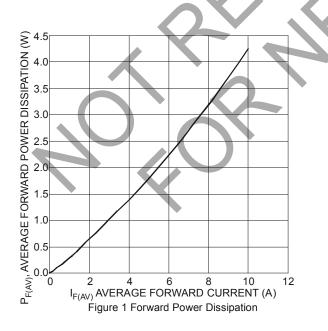
Characteristic	Symbol	Value	Unit
Typical Thermal Resistance Junction to Ambient (Note 6)	R _{0JA}	18	°C/W
Typical Thermal Resistance Junction to Case (Note 6)	R _{0JC}	2	°C/W
Typical Thermal Resistance Junction to Lead (Notes 6 and 7)	R _{θJL}	4	°C/W
Operating and Storage Temperature Range	T _{J,} T _{STG}	-55 to +150	°C

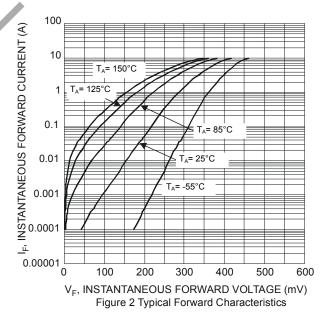
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Forward Voltage Drop	V _F		0.31 0.42 0.36	— 0.47 0.41	V	I _F = 5A, T _J = +85°C I _F = 10A, T _J = +25°C I _F = 10A, T _J = +125°C
Leakage Current (Note 8)	IR		0.06 2 15	0.15 12 50	mA	V _R = 50V, T _J = +25°C V _R = 50V, T _J = +85°C V _R = 50V, T _J = +125°C

Notes: 6. Device mounted on FR4 PCB with 1inch copper pad layout with AL substrate and additional HK1 (37mm x 55mm x15mm).

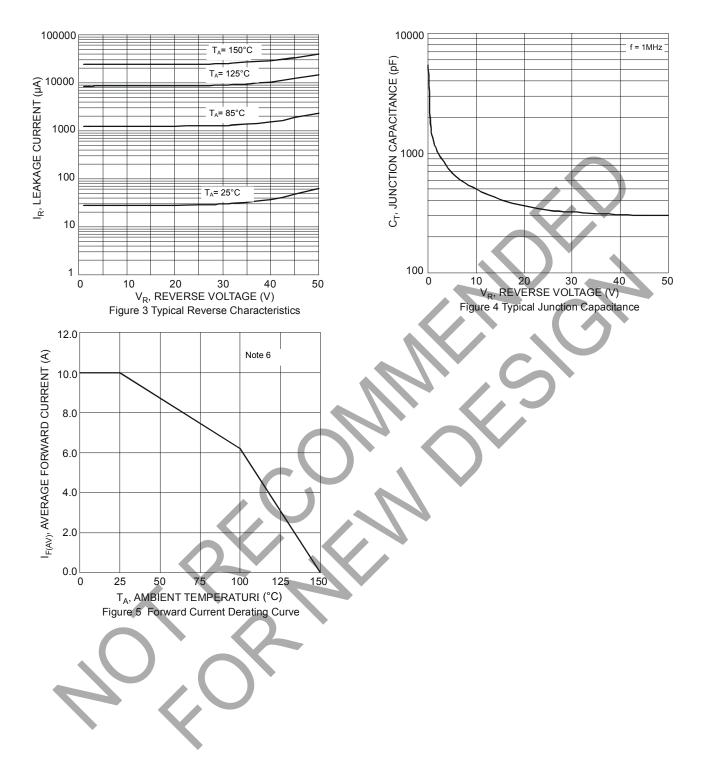
- 7. Junction to Lead (Cathode Terminal).
- 8. Short duration pulse test used to minimize self-heating effect.







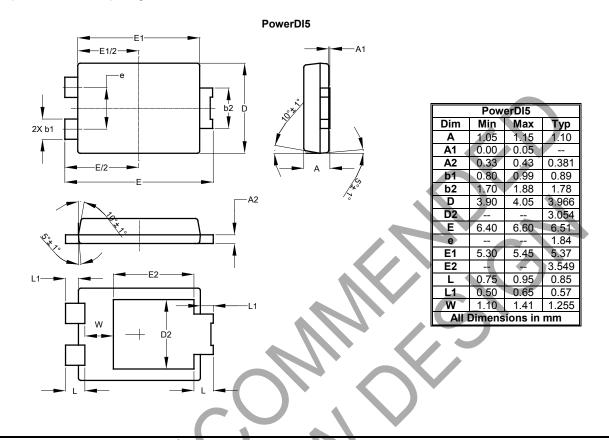
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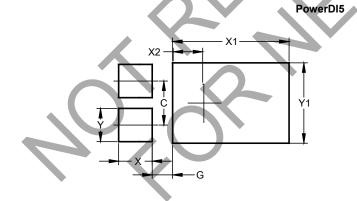
Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.



Suggested Pad Layout

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Dimensions	Value (in mm)
С	1.840
G	0.852
Х	1.400
X1	4.860
X2	1.310
Y	1.390
Y1	3.360



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