Maximum Ratings @T_A = 25°C unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load.
For capacitance load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V_{RRM} V_{RWM} V_{RM}	400	V
Average Rectified Output Current (See Figure 1)	I_O	6	A
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I_{FSM}	220	A

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Maximum Thermal Resistance Junction to Ambient (Note 3)	$R_{\Theta JA}$	30	°C/W
Maximum Thermal Resistance Junction to Case (Note 4)	$R_{\Theta JC}$	2.5	°C/W
Operating and Storage Temperature Range	T_J, T_{STG}	-65 to +175	ºC

Electrical Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Forward Voltage Drop	V_F	- - -	0.83 0.85 0.72	- 0.91 0.77	V	I_F = 5A, T_J = 25ºC I_F = 6A, T_J = 25ºC I_F = 6A, T_J = 125ºC
Leakage Current (Note 5)	I_R	- -	0.8 -	50 5	µA mA	V_R = 400V, T_J = 25 ºC V_R = 400V, T_J = 125 ºC
Reverse Recovery Time	t_{rr}	-	120	-	nS	I_F = 0.5A, I_R = 1.0A, I_{RR} = 0.25A
Junction Capacitance	C_J	-	100	-	pF	V_R = 4.0V, f = 1MHz

Notes: 3. FR-4 PCB, 2 oz. Copper, minimum recommended pad layout per http://www.diodes.com.
4. Device mounted on Polymide substrate PC board, 16*MRP layout http://www.diodes.com.
5. Short duration pulse test used to minimize self-heating effect.

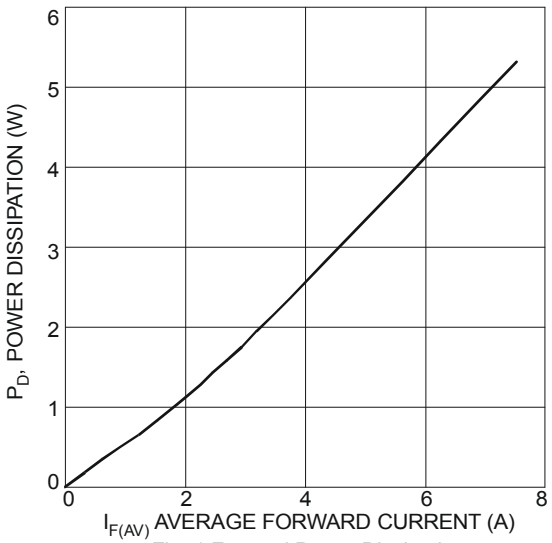


Fig. 1 Forward Power Dissipation

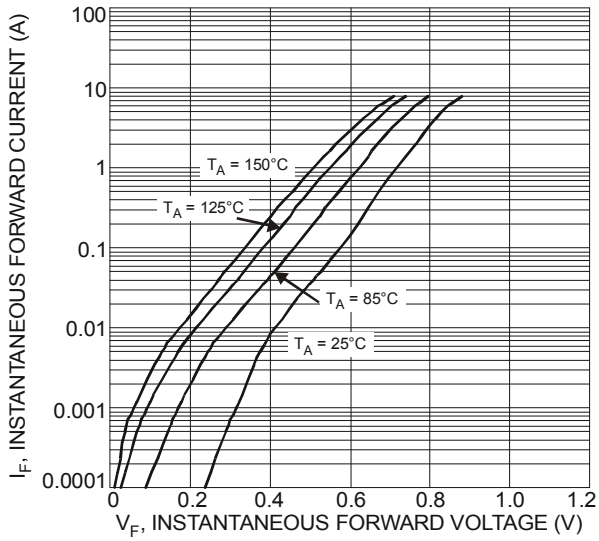


Fig. 2 Typical Forward Characteristics

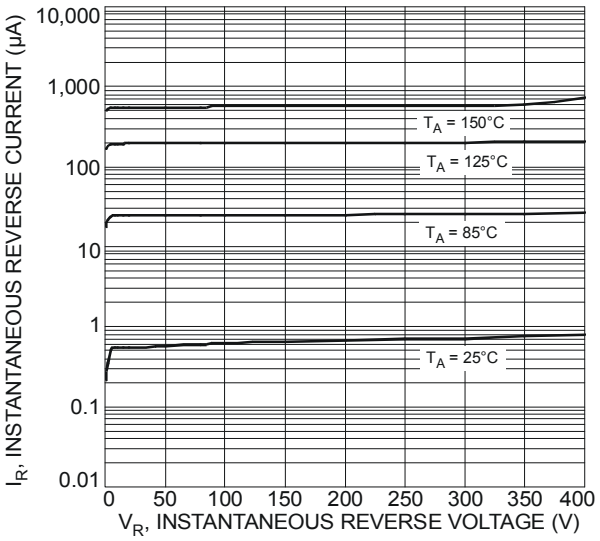


Fig. 3 Typical Reverse Characteristics

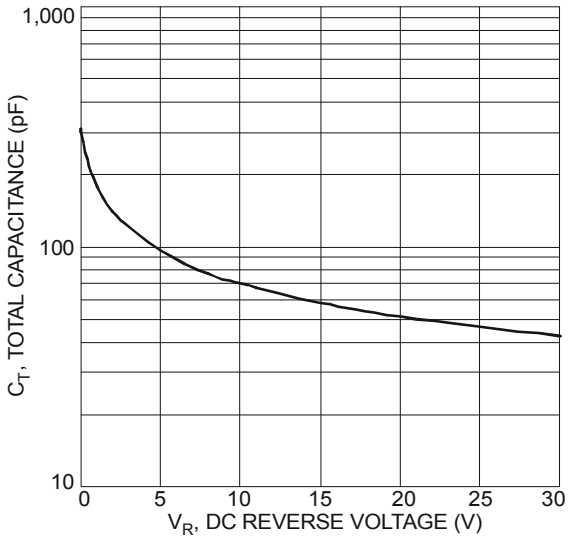


Fig. 4 Total Capacitance vs. Reverse Voltage

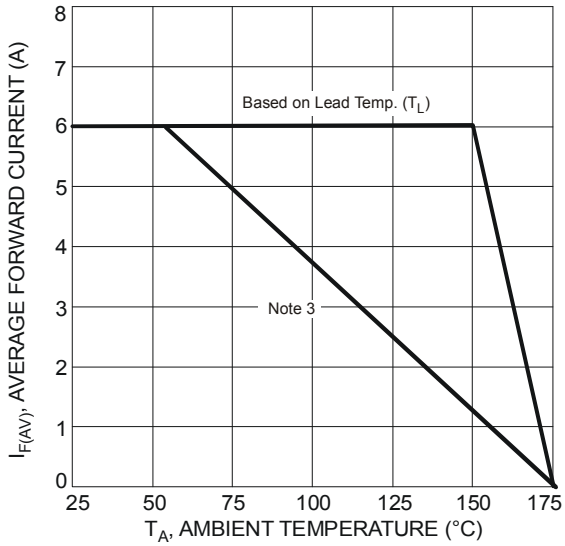
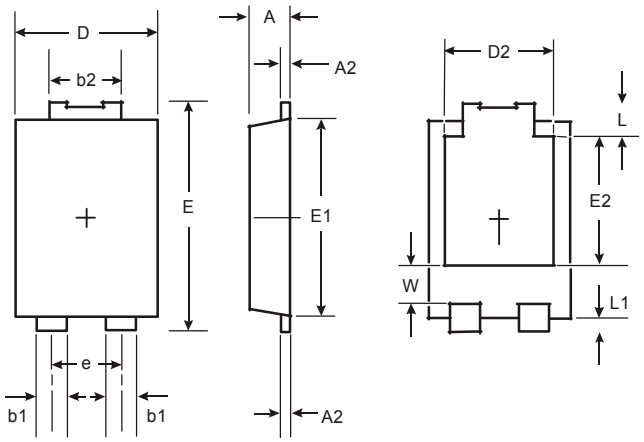


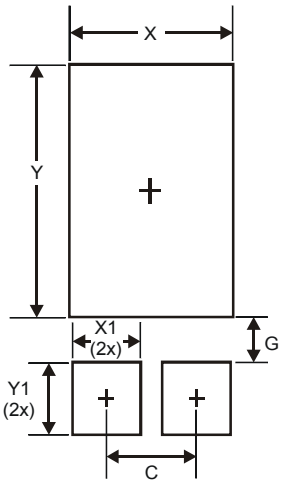
Fig. 5 Forward Current Derating Curve

Package Outline Dimensions



POWERDI®5		
Dim	**Min**	**Max**
A	1.05	1.15
A2	0.33	0.43
b1	0.80	0.99
b2	1.70	1.88
D	3.90	4.05
D2	3.054 Typ	
E	6.40	6.60
e	1.84 Typ	
E1	5.30	5.45
E2	3.549 Typ	
L	0.75	0.95
L1	0.50	0.65
W	1.10	1.41
All Dimensions in mm		

Suggested Pad Layout



Dimensions	Value (in mm)
C	1.840
G	0.852
X	3.360
X1	1.390
Y	4.860
Y1	1.400