

MAX9015–MAX9020

SOT23, Dual, Precision, 1.8V, nanoPower Comparators With/Without Reference

Absolute Maximum Ratings

Supply Voltage (V_{CC} to V_{EE})6V
 $IN+$, $IN-$, $INA+$, $INB+$, $INA-$, $INB-$,
 $REF/INA-$, REF ($V_{EE} - 0.3V$) to ($V_{CC} + 0.3V$)
Output Voltage (OUT_+)
MAX9015A, MAX9017_, MAX9019_... ($V_{EE} - 0.3V$) to ($V_{CC} + 0.3V$)
MAX9016A, MAX9018_, MAX9020_... ($V_{EE} - 0.3V$) to +6V
Output Current (REF , OUT_+ , $REF/INA-$) $\pm 50mA$

Output Short-Circuit Duration (REF , OUT_+ , $REF/INA-$) 10s
Continuous Power Dissipation ($T_A = +70^\circ C$)
8-Pin SOT23 (derate 9.1mW/ $^\circ C$ above $+70^\circ C$) 727mW
Operating Temperature Range $-40^\circ C$ to $+85^\circ C$
Storage Temperature Range $-65^\circ C$ to $+150^\circ C$
Junction Temperature $+150^\circ C$
Lead Temperature (soldering, 10s) $+300^\circ C$
Soldering Temperature (reflow) $+260^\circ C$

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Electrical Characteristics—MAX9015–MAX9018 (Single and Duals with REF)

($V_{CC} = 5V$, $V_{EE} = 0V$, $V_{IN-} = V_{REF}$, $T_A = -40^\circ C$ to $+85^\circ C$, unless otherwise noted. Typical values are at $T_A = +25^\circ C$.) (Note 1)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
Supply Voltage Range	V_{CC}	Inferred from the PSRR test		1.8		5.5	V
Supply Current	I_{CC}	MAX9015A/ MAX9016A	$V_{CC} = 1.8V$, $T_A = +25^\circ C$		1.0	1.5	μA
			$V_{CC} = 5.0V$, $T_A = +25^\circ C$		1.1	1.7	
			$V_{CC} = 5.0V$, $T_A = T_{MIN}$ to T_{MAX}			2.0	
		MAX9017_ / MAX9018_	$V_{CC} = 1.8V$, $T_A = +25^\circ C$		1.2	1.9	
			$V_{CC} = 5.0V$, $T_A = +25^\circ C$		1.4	2.3	
			$V_{CC} = 5.0V$, $T_A = T_{MIN}$ to T_{MAX}			2.8	
Input Common-Mode Voltage Range (MAX9015A/MAX9016A)	V_{CM}	Inferred from V_{OS} test		$V_{EE} - 0.2$		$V_{CC} + 0.2$	V
$IN+$ Voltage Range (MAX9017_/MAX9018_)	V_{IN+}	Inferred from the output swing test		$V_{EE} - 0.2$		$V_{CC} + 0.2$	V
Input Offset Voltage	V_{OS}	$V_{EE} - 0.2V < V_{CM} < V_{CC} + 0.2V$ (Note 2)	$T_A = +25^\circ C$		0.15	5	mV
			$T_A = T_{MIN}$ to T_{MAX}			10	
Input-Referred Hysteresis	V_{HB}	$V_{EE} - 0.2V < V_{CM} < V_{CC} + 0.2V$ (Note 3)			4		mV
Input Bias Current ($IN+$, $IN-$, $INA+$, $INB+$, $INB-$)	I_B	$T_A = +25^\circ C$			± 0.15	± 1	nA
		$T_A = T_{MIN}$ to T_{MAX}				± 2	
Power-Supply Rejection Ratio	PSRR	$V_{CC} = 1.8V$ to $5.5V$			0.1	1	mV/V
Output Voltage Swing High (MAX9015A/MAX9017_)	$V_{CC} - V_{OH}$	$V_{CC} = 1.8V$, $I_{SOURCE} = 1mA$	$T_A = +25^\circ C$		100	200	mV
			$T_A = T_{MIN}$ to T_{MAX}			300	
		$V_{CC} = 5.0V$, $I_{SOURCE} = 6mA$	$T_A = +25^\circ C$		250	350	
			$T_A = T_{MIN}$ to T_{MAX}			450	
Output Voltage Swing Low (MAX9015A/MAX9016A/ MAX9017_/MAX9018_)	V_{OL}	$V_{CC} = 1.8V$, $I_{SINK} = 1mA$	$T_A = +25^\circ C$		105	200	mV
			$T_A = T_{MIN}$ to T_{MAX}			300	
		$V_{CC} = 5.0V$, $I_{SINK} = 6mA$	$T_A = +25^\circ C$		285	350	
			$T_A = T_{MIN}$ to T_{MAX}			450	

MAX9015–MAX9020

SOT23, Dual, Precision, 1.8V,
nanoPower Comparators
With/Without Reference

Electrical Characteristics—MAX9015–MAX9018 (Single and Duals with REF) (continued)

($V_{CC} = 5V$, $V_{EE} = 0V$, $V_{IN-} = V_{REF}$, $T_A = -40^{\circ}C$ to $+85^{\circ}C$, unless otherwise noted. Typical values are at $T_A = +25^{\circ}C$.) (Note 1)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
Output Leakage Current (MAX9016A/MAX9018_)	I_{LEAK}	$V_{CC} = 5.5V$, $V_{OUT} = 5.5V$			0.001	1	μA
Output Short-Circuit Current	I_{SC}	Sourcing, $V_{OUT} = V_{EE}$ (MAX9015A/MAX9017_ only)	$V_{CC} = 1.8V$		3		mA
			$V_{CC} = 5.0V$		35		
		Sinking, $V_{OUT} = V_{CC}$	$V_{CC} = 1.8V$		3		
			$V_{CC} = 5.0V$		33		
High-to-Low Propagation Delay (Note 4)	t_{PD-}	$V_{CC} = 1.8V$			7		μs
		$V_{CC} = 5.0V$			6		
Low-to-High Propagation Delay (Note 4)	t_{PD+}	$V_{CC} = 1.8V$	MAX9015A/MAX9017_		11		μs
			MAX9016A/MAX9018_, $R_{PULLUP} = 100k\Omega$ to V_{CC}		12		
		$V_{CC} = 5.0V$	MAX9015A/MAX9017_		28		
			MAX9016A/MAX9018_, $R_{PULLUP} = 100k\Omega$ to V_{CC}		31		
Rise Time	t_{RISE}	$C_L = 15pF$ (MAX9015A/MAX9017_)			1.6		μs
Fall Time	t_{FALL}	$C_L = 15pF$			0.2		μs
Power-Up Time	t_{ON}				1.2		ms
Reference Voltage (Note 5)	V_{REF}	MAX901_A	$T_A = +25^{\circ}C$, 1.0%	1.224	1.236	1.248	V
			$T_A = T_{MIN}$ to T_{MAX} , 2.5%	1.205		1.267	
		MAX901_B	$T_A = +25^{\circ}C$, 1.75%	1.218	1.240	1.262	
			$T_A = T_{MIN}$ to T_{MAX} , 4.5%	1.184		1.296	
Reference Voltage Temperature Coefficient	TC_{REF}				40		ppm/ $^{\circ}C$
Reference Output Voltage Noise	E_N	BW = 10Hz to 1kHz, $C_{REF} = 1nF$			29		μV_{RMS}
		BW = 10Hz to 6kHz, $C_{REF} = 1nF$			60		
Reference Line Regulation	$\frac{\Delta V_{REF}}{\Delta V_{CC}}$	$1.8V \leq V_{CC} \leq 5.5V$			0.5		mV/V
Reference Load Regulation	$\frac{\Delta V_{REF}}{\Delta I_{OUT}}$	$I_{OUT} = 0$ to $100nA$			0.03		mV/nA

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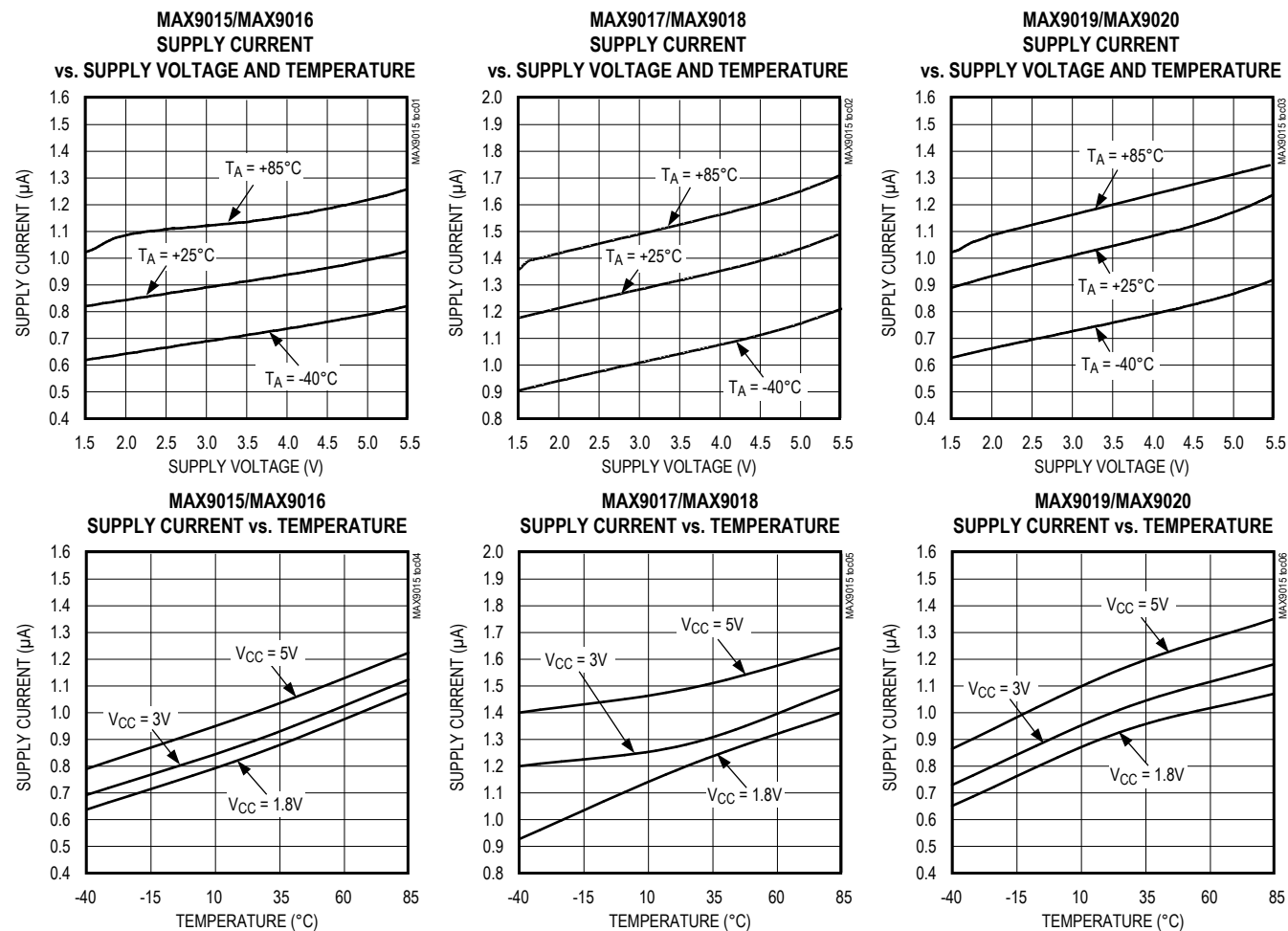
Electrical Characteristics—MAX9019/MAX9020 (Duals without REF)

($V_{CC} = 5V$, $V_{EE} = 0V$, $T_A = -40^{\circ}C$ to $+85^{\circ}C$, unless otherwise noted. Typical values are at $T_A = +25^{\circ}C$.) (Note 1)

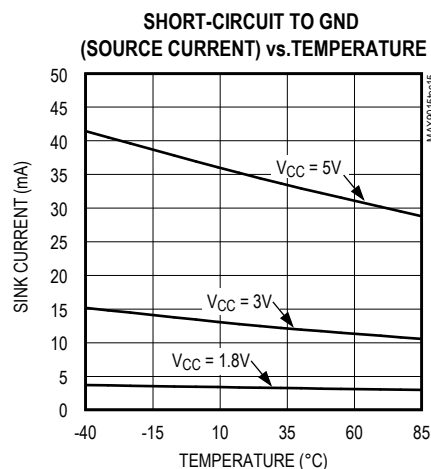
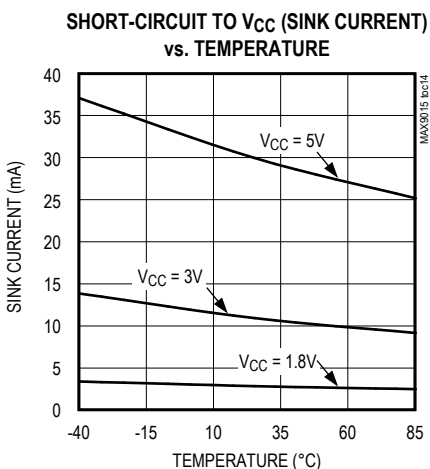
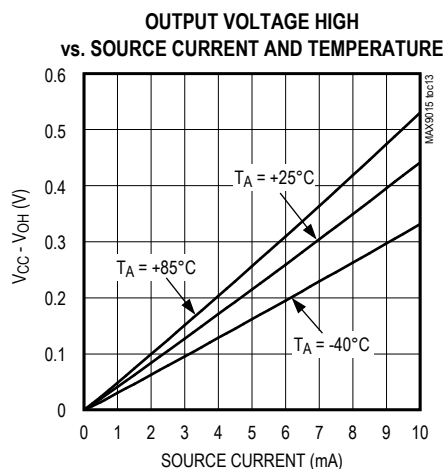
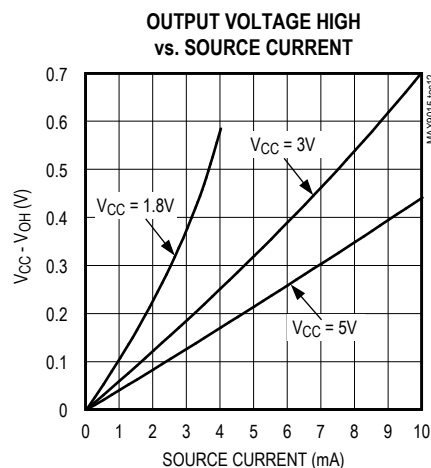
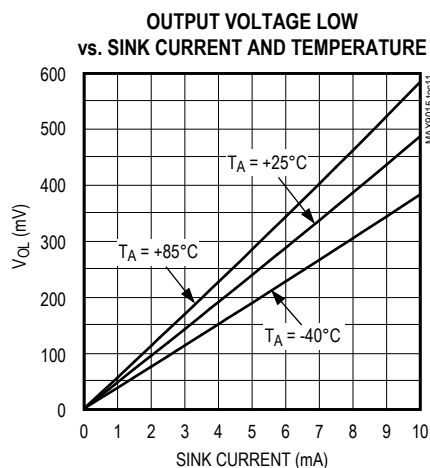
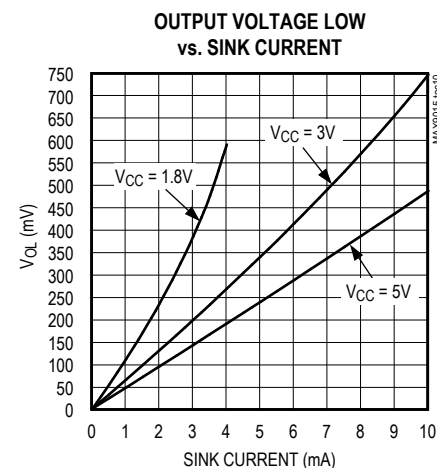
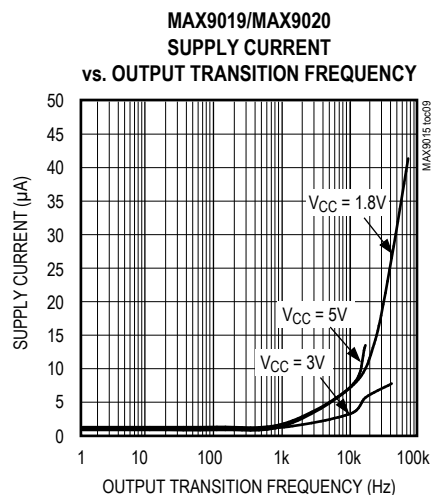
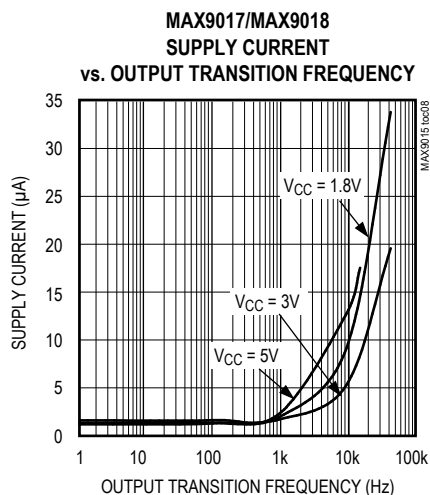
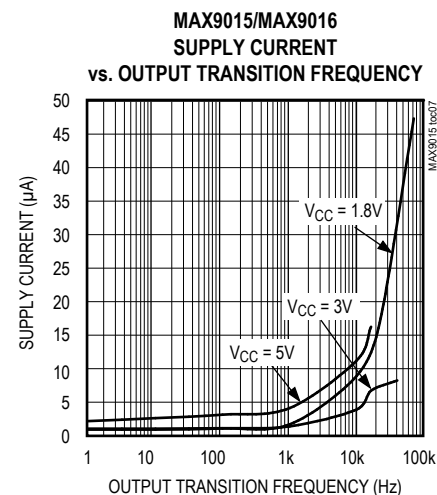
PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
Supply Voltage Range	V_{CC}	Inferred from the PSRR test		1.8		5.5	V
Supply Current	I_{CC}	MAX9019/ MAX9020	$V_{CC} = 1.8V$, $T_A = +25^{\circ}C$		0.85	1.50	μA
			$V_{CC} = 5.0V$, $T_A = +25^{\circ}C$		1.1	1.70	
			$V_{CC} = 5.0V$, $T_A = T_{MIN}$ to T_{MAX}			2.0	
Input Common-Mode Voltage Range	V_{CM}	Inferred from V_{OS} test		$V_{EE} - 0.2$		$V_{CC} + 0.2$	V
Input Offset Voltage	V_{OS}	$V_{EE} - 0.2V < V_{CM} < V_{CC} + 0.2V$ (Note 2)	$T_A = +25^{\circ}C$		1	5	mV
			$T_A = T_{MIN}$ to T_{MAX}			10	
Input-Referred Hysteresis	V_{HB}	$V_{EE} - 0.2V < V_{CM} < V_{CC} + 0.2V$ (Note 3)			4		mV
Input Bias Current (INA-, INA+, INB+, INB-)	I_B	$T_A = +25^{\circ}C$			0.15	1	nA
		$T_A = T_{MIN}$ to T_{MAX}				2	
Power-Supply Rejection Ratio	PSRR	$V_{CC} = 1.8V$ to $5.5V$			0.1	1	mV/V
Output Voltage Swing High (MAX9019 Only)	$V_{CC} - V_{OH}$	$V_{CC} = 1.8V$, $I_{SOURCE} = 1mA$	$T_A = +25^{\circ}C$		55	200	mV
			$T_A = T_{MIN}$ to T_{MAX}			300	
		$V_{CC} = 5.0V$, $I_{SOURCE} = 6mA$	$T_A = +25^{\circ}C$		190	350	
			$T_A = T_{MIN}$ to T_{MAX}			450	
Output Voltage Swing Low	V_{OL}	$V_{CC} = 1.8V$, $I_{SINK} = 1mA$	$T_A = +25^{\circ}C$		55	200	mV
			$T_A = T_{MIN}$ to T_{MAX}			300	
		$V_{CC} = 5.0V$, $I_{SINK} = 6mA$	$T_A = +25^{\circ}C$		190	350	
			$T_A = T_{MIN}$ to T_{MAX}			450	
Output Leakage Current (MAX9020 Only)	I_{LEAK}	$V_{CC} = 5.5V$, $V_{OUT} = 5.5V$			0.001	1	μA
Output Short-Circuit Current	I_{SC}	Sourcing, $V_{OUT} = V_{EE}$ (MAX9019 only)	$V_{CC} = 1.8V$		3		mA
			$V_{CC} = 5.0V$		35		
		Sinking, $V_{OUT} = V_{CC}$	$V_{CC} = 1.8V$		3		
			$V_{CC} = 5.0V$		33		
High-to-Low Propagation Delay (Note 4)	t_{PD-}	$V_{CC} = 1.8V$			7		μs
		$V_{CC} = 5.0V$			6		
Low-to-High Propagation Delay (Note 4)	t_{PD+}	$V_{CC} = 1.8V$	MAX9019		11		μs
			MAX9020, $R_{PULLUP} = 100k\Omega$ to V_{CC}		12		
		$V_{CC} = 5.0V$	MAX9019		28		
			MAX9020, $R_{PULLUP} = 100k\Omega$ to V_{CC}		31		

Electrical Characteristics—MAX9019/MAX9020 (Duals without REF) (continued)(V_{CC} = 5V, V_{EE} = 0V, T_A = -40°C to +85°C, unless otherwise noted. Typical values are at T_A = +25°C.) (Note 1)

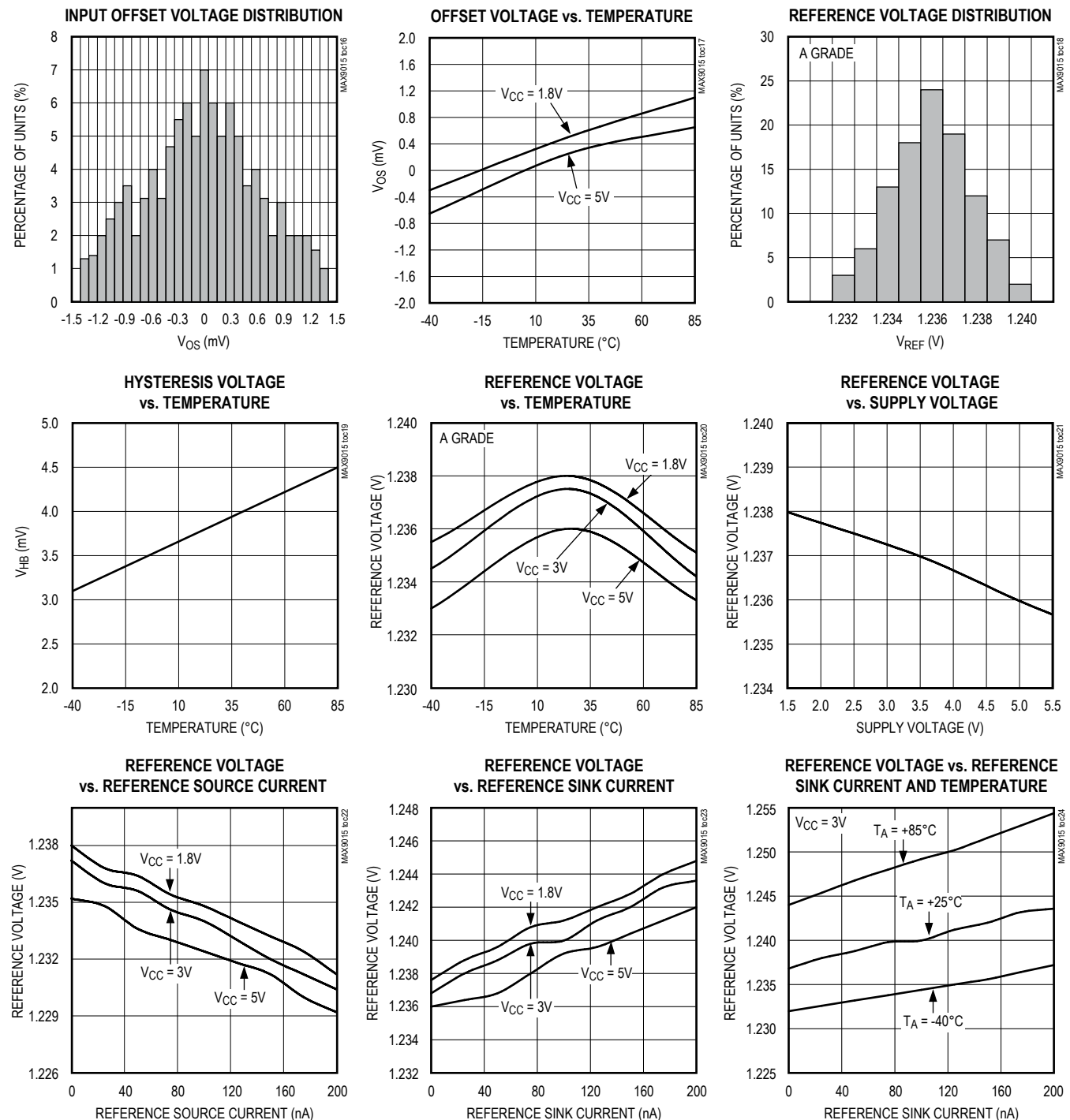
PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Rise Time	t _{RISE}	C _L = 15pF (MAX9019 only)		1.6		μs
Fall Time	t _{FALL}	C _L = 15pF		0.2		μs
Power-Up Time	t _{ON}			1.2		ms

Note 1: All devices are 100% tested at T_A = +25°C. Specifications over temperature (T_A = T_{MIN} to T_{MAX}) are guaranteed by design, not production tested.**Note 2:** V_{OS} is defined as the center of the hysteresis band at the input.**Note 3:** The hysteresis-related trip points are defined as the edges of the hysteresis band, measured with respect to the center of the band (i.e., V_{OS}) (Figure 1).**Note 4:** Specified with an input overdrive (V_{OVERDRIVE}) of 100mV, and a load capacitance of C_L = 15pF. V_{OVERDRIVE} is defined above and beyond the offset voltage and hysteresis of the comparator input.**Note 5:** High current traces should not be routed in the vicinity of or below MAX9018. There is a chance of voltage reference being overloaded resulting in drop of output voltage.**Typical Operating Characteristics**(V_{CC} = 5V, V_{EE} = 0V, C_L = 15pF, V_{OVERDRIVE} = 100mV, T_A = +25°C, unless otherwise noted.)

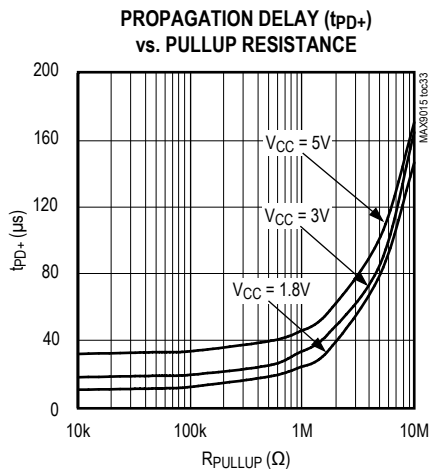
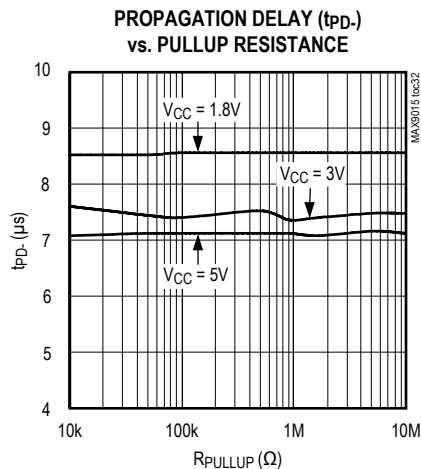
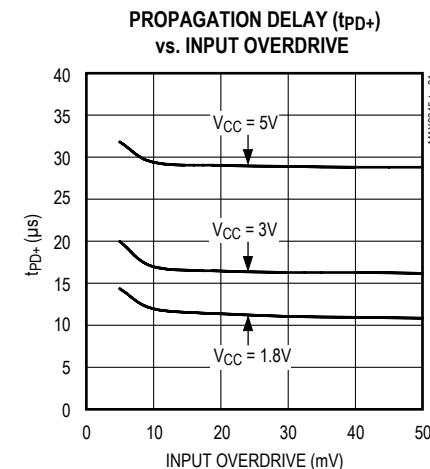
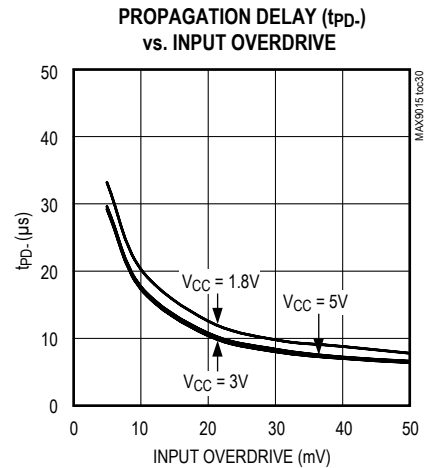
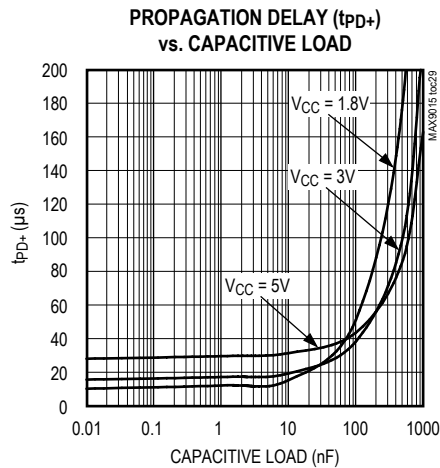
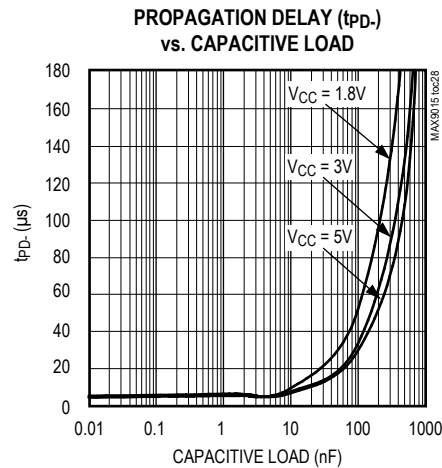
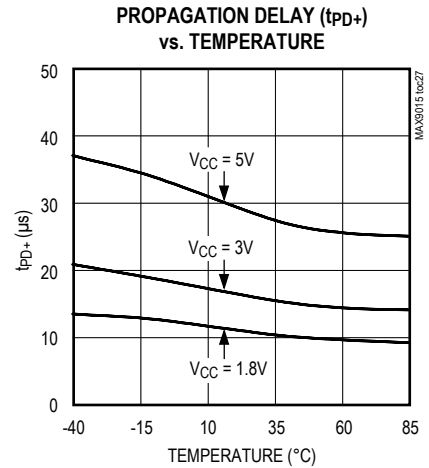
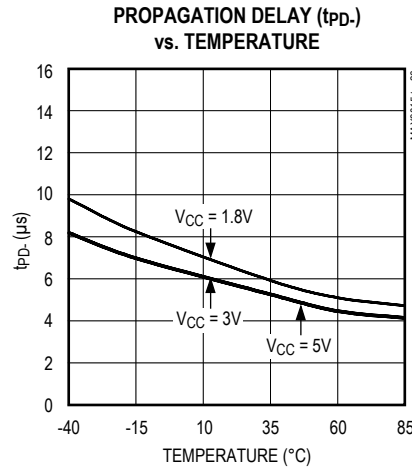
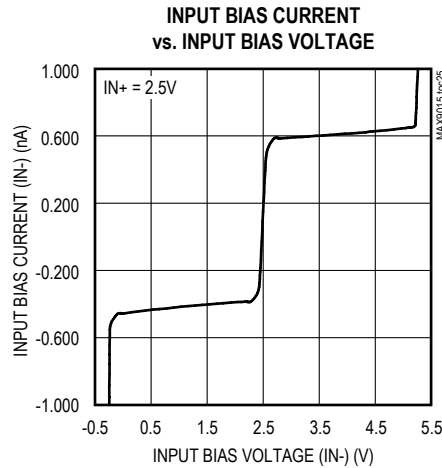
Typical Operating Characteristics (continued)

(V_{CC} = 5V, V_{EE} = 0V, C_L = 15pF, V_{OVERDRIVE} = 100mV, T_A = +25°C, unless otherwise noted.)

Typical Operating Characteristics (continued)

(V_{CC} = 5V, V_{EE} = 0V, C_L = 15pF, V_{OVERDRIVE} = 100mV, T_A = +25°C, unless otherwise noted.)

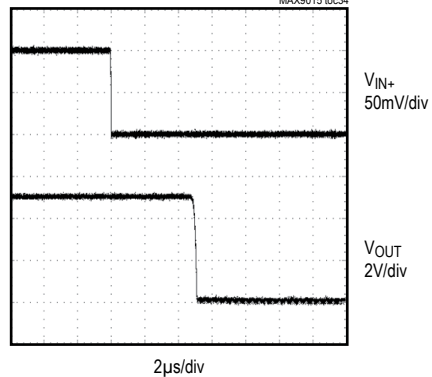
Typical Operating Characteristics (continued)

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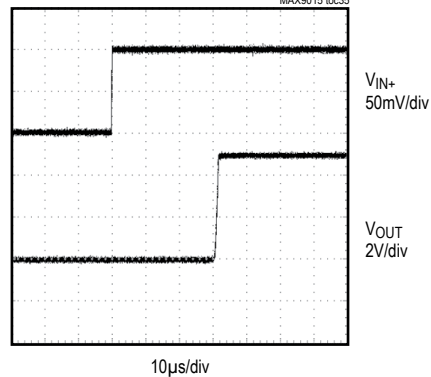
Typical Operating Characteristics (continued)

(V_{CC} = 5V, V_{EE} = 0V, C_L = 15pF, V_{OVERDRIVE} = 100mV, T_A = +25°C, unless otherwise noted.)PROPAGATION DELAY (t_{PD-}) (V_{CC} = 5V)

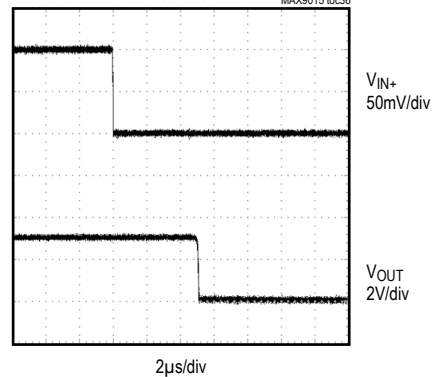
MAX9015 toc34

PROPAGATION DELAY (t_{PD+}) (V_{CC} = 5V)

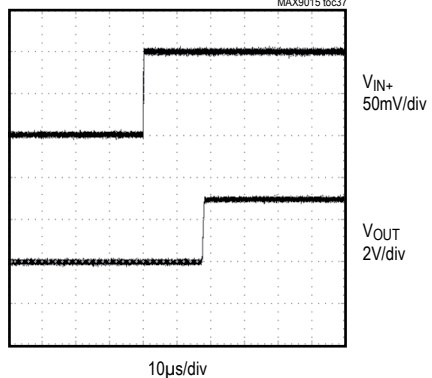
MAX9015 toc35

PROPAGATION DELAY (t_{PD-}) (V_{CC} = 3V)

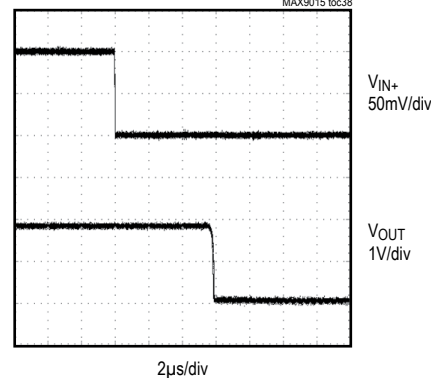
MAX9015 toc36

PROPAGATION DELAY (t_{PD+}) (V_{CC} = 3V)

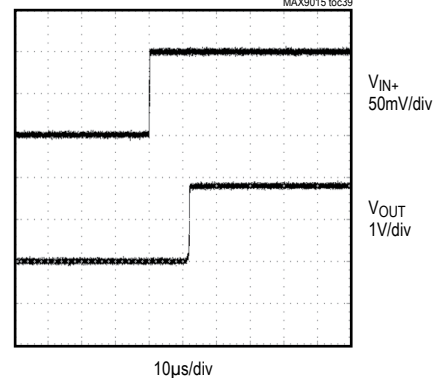
MAX9015 toc37

PROPAGATION DELAY (t_{PD-}) (V_{CC} = 1.8V)

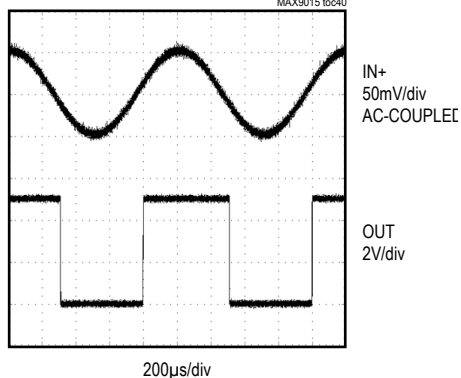
MAX9015 toc38

PROPAGATION DELAY (t_{PD+}) (V_{CC} = 1.8V)

MAX9015 toc39

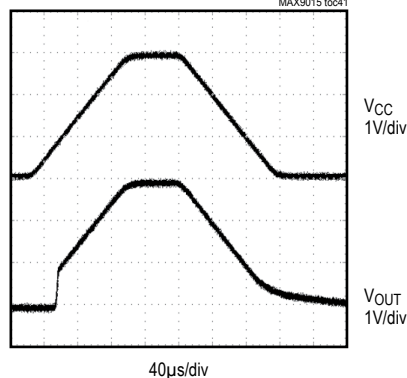
1kHz RESPONSE (V_{CC} = 5V)

MAX9015 toc40



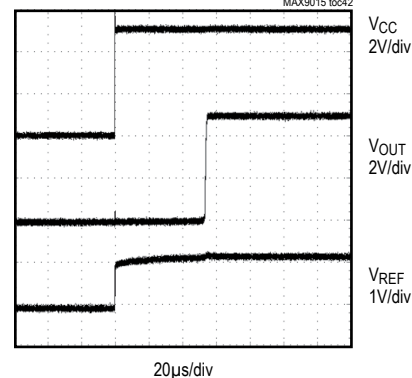
SLOW POWER-UP/DOWN RESPONSE

MAX9015 toc41



POWER-UP RESPONSE

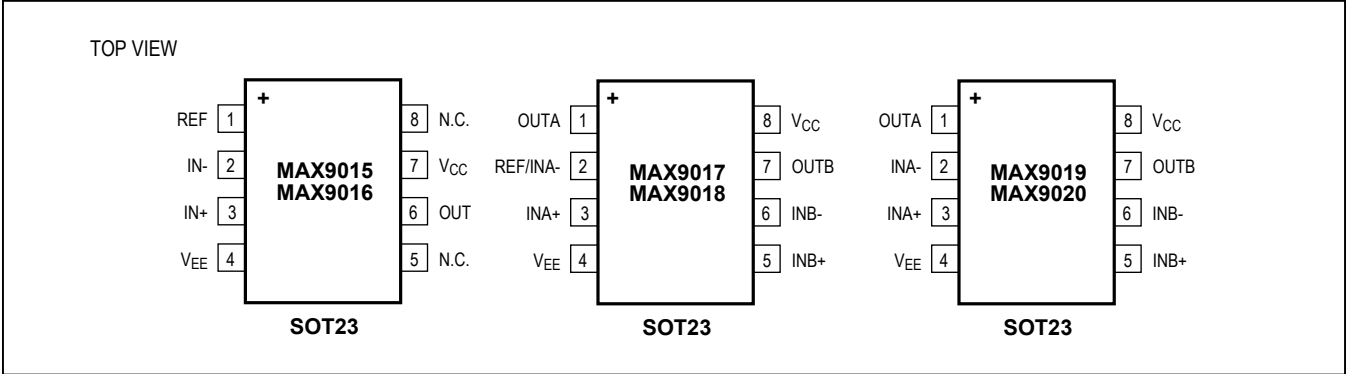
MAX9015 toc42



MAX9015–MAX9020

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Pin Configurations



Pin Description

PIN			NAME	FUNCTION
MAX9015/ MAX9016	MAX9017/ MAX9018	MAX9019/ MAX9020		
1	—	—	REF	1.24V Reference Output
2	—	—	IN-	Comparator Inverting Input
3	—	—	IN+	Comparator Noninverting Input
4	4	4	VEE	Negative Supply Voltage
5, 8	—	—	N.C.	No Connection. Not internally connected.
6	—	—	OUT	Comparator Output
7	8	8	VCC	Positive Supply Voltage
—	1	1	OUTA	Comparator A Output
—	3	3	INA+	Comparator A Noninverting Input
—	5	5	INB+	Comparator B Noninverting Input
—	6	6	INB-	Comparator B Inverting Input
—	7	7	OUTB	Comparator B Output
—	—	2	INA-	Comparator A Inverting Input
—	2	—	REF/ INA-	1.24V Reference Output. Internally connected to the inverting input of comparator A (MAX9017/MAX9018 only).

Detailed Description

The MAX9015–MAX9018 feature an on-board 1.24V $\pm 0.5\%$ ($\pm 1.45\%$ for the B grade) reference, yet draw an ultra-low supply current. The MAX9019/MAX9020 (duals without reference) consume just 850nA of supply current. All devices are guaranteed to operate down to 1.8V supply. Their common-mode input voltage range extends 200mV beyond-the-rails. An internal 4mV hysteresis ensures clean output switching, even with slow-moving input signals. Large internal output drivers swing rail-to-rail with up to $\pm 6\text{mA}$ loads (MAX9015/MAX9017/MAX9019).

The output stage employs a unique design that minimizes supply-current surges while switching, which virtually eliminates the supply glitches typical of many other comparators. The MAX9015/MAX9017/MAX9019 have a push-pull output stage that sinks as well as sources current. The MAX9016/MAX9018/MAX9020 have an open-drain output stage that can be pulled beyond V_{CC} up to 5.5V above V_{EE} . These open-drain versions are ideal for implementing wire-ORed output logic functions.

Input Stage Circuitry

The input common-mode voltage ranges extend from $V_{EE} - 0.2\text{V}$ to $V_{CC} + 0.2\text{V}$. These comparators operate at any differential input voltage within these limits. Input bias current is typically $\pm 150\text{pA}$ at the trip point, if the input voltage is between the supply rails. Comparator inputs are protected from overvoltage by internal ESD protection diodes connected to the supply rails. As the input voltage exceeds the supply rails, these ESD protection diodes become forward biased and begin to conduct increasing input bias current (see the Input Bias Current vs. Input Bias Voltage graph in the [Typical Operating Characteristics](#)).

Output Stage Circuitry

The devices feature a unique break-before-make output stage capable of driving $\pm 8\text{mA}$ loads rail-to-rail. Many comparators consume orders of magnitude more current during switching than during steady-state operation. However, with this device family of comparators, the supply-current change during an output transition is extremely small. In the [Typical Operating Characteristics](#), the Supply Current vs. Output Transition Frequency graphs show the minimal supply-current increase as the output switching frequency approaches 1kHz. This characteristic reduces the need for power-supply filter capacitors to reduce glitches created by comparator switching currents. In battery-powered applications, this characteristic results in a substantial increase in battery life.

Reference (MAX9015–MAX9018)

The MAX9015–MAX9018s' internal +1.24V reference has a typical temperature coefficient of 40ppm/ $^{\circ}\text{C}$ over the full -40°C to $+85^{\circ}\text{C}$ temperature range. The reference is a very-low-power bandgap cell, with a typical 35k Ω output impedance. REF can source and sink up to 100nA to external circuitry. For applications needing increased drive, buffer REF with a low input-bias current op amp such as the MAX4162. Most applications require no REF bypass capacitor. For noisy environments or fast transients, connect a 1nF to 10nF ceramic capacitor from REF to GND.

Applications Information

Low-Voltage, Low-Power Operation

The MAX9015–MAX9020 are ideally suited for use with most battery-powered systems. [Table 1](#) lists a variety of battery types, capacities, and approximate operating times for the MAX9015–MAX9020, assuming nominal conditions.

Table 1. Battery Applications Using the MAX9015–MAX9020

BATTERY TYPE	RECHARGEABLE	V_{FRESH} (V)	$V_{\text{END-OF-LIFE}}$ (V)	CAPACITY, AA SIZE (mA-hr)	MAX9015A/ MAX9016A OPERATING TIME (hr)	MAX9017/ MAX9018 OPERATING TIME (hr)	MAX9019/ MAX9020 OPERATING TIME (hr)
Alkaline (2 cells)	No	3.0	1.8	2000	2000k	1540k	1333k
Nickel-cadmium (2 cells)	Yes	2.4	1.8	750	750k	570k	500k
Nickel-metal-hydride (2 cells)	Yes	2.4	1.8	1000	1000k	770k	660k
Lithium-ion (1 cell)	Yes	3.6	2.9	1000	1000k	770k	660k

Internal Hysteresis

Many comparators oscillate in the linear region of operation because of noise or undesired parasitic feedback. Oscillations can occur when the voltage on one input is equal or very close to the voltage on the other input. The devices have internal 4mV hysteresis to counter parasitic effects and noise.

The hysteresis in a comparator creates two trip points: one for the rising input voltage (V_{THR}) and one for the falling input voltage (V_{THF}) (Figure 1). The difference between the trip points is the hysteresis (V_{HB}). When the comparator's input voltages are equal, the hysteresis effectively causes one comparator input to move quickly past the other, thus taking the input out of the region where oscillation occurs. Figure 1 illustrates the case in which the comparator's inverting input has a fixed voltage applied, and the noninverting input is varied. If the inputs were reversed, the figure would be the same, except with an inverted output.

Additional Hysteresis (MAX9015/MAX9017/MAX9019) (Push-Pull Outputs)

The MAX9015/MAX9017/MAX9019 feature a built-in 4mV hysteresis band (V_{HB}). Additional hysteresis can be generated with three resistors using positive feedback (Figure 2). Use the following procedure to calculate resistor values:

- 1) Select R3. Input bias current at IN_+ is less than 2nA, so the current through R3 should be at least 0.2μA to minimize errors caused by input bias current. The current through R3 at the trip point is $(V_{REF} - V_{OUT})/R3$. Considering the two possible output states in solving for R3 yields two formulas: $R3 = V_{REF}/I_{R3}$ or $R3 = (V_{CC} - V_{REF})/I_{R3}$. Use the smaller of the two resulting resistor values. For example, when using the MAX9017 ($V_{REF} = 1.24V$) and $V_{CC} = 5V$, and if we choose $I_{R3} = 0.2μA$, then the two resistor values are 6.2MΩ and 19MΩ. Choose a 6.2MΩ standard value for R3.
- 2) Choose the hysteresis band required (V_{HB}). For this example, choose 50mV.
- 3) Calculate R1 according to the following equation:

$$R1 = 6.2M\Omega \left(\frac{50mV}{5V} \right) = 12k\Omega$$

For this example, insert the values:

$$R1 = 6.2M\Omega \left(\frac{50mV}{5V} \right) = 12k\Omega$$

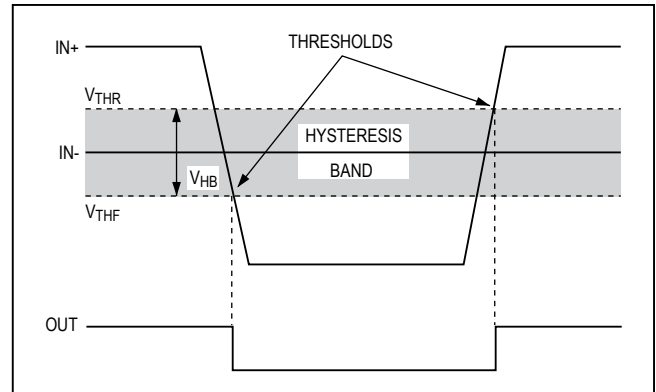


Figure 1. Threshold Hysteresis Band

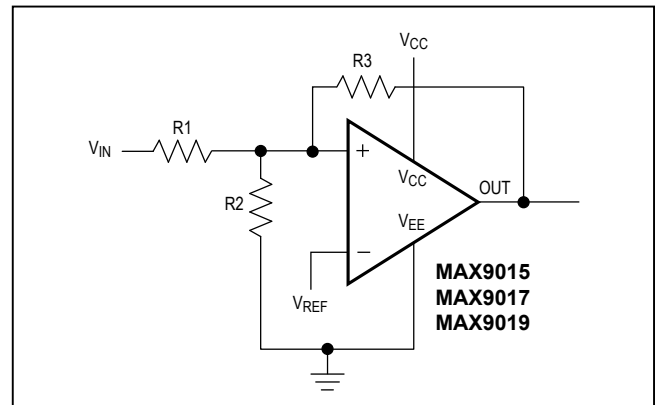


Figure 2. MAX9015/MAX9017/MAX9019 Additional Hysteresis

- 4) Choose the trip point for V_{IN} rising (V_{THR}) such that:

$$V_{THR} > V_{REF} \left(1 + \frac{V_{HB}}{V_{CC}} \right)$$

where V_{THR} is the trip point for V_{IN} rising. This is the threshold voltage at which the comparator switches its output from low to high as V_{IN} rises above the trip point. For this example, choose 3V.

- 5) Calculate R2 as follows:

$$R2 = \frac{1}{\left[\left(\frac{V_{THR}}{V_{REF} \times R1} \right) - \left(\frac{1}{R1} \right) - \left(\frac{1}{R3} \right) \right]}$$

$$R2 = \frac{1}{\left[\left(\frac{3.0V}{(1.24V \times 62k\Omega)} \right) - \left(\frac{1}{62k\Omega} \right) - \left(\frac{1}{6.2M\Omega} \right) \right]} = 43.99k\Omega$$

For this example, choose a 44.2kΩ standard value.

- 6) Verify the trip voltages and hysteresis as follows:

V_{IN} rising: = 2.992V, which is equivalent to V_{REF} times $R1$ divided by the parallel combination of $R1$, $R2$:

$$V_{THR} = V_{REF} \times R1 \left[\left(\frac{1}{R1} \right) + \left(\frac{1}{R2} \right) + \left(\frac{1}{R3} \right) \right]$$

and $R3$.

V_{IN} falling: = 2.942V:

$$V_{THF} = V_{THR} - \left(\frac{R1 \times V_{CC}}{R3} \right)$$

Hysteresis = $V_{THR} - V_{THF} = 50\text{mV}$.

Additional Hysteresis (MAX9016/MAX9018/MAX9020) (Open-Drain Outputs)

The MAX9016/MAX9018/MAX9020 feature a built-in 4mV hysteresis band. These devices have open-drain outputs and require an external pullup resistor (Figure 3). Additional hysteresis can be generated using positive feedback, but the formulas differ slightly from those of the MAX9015/MAX9017/MAX9019. Use the following procedure to calculate resistor values:

- 1) Select $R3$. Input bias current at IN_{-} is less than 2nA, so the current through $R3$ should be at least $0.2\mu\text{A}$ to minimize errors caused by input bias current. The current through $R3$ at the trip point is $(V_{REF} - V_{OUT})/R3$. Considering the two possible output states in solving for $R3$ yields two formulas: $R3 = V_{REF}/I_{R3}$ or $R3 = [(V_{CC} - V_{REF})/I_{R3}] - R4$. Use the smaller of the two resulting resistor values. For example, when using the MAX9018 ($V_{REF} = 1.24\text{V}$) and $V_{CC} = 5\text{V}$, and if we choose $I_{R3} = 0.2\mu\text{A}$, and $R4 = 1\text{M}\Omega$, then the two resistor values are $6.2\text{M}\Omega$ and $18\text{M}\Omega$. Choose a $6.2\text{M}\Omega$ standard value for $R3$.
- 2) Choose the hysteresis band required (V_{HB}).
- 3) Calculate $R1$ according to the following equation. For this example, insert the values:

$$R1 = (R3 + R4) \left(\frac{V_{HB}}{V_{CC}} \right)$$

$$R1 = (6.2\text{M}\Omega + 1\text{M}\Omega) \left(\frac{50\text{mV}}{5\text{V}} \right) = 72\text{k}\Omega$$

- 4) Choose the trip point for V_{IN} rising (V_{THR}) such that:

$$V_{THR} > V_{REF} \left(1 + \frac{V_{HB}}{V_{CC}} \right)$$

(V_{THR} is the trip point for V_{IN} rising). This is the threshold voltage at which the comparator switches its output from low to high as V_{IN} rises above the trip point. For this example, choose 3V:

- 5) Calculate $R2$ as follows:

$$R2 = \frac{1}{\left[\left(\frac{V_{THR}}{V_{REF} \times R1} \right) - \left(\frac{1}{R1} \right) - \left(\frac{1}{R3} \right) \right]}$$

$$R2 = \frac{1}{\left[\left(\frac{3.0\text{V}}{1.24\text{V} \times 72\text{k}\Omega} \right) - \left(\frac{1}{72\text{k}\Omega} \right) - \left(\frac{1}{6.2\text{M}\Omega} \right) \right]} = 51.1\text{k}\Omega$$

For this example, choose a $49.9\text{k}\Omega$ standard value.

- 6) Verify the trip voltages and hysteresis as follows:

$$V_{IN \text{ rising}} : V_{THR} = V_{REF} \times R1 \left[\left(\frac{1}{R1} \right) + \left(\frac{1}{R2} \right) + \left(\frac{1}{R3} \right) \right]$$

$$= 3.043\text{V}$$

$$V_{IN \text{ falling}} : V_{THF} = V_{REF} \times R1 \left[\left(\frac{1}{R1} \right) + \left(\frac{1}{R2} \right) + \left(\frac{1}{R3} \right) \right]$$

$$- \frac{R1}{R3 + R4} \times V_{CC} = 2.993\text{V}$$

Hysteresis = $V_{THR} - V_{THF} = 50\text{mV}$.

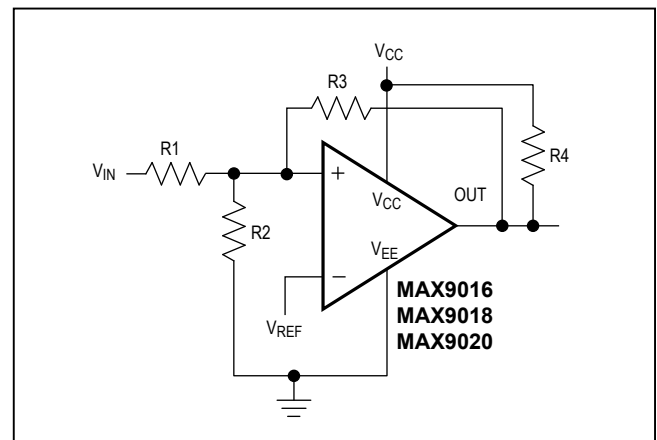


Figure 3. MAX9016/MAX9018/MAX9020 Additional Hysteresis

Board Layout and Bypassing

The devices' ultra-low supply current typically requires no power-supply bypass capacitors. However, when the supply has high output impedance, long lead lengths or excessive noise, or fast transients, bypass V_{CC} to V_{EE} with a 0.1 μ F capacitor placed as close to the V_{CC} pin as possible. Minimize signal trace lengths to reduce stray capacitance. Use a ground plane and surface-mount components for best performance. If REF is decoupled, use a low-leakage ceramic capacitor. High traces should not be routed in the vicinity of or below MAX9018. There is a chance of voltage reference being overloaded resulting in drop of output voltage.

Window Detector

The MAX9018 is ideal for window detectors (undervoltage/overvoltage detectors). Figure 4 shows a window detector circuit for a single-cell Li+ battery with a 2.9V end-of-life charge, a peak charge of 4.2V, and a nominal value of 3.6V. Choose different thresholds by changing the values of R1, R2, and R3. OUTA provides an active-low undervoltage indication, and OUTB provides an active-low overvoltage indication. ANDing the two open-drain outputs provides an active-high, power-good signal.

The design procedure is as follows:

- 1) Select R1. The input bias current into INB- is normally less than 2nA, so the current through R1 should exceed 100nA for the thresholds to be accurate. In this example, choose R1 = 1.24M Ω (1.24V/1 μ A).
- 2) Calculate R2 + R3. The overvoltage threshold should be 4.2V when V_{IN} is rising. The design equation is as follows:

$$\begin{aligned} R2 + R3 &= R1 \times \left[\left(\frac{V_{OTH}}{V_{REF} + V_{HB}} \right) - 1 \right] \\ &= 1.24\text{M}\Omega \times \left[\left(\frac{4.2\text{V}}{1.24\text{V} + 0.004} \right) - 1 \right] \\ &= 2.95\text{M}\Omega \end{aligned}$$

- 3) Calculate R2. The undervoltage threshold should be 2.9V when V_{IN} is falling. The design equation is as follows:

$$\begin{aligned} R2 &= (R1 + R2 + R3) \times \left(\frac{V_{REF} - V_{HB}}{V_{UTH}} \right) - R1 \\ &= (1.24\text{M}\Omega + 2.95\text{M}\Omega) \times \frac{(1.236)}{2.9} - 1.24\text{M}\Omega \\ &= 546\text{k}\Omega \end{aligned}$$

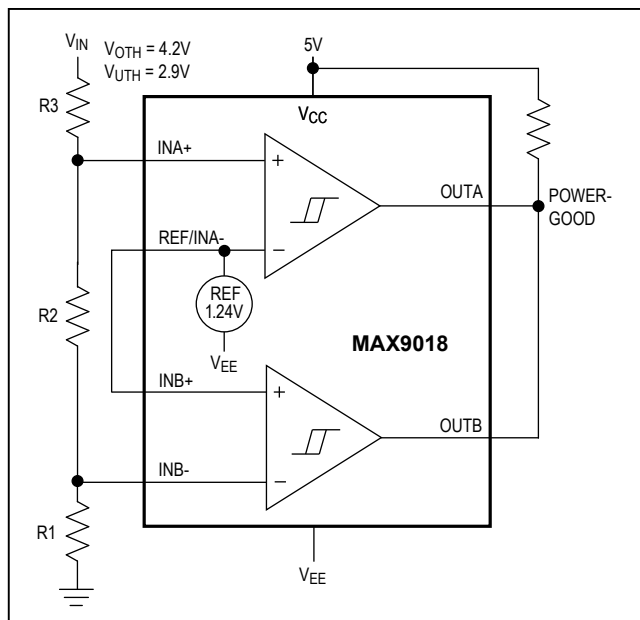


Figure 4. Window Detector Circuit

For this example, choose a 499k Ω standard value 1% resistor.

- 4) Calculate R3:

$$\begin{aligned} R3 &= (R2 + R3) - R2 \\ &= 2.95\text{M}\Omega - 546\text{k}\Omega \\ &= 240\text{M}\Omega \end{aligned}$$

- 5) Verify the resistor values. The equations are as follows, evaluated for the above example:

$$V_{OTH} = (V_{REF} + V_{HB}) \times \frac{(R1 + R2 + R3)}{R1} = 4.20\text{V}$$

Overvoltage threshold:

$$V_{UTH} = (V_{REF} - V_{HB}) \times \frac{(R1 + R2 + R3)}{(R1 + R2)} = 2.97\text{V}$$

where the internal hysteresis band, V_{HB} , is 4mV.

Zero-Crossing Detector

Figure 5 shows a zero-crossing detector application. The MAX9015/MAX9016/MAX9019/MAX9020s' inverting input is connected to ground, and its noninverting input is connected to a 100mV_{p-p} signal source. As the signal at the noninverting input crosses zero, the comparator's output changes state.

Logic-Level Translator

The open-drain comparators can be used to convert 5V logic to 3V logic levels. The MAX9020 can be powered by the 5V supply voltage, and the pullup resistor for the MAX9020's open-drain output is connected to the 3V supply voltage. This configuration allows the full 5V logic swing without creating overvoltage on the 3V logic inputs. For 3V to 5V logic-level translations, connect the 3V supply voltage to V_{CC} and the 5V supply voltage to the pullup resistor.

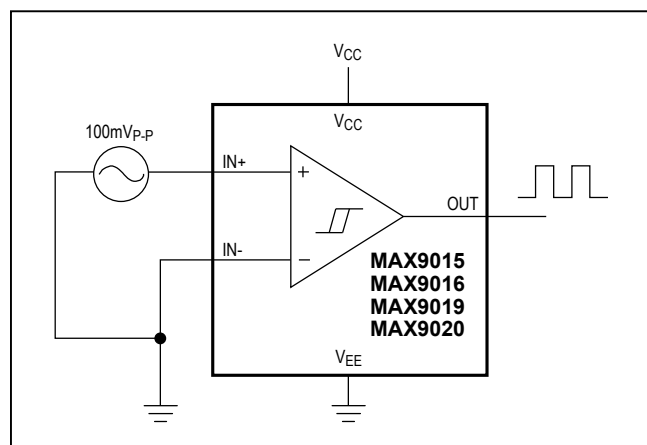
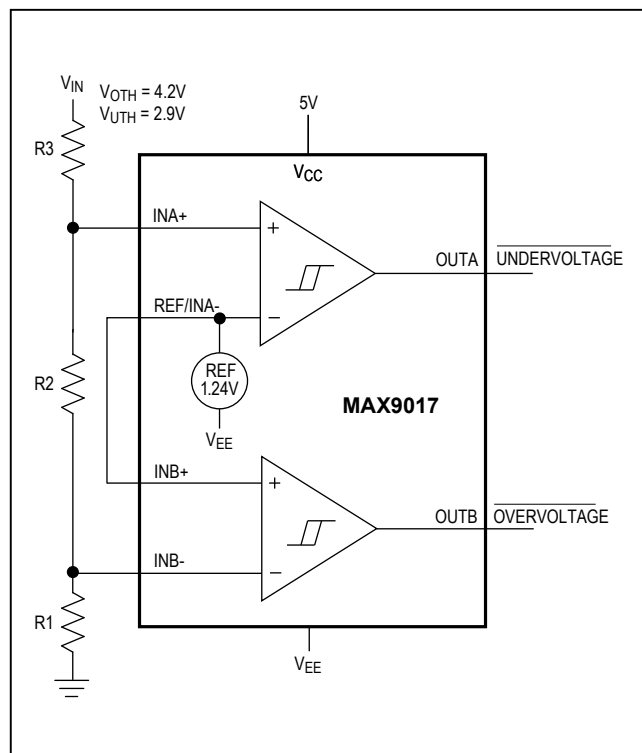


Figure 5. Zero-Crossing Detector

Typical Application Circuit

MAX9015–MAX9020

SOT23, Dual, Precision, 1.8V,
nanoPower Comparators
With/Without Reference

Selector Guide

PART	COMPARATOR(S)	INTERNAL REFERENCE (V)	OUTPUT TYPE	SUPPLY CURRENT (μA)
MAX9015A	1	1.236 ±1%	Push-pull	1
MAX9016A	1	1.236 ±1%	Open drain	1
MAX9017A	2	1.236 ±1%	Push-pull	1.2
MAX9017B	2	1.240 ±1.75%	Push-pull	1.2
MAX9018A	2	1.236 ±1%	Open drain	1.2
MAX9018B	2	1.240 ±1.75%	Open drain	1.2
MAX9019	2	—	Push-pull	0.85
MAX9020	2	—	Open drain	0.85

Ordering Information

PART	TEMP RANGE	PIN-PACKAGE	TOP MARK
MAX9015 AEKA-T	-40°C to +85°C	8 SOT23	AEIW
MAX9015AEKA+T	-40°C to +85°C	8 SOT23	AEIW
MAX9015AEKA/V+T	-40°C to +85°C	8 SOT23	+AETV
MAX9016 AEKA-T	-40°C to +85°C	8 SOT23	AEIX
MAX9016AEKA+T	-40°C to +85°C	8 SOT23	AEIX
MAX9017 AEKA-T	-40°C to +85°C	8 SOT23	AEIQ
MAX9017AEKA+T	-40°C to +85°C	8 SOT23	AEIQ
MAX9017BEKA-T	-40°C to +85°C	8 SOT23	AEIS
MAX9017BEKA+T	-40°C to +85°C	8 SOT23	AEIS
MAX9017AEKA/V+T	-40°C to +85°C	8 SOT23	AEIQ
MAX9018 AEKA-T	-40°C to +85°C	8 SOT23	AEIR
MAX9018AEKA+T	-40°C to +85°C	8 SOT23	AEIR
MAX9018BEKA-T	-40°C to +85°C	8 SOT23	AEIT
MAX9018BEKA+T	-40°C to +85°C	8 SOT23	AEIT
MAX9018BEKA/V+T	-40°C to +85°C	8 SOT23	AEIT
MAX9019 EKA-T	-40°C to +85°C	8 SOT23	AEIU
MAX9019EKA+T	-40°C to +85°C	8 SOT23	AEIU
MAX9020 EKA-T	-40°C to +85°C	8 SOT23	AEIV
MAX9020EKA+T	-40°C to +85°C	8 SOT23	AEIV

T = Tape and reel.

/V denotes an automotive qualified part.

Chip Information

PROCESS: TRANSISTOR COUNT: 349

PROCESS: BiCMOS

Package Information

For the latest package outline information and land patterns (footprints), go to www.maximintegrated.com/packages. Note that a "+", "#", or "-" in the package code indicates RoHS status only. Package drawings may show a different suffix character, but the drawing pertains to the package regardless of RoHS status.

PACKAGE TYPE	PACKAGE CODE	OUTLINE NO.	LAND PATTERN NO.
8 SOT23	K8-5	21-0078	90-0176

Revision History

REVISION NUMBER	REVISION DATE	DESCRIPTION	PAGES CHANGED
2	12/09	Updated EC table parameters after final test changes	2, 4
3	10/13	Added Note 5 to <i>Electrical Characteristics</i> and revised <i>Board Layout</i> and <i>Bypassing</i> section	5, 14
4	1/15	Added MAX9015AEKA/V+T to <i>Ordering Information</i>	2
5	1/16	Added MAX9017AEKA/V+T to <i>Ordering Information</i>	2
6	3/18	Added AEC statement to <i>Benefits and Features</i> section and updated <i>Ordering Information</i> table	1, 16

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