Power-Supply Monitor with Reset

ABSOLUTE MAXIMUM RATINGS

Terminal Voltage (with respect to GND)
- V_{CC} ..... -0.3V to 6.0V
- RESET ..... -0.3V to (V_{CC} + 0.3V)

Input Current, V_{CC} 20mA
Output Current, RESET 20mA
Rate-of-Rise, V_{CC} 100V/µs
Continuous Power Dissipation (T_A = +70°C)
- Plastic DIP (derate 9.09mW/°C above +70°C) 727mW
- µMAX (derate 4.10mW/°C above +70°C) 330mW
- SO (derate 5.88mW/°C above +70°C) 471mW

Operating Temperature Ranges
- MAX709_C___ ..... 0°C to +70°C
- MAX709_E___ ..... -40°C to +85°C

Storage Temperature Range ..... -65°C to +160°C
Lead Temperature (soldering, 10sec) ..... +300°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS

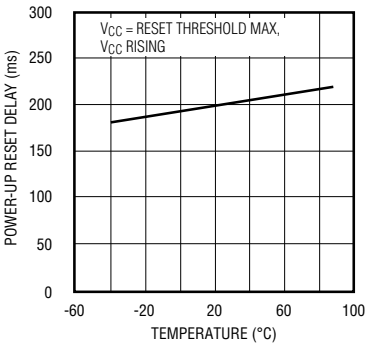
(V_{CC} = full range, T_A = T_{MIN} to T_{MAX}, unless otherwise noted.)

PARAMETER	CONDITIONS		MIN	TYP	MAX	UNITS
V_{CC} Range	MAX709_C		1.0		5.5	V
	MAX709_E		1.2		5.5	
Supply Current (Note 1)	MAX709R/S/T only	MAX709_C, V_{CC} < 3.6V		35	85	µA
		MAX709_E, V_{CC} < 3.6V		35	110	
	All versions	MAX709_C, V_{CC} < 5.5V		65	150	
		MAX709_E, V_{CC} < 5.5V		65	200	
RESET Threshold, V_{TH}	MAX709L		4.50	4.65	4.75	V
	MAX709M		4.25	4.40	4.50	
	MAX709T		3.00	3.08	3.15	
	MAX709S		2.85	2.93	3.00	
	MAX709R		2.55	2.63	2.70	
V_{CC} to RESET Delay	V_{CC} = reset threshold max to reset threshold min			20		µs
Reset Active Timeout Period	V_{CC} = reset threshold max, V_{CC} rising		140	280	560	ms
RESET Output Voltage	I_{SINK} = 1.2mA, V_{CC} = reset threshold min, MAX709R/S/T only				0.3	V
	I_{SINK} = 3.2mA, V_{CC} = reset threshold min, MAX709L/M only				0.4	
	I_{SINK} = 50µA, V_{CC} ≥ 1.0V, MAX709_C				0.3	
	I_{SINK} = 100µA, V_{CC} ≥ 1.2V, MAX709_E				0.4	
	I_{SOURCE} = 500µA, V_{CC} ≥ reset threshold max, MAX709R/S/T only		0.8 x V_{CC}			
	I_{SOURCE} = 800µA, V_{CC} ≥ reset threshold max, MAX709L/M only		V_{CC} - 1.5			

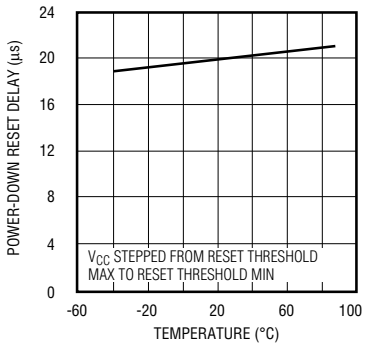
Note 1: Supply current is measured with V_{CC} = 3.6V for MAX709R/S/T, and V_{CC} = 5.5V for all versions.

Typical Operating Characteristics

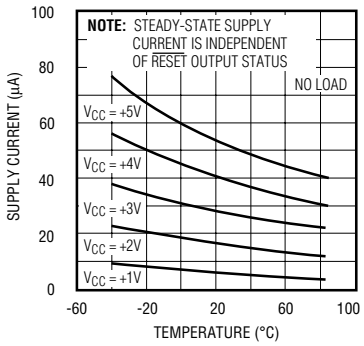
POWER-UP RESET DELAY vs. TEMPERATURE



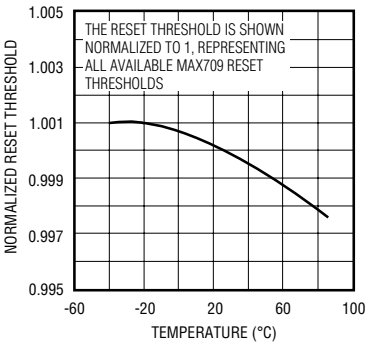
POWER-DOWN RESET DELAY vs. TEMPERATURE



SUPPLY CURRENT vs. TEMPERATURE



NORMALIZED RESET THRESHOLD vs. TEMPERATURE



Power-Supply Monitor with Reset

Pin Description

PIN	NAME	FUNCTION
1, 4, 5, 6, 8	N.C.	No Connect. There is no internal connection to this pin.
2	V_{CC}	+5V, +3.3V, or +3V Supply Voltage
3	GND	Ground
7	RESET	Reset Output remains low while V_{CC} is below the reset threshold, and for 280ms after V_{CC} rises above the reset threshold.

Applications Information

Negative-Going V_{CC} Transients

In addition to issuing a reset to the microprocessor (µP) during power-up, power-down, and brownout conditions, the MAX709 is relatively immune to short duration negative-going V_{CC} transients (glitches).

Figure 1 shows typical transient duration vs. reset comparator overdrive, for which the MAX709 does not generate a reset pulse. The graph was generated using a negative-going pulse applied to V_{CC}, starting 1.5V above the actual reset threshold and ending below it by the magnitude indicated (reset comparator overdrive). The graph indicates the typical maximum pulse width that a negative-going V_{CC} transient may have without causing a reset pulse to be issued. As the magnitude of the transient increases (goes farther below the reset threshold), the maximum allowable pulse width decreases. Typically, for the MAX709L/MAX709M, a V_{CC} transient that goes 100mV below the reset threshold and lasts 40µs or less will not cause a reset pulse to be issued.

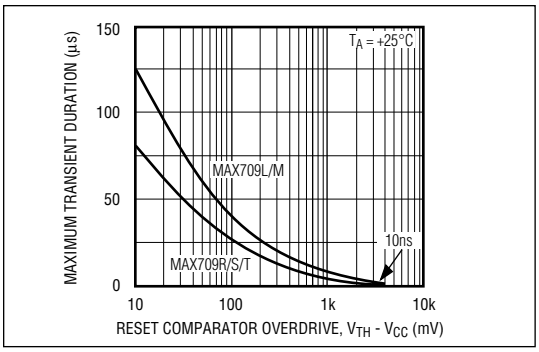


Figure 1. Maximum Transient Duration without Causing a Reset Pulse vs. Reset Comparator Overdrive

A 0.1µF bypass capacitor mounted as close as possible to pin 2 (V_{CC}) provides additional transient immunity.

Ensuring a Valid RESET Output Down to V_{CC} = 0V

When V_{CC} falls below 1V, the MAX709 RESET output no longer sinks current—it becomes an open circuit. Therefore, high-impedance CMOS logic inputs connected to the RESET output can drift to undermined voltages. This presents no problem in most applications, since most µP and other circuitry is inoperative with V_{CC} below 1V. However, in applications where the RESET output must be valid down to 0V, adding a pull-down resistor to the RESET pin will cause any stray leakage currents to flow to ground, holding RESET low (see Figure 2). The resistance value of R1 is not critical. It should be about 100kΩ, which is large enough not to load RESET and small enough to pull RESET to ground.

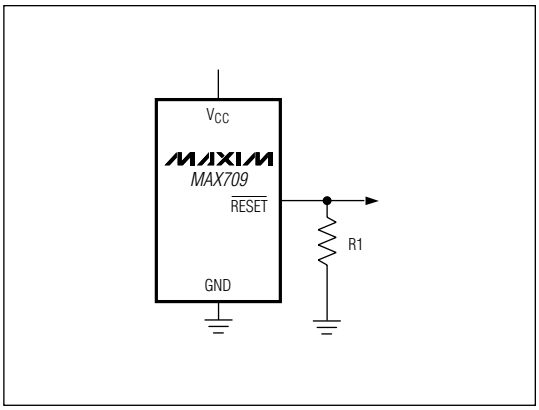


Figure 2. RESET Valid to V_{CC} = Ground Circuit

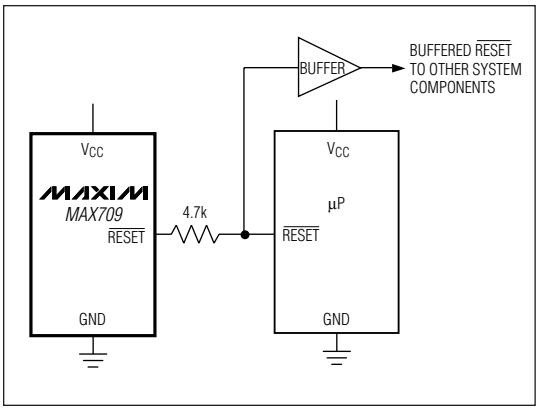


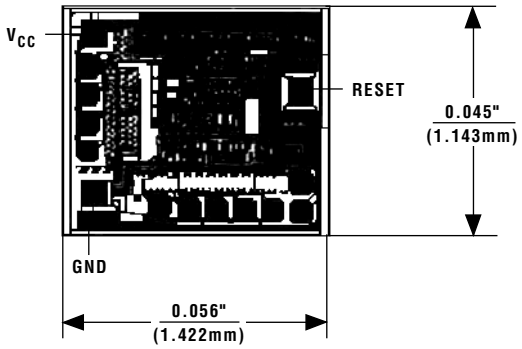
Figure 3. Interfacing to µPs with Bidirectional Reset I/O

Interfacing to µPs with Bidirectional Reset Pins

Microprocessors with bidirectional reset pins (such as the Motorola 68HC11 series) can contend with the MAX709 reset output. If, for example the MAX709 RESET output is asserted high and the µP wants to pull it low, indeterminate logic levels may result. To correct this, connect a 4.7kΩ resistor between the MAX709 RESET output and the µP reset I/O (see Figure 3). Buffer the MAX709 RESET output to other system components.

Part Number	Nominal Reset Threshold (V)	Minimum Reset Pulse Width (ms)	Nominal Watchdog Timeout Period (sec)	Backup-Battery Switch	$\overline{CE}$ - Write Protect	Power-Fail Comparator	Manual-Reset Input	Watchdog Output	Low-Line Output	Active-High Reset	Battery-On Output
MAX690A/692A	4.65/4.40	140	1.6	✔		✔					
MAX691A/693A	4.65/4.40	140/adj.	1.6/adj.	✔	✔/10ns	✔		✔	✔	✔	✔
MAX696	Adj.	35/adj.	1.6/adj.	✔		✔		✔	✔	✔	✔
MAX697	Adj.	35/adj.	1.6/adj.		✔	✔		✔	✔	✔	
MAX700	4.65/adj.	200	-				✔			✔	
MAX703/704	4.65/4.40	140	-	✔		✔	✔				
MAX705/706	4.65/4.40	140	1.6			✔	✔	✔			
MAX706P	2.63	140	1.6			✔	✔	✔		✔	
MAX706R/S/T	2.63/2.93/3.08	140	1.6			✔	✔	✔			
MAX707/708	4.65/4.40	140	-			✔	✔			✔	
MAX708R/S/T	2.63/2.93/3.08	140	-			✔	✔			✔	
MAX709L/M/R/S/T	4.65/4.40/2.63/2.93/3.08	140	-								
MAX791	4.65	140	1	✔	✔/10ns	✔	✔	✔	✔	✔	✔
MAX792L/M/R/S/T	4.65/4.40/2.63/2.93/3.08	140	1		✔/10ns	✔	✔	✔	✔	✔	
MAX800L/M	4.60/4.40	140	1.6/adj.	✔	✔/10ns	✔/±2%		✔	✔	✔	✔
MAX802L/M	4.60/4.40	140	1.6	✔		✔/±2%					
MAX805L	4.65	140	1.6	✔		✔				✔	
MAX813L	4.65	140	1.6			✔	✔	✔		✔	
MAX820L/M/R/S/T	4.65/4.40/2.63/2.93/3.08	140	1		✔/10ns	✔/±2%	✔	✔	✔	✔	
MAX1232	4.37/4.62	250	0.15/0.60/1.2				✔				
MAX1259	-	-	-	✔		✔					

Chip Topography



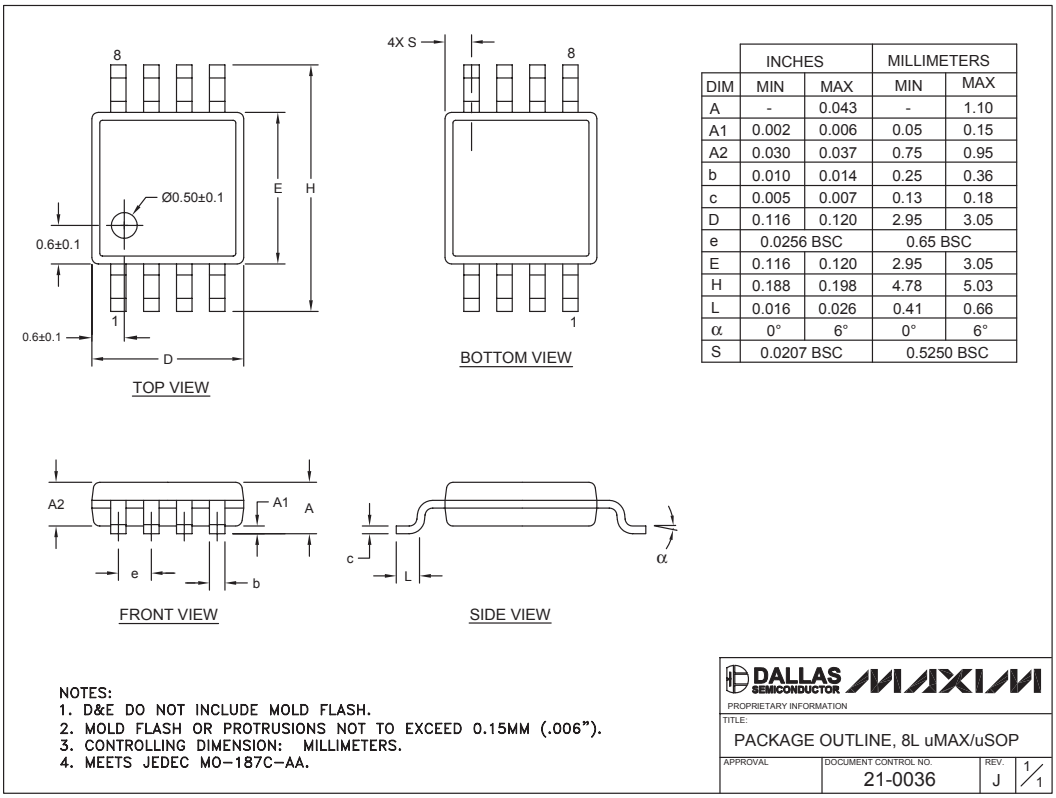
TRANSISTOR COUNT: 380

SUBSTRATE CONNECTED TO V_{CC}

Power-Supply Monitor with Reset

Package Information

(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information, go to **www.maxim-ic.com/packages**.)



	INCHES		MILLIMETERS	
DIM	MIN	MAX	MIN	MAX
A	-	0.043	-	1.10
A1	0.002	0.006	0.05	0.15
A2	0.030	0.037	0.75	0.95
b	0.010	0.014	0.25	0.36
c	0.005	0.007	0.13	0.18
D	0.116	0.120	2.95	3.05
e	0.0256 BSC		0.65 BSC	
E	0.116	0.120	2.95	3.05
H	0.188	0.198	4.78	5.03
L	0.016	0.026	0.41	0.66
α	0°	6°	0°	6°
S	0.0207 BSC		0.5250 BSC	

NOTES:
1. D&E DO NOT INCLUDE MOLD FLASH.
2. MOLD FLASH OR PROTRUSIONS NOT TO EXCEED 0.15MM (.006").
3. CONTROLLING DIMENSION: MILLIMETERS.
4. MEETS JEDEC MO-187C-AA.

DALLAS SEMICONDUCTOR MAXIM

PROPRIETARY INFORMATION

TITLE: PACKAGE OUTLINE, 8L uMAX/uSOP

APPROVAL	DOCUMENT CONTROL NO.	REV.	
	21-0036	J	1/1