

MAX3483E/MAX3485E/  
MAX3486E/MAX3488E/  
MAX3490E/MAX3491E

3.3V-Powered,  $\pm 15\text{kV}$  ESD-Protected,  
12Mbps and Slew-Rate-Limited  
True RS-485/RS-422 Transceivers

## Absolute Maximum Ratings

Supply Voltage ( $V_{CC}$ ).....+7V  
Control Input Voltage ( $\overline{RE}$ , DE).....-0.3V to +7V  
Driver Input Voltage (DI).....-0.3V to +7V  
Driver Output Voltage (A, B, Y, Z).....-7.5V to +12.5V  
Receiver Input Voltage (A, B).....-7.5V to +12.5V  
Receiver Output Voltage (RO).....-0.3V to ( $V_{CC} + 0.3\text{V}$ )  
Continuous Power Dissipation ( $T_A = +70^\circ\text{C}$ )  
    8-Pin SO (derate 5.88mW/ $^\circ\text{C}$  above  $+70^\circ\text{C}$ ).....471mW  
    8-Pin Plastic DIP (derate 9.09mW/ $^\circ\text{C}$  above  $+70^\circ\text{C}$ )..727mW

14-Pin SO (derate 8.33mW/ $^\circ\text{C}$  above  $+70^\circ\text{C}$ ) .....667mW  
14-Pin Plastic DIP (derate 10mW/ $^\circ\text{C}$  above  $+70^\circ\text{C}$ ) .....800mW  
Operating Temperature Ranges  
    MAX34\_ \_ EC\_ \_..... $0^\circ\text{C}$  to  $+70^\circ\text{C}$   
    MAX34\_ \_ EE\_ \_..... $-40^\circ\text{C}$  to  $+85^\circ\text{C}$   
Junction Temperature..... $+150^\circ\text{C}$   
Storage Temperature Range..... $-65^\circ\text{C}$  to  $+150^\circ\text{C}$   
Lead Temperature (soldering, 10sec) ..... $+300^\circ\text{C}$

*Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.*

## Package Information

### TDFN-14

| PACKAGE CODE                                   | S14+1                   |
|------------------------------------------------|-------------------------|
| Outline Number                                 | <a href="#">21-0041</a> |
| Land Pattern Number                            | <a href="#">90-0112</a> |
| <b>Thermal Resistance, Single-Layer Board:</b> |                         |
| Junction to Ambient ( $\theta_{JA}$ )          | $120^\circ\text{C/W}$   |
| Junction to Case ( $\theta_{JC}$ )             | $37^\circ\text{C/W}$    |
| <b>Thermal Resistance, Four-Layer Board:</b>   |                         |
| Junction to Ambient ( $\theta_{JA}$ )          | $84^\circ\text{C/W}$    |
| Junction to Case ( $\theta_{JC}$ )             | $34^\circ\text{C/W}$    |

For the latest package outline information and land patterns (footprints), go to [www.maximintegrated.com/packages](http://www.maximintegrated.com/packages). Note that a "+", "#", or "-" in the package code indicates RoHS status only. Package drawings may show a different suffix character, but the drawing pertains to the package regardless of RoHS status.

Package thermal resistances were obtained using the method described in JEDEC specification JESD51-7, using a four-layer board. For detailed information on package thermal considerations, refer to [www.maximintegrated.com/thermal-tutorial](http://www.maximintegrated.com/thermal-tutorial).

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## DC Electrical Characteristics

( $V_{CC} = +3.3\text{V} \pm 0.3\text{V}$ ,  $T_A = T_{MIN}$  to  $T_{MAX}$ , unless otherwise noted. Typical values are at  $T_A = +25^\circ\text{C}$ .)

| PARAMETER                                                                                          | SYMBOL          | CONDITIONS                                                                                 | MIN                    | TYP     | MAX     | UNITS         |
|----------------------------------------------------------------------------------------------------|-----------------|--------------------------------------------------------------------------------------------|------------------------|---------|---------|---------------|
| Differential Driver Output                                                                         | $V_{OD}$        | $R_L = 100\Omega$ (RS-422), Figure 4                                                       | 2.0                    |         |         | V             |
|                                                                                                    |                 | $R_L = 54\Omega$ (RS-485), Figure 4                                                        | 1.5                    |         |         |               |
|                                                                                                    |                 | $R_L = 60\Omega$ (RS-485), $V_{CC} = 3.3\text{V}$ , Figure 5                               | 1.5                    |         |         |               |
| Change in Magnitude of Driver Differential Output Voltage for Complementary Output States (Note 1) | $\Delta V_{OD}$ | $R_L = 54\Omega$ or $100\Omega$ , Figure 4                                                 |                        |         | 0.2     | V             |
| Driver Common-Mode Output Voltage                                                                  | $V_{OC}$        | $R_L = 54\Omega$ or $100\Omega$ , Figure 4                                                 |                        |         | 3       | V             |
| Change in Magnitude of Common-Mode Output Voltage (Note 1)                                         | $\Delta V_{OC}$ | $R_L = 54\Omega$ or $100\Omega$ , Figure 4                                                 |                        |         | 0.2     | V             |
| Input High Voltage                                                                                 | $V_{IH}$        | DE, DI, $\overline{RE}$                                                                    | 2.0                    |         |         | V             |
| Input Low Voltage                                                                                  | $V_{IL}$        | DE, DI, $\overline{RE}$                                                                    |                        |         | 0.8     | V             |
| Logic Input Current                                                                                | $I_{IN1}$       | DE, DI, $\overline{RE}$                                                                    |                        |         | $\pm 2$ | $\mu\text{A}$ |
| Input Current (A, B)                                                                               | $I_{IN2}$       | DE = 0V,<br>$V_{CC} = 0\text{V}$ or $3.6\text{V}$                                          | $V_{IN} = 12\text{V}$  |         | 1.0     | mA            |
|                                                                                                    |                 |                                                                                            | $V_{IN} = -7\text{V}$  |         | -0.8    |               |
| Output Leakage (Y, Z)                                                                              | $I_O$           | DE = 0V, $\overline{RE} = 0\text{V}$ ,<br>$V_{CC} = 0\text{V}$ or $3.6\text{V}$ , MAX3491E | $V_{OUT} = 12\text{V}$ |         | 20      | $\mu\text{A}$ |
|                                                                                                    |                 |                                                                                            | $V_{OUT} = -7\text{V}$ |         | -20     |               |
| Output Leakage (Y, Z) in Shutdown Mode                                                             | $I_O$           | DE = 0V, $\overline{RE} = V_{CC}$ ,<br>$V_{CC} = 0\text{V}$ or $3.6\text{V}$ , MAX3491E    | $V_{OUT} = 12\text{V}$ |         | 1       | $\mu\text{A}$ |
|                                                                                                    |                 |                                                                                            | $V_{OUT} = -7\text{V}$ |         | -1      |               |
| Receiver Differential Threshold Voltage                                                            | $V_{TH}$        | $-7\text{V} \leq V_{CM} \leq 12\text{V}$                                                   | -0.2                   |         | 0.2     | V             |
| Receiver Input Hysteresis                                                                          | $\Delta V_{TH}$ | $V_{CM} = 0\text{V}$                                                                       |                        | 50      |         | mV            |
| Receiver Output High Voltage                                                                       | $V_{OH}$        | $I_{OUT} = -1.5\text{mA}$ , $V_{ID} = 200\text{mV}$ , Figure 6                             | $V_{CC} - 0.4$         |         |         | V             |
| Receiver Output Low Voltage                                                                        | $V_{OL}$        | $I_{OUT} = 2.5\text{mA}$ , $V_{ID} = 200\text{mV}$ , Figure 6                              |                        | 0.4     |         | V             |
| Three-State (High Impedance) Output Current at Receiver                                            | $I_{OZR}$       | $V_{CC} = 3.6\text{V}$ , $0\text{V} \leq V_{OUT} \leq V_{CC}$                              |                        | $\pm 1$ |         | $\mu\text{A}$ |
| Receiver Input Resistance                                                                          | $R_{IN}$        | $-7\text{V} \leq V_{CM} \leq 12\text{V}$                                                   | 12                     |         |         | k $\Omega$    |
| Supply Voltage Range                                                                               | $V_{CC}$        |                                                                                            | 3.0                    |         | 3.6     | V             |

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## DC Electrical Characteristics (continued)

( $V_{CC} = +3.3\text{V} \pm 0.3\text{V}$ ,  $T_A = T_{MIN}$  to  $T_{MAX}$ , unless otherwise noted. Typical values are at  $T_A = +25^\circ\text{C}$ )

| PARAMETER                             | SYMBOL     | CONDITIONS                                                              | MIN                                             | TYP      | MAX      | UNITS         |
|---------------------------------------|------------|-------------------------------------------------------------------------|-------------------------------------------------|----------|----------|---------------|
| Supply Current                        | $I_{CC}$   | No load,<br>DI = 0 or $V_{CC}$                                          | DE = $V_{CC}$ , $\overline{RE} = 0$ or $V_{CC}$ | 1.1      | 2.2      | mA            |
|                                       |            |                                                                         | DE = 0V, $\overline{RE} = 0$                    | 0.95     | 1.9      |               |
| Supply Current in Shutdown Mode       | $I_{SHDN}$ | DE = 0, $\overline{RE} = V_{CC}$ , DI = $V_{CC}$ or 0                   |                                                 | 0.002    | 1        | $\mu\text{A}$ |
| Driver Short-Circuit Output Current   | $I_{OSD}$  | $V_{OUT} = -7\text{V}$                                                  |                                                 |          | -250     | mA            |
|                                       |            | $V_{OUT} = 12\text{V}$                                                  |                                                 |          | 250      |               |
| Receiver Short-Circuit Output Current | $I_{OSR}$  | $0 \leq V_{RO} \leq V_{CC}$                                             | $\pm 8$                                         |          | $\pm 60$ | mA            |
| ESD Protection for Y, Z, A, B         |            | IEC 1000-4-2 Air Discharge                                              |                                                 | $\pm 15$ |          | kV            |
|                                       |            | IEC 1000-4-2 Contact Discharge (MAX3483E, MAX3485E, MAX3486E, MAX3491E) |                                                 | $\pm 8$  |          |               |
|                                       |            | IEC 1000-4-2 Contact Discharge (MAX3490E, MAX3488E)*                    |                                                 | $\pm 6$  |          |               |
|                                       |            | Human Body Model                                                        |                                                 | $\pm 15$ |          |               |

\*MAX3488E and MAX3491E will be compliant to  $\pm 8\text{kV}$  per IEC 1000-4-2 Contact Discharge by September 1999.

## Driver Switching Characteristics—MAX3485E/MAX3490E/MAX3491E

( $V_{CC} = +3.3\text{V}$ ,  $T_A = +25^\circ\text{C}$ .)

| PARAMETER                                                          | SYMBOL    | CONDITIONS                    | MIN | TYP  | MAX     | UNITS |
|--------------------------------------------------------------------|-----------|-------------------------------|-----|------|---------|-------|
| Maximum Data Rate                                                  |           |                               | 12  | 15   |         | Mbps  |
| Driver Differential Output Delay                                   | $t_{DD}$  | $R_L = 60\Omega$ , Figure 7   | 1   | 22   | 35      | ns    |
| Driver Differential Output Transition Time                         | $t_{TD}$  | $R_L = 60\Omega$ , Figure 7   | 3   | 11   | 25      | ns    |
| Driver Propagation Delay, Low-to-High Level                        | $t_{PLH}$ | $R_L = 27\Omega$ , Figure 8   | 7   | 23   | 35      | ns    |
| Driver Propagation Delay, High-to-Low Level                        | $t_{PHL}$ | $R_L = 27\Omega$ , Figure 8   | 7   | 23   | 35      | ns    |
| $ t_{PLH} - t_{PHL} $ Driver Propagation Delay Skew (Note 2)       | $t_{PDS}$ | $R_L = 27\Omega$ , Figure 8   |     | -1.4 | $\pm 8$ | ns    |
| <b>DRIVER-OUTPUT ENABLE/DISABLE TIMES (MAX3485E/MAX3491E only)</b> |           |                               |     |      |         |       |
| Driver-Output Enable Time to Low Level                             | $t_{PZL}$ | $R_L = 110\Omega$ , Figure 10 |     | 42   | 90      | ns    |
| Driver-Output Enable Time to High Level                            | $t_{PZH}$ | $R_L = 110\Omega$ , Figure 9  |     | 42   | 90      | ns    |
| Driver-Output Disable Time from High Level                         | $t_{PHZ}$ | $R_L = 110\Omega$ , Figure 9  |     | 35   | 80      | ns    |
| Driver-Output Disable Time from Low Level                          | $t_{PLZ}$ | $R_L = 110\Omega$ , Figure 10 |     | 35   | 80      | ns    |
| Driver-Output Enable Time from Shutdown to Low Level               | $t_{PSL}$ | $R_L = 110\Omega$ , Figure 10 |     | 650  | 900     | ns    |
| Driver-Output Enable Time from Shutdown to High Level              | $t_{PSH}$ | $R_L = 110\Omega$ , Figure 9  |     | 650  | 900     | ns    |

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### Driver Switching Characteristics—MAX3486E

( $V_{CC} = +3.3\text{V}$ ,  $T_A = +25^\circ\text{C}$ .)

| PARAMETER                                                    | SYMBOL    | CONDITIONS                    | MIN | TYP | MAX      | UNITS |
|--------------------------------------------------------------|-----------|-------------------------------|-----|-----|----------|-------|
| Maximum Data Rate                                            |           |                               | 2.5 |     |          | Mbps  |
| Driver Differential Output Delay                             | $t_{DD}$  | $R_L = 60\Omega$ , Figure 7   | 20  | 42  | 70       | ns    |
| Driver Differential Output Transition Time                   | $t_{TD}$  | $R_L = 60\Omega$ , Figure 7   | 15  | 28  | 60       | ns    |
| Driver Propagation Delay, Low-to-High Level                  | $t_{PLH}$ | $R_L = 27\Omega$ , Figure 8   | 20  | 42  | 75       | ns    |
| Driver Propagation Delay, High-to-Low Level                  | $t_{PHL}$ | $R_L = 27\Omega$ , Figure 8   | 20  | 42  | 75       | ns    |
| $ t_{PLH} - t_{PHL} $ Driver Propagation Delay Skew (Note 2) | $t_{PDS}$ | $R_L = 27\Omega$ , Figure 8   |     | -6  | $\pm 12$ | ns    |
| <b>DRIVER-OUTPUT ENABLE/DISABLE TIMES</b>                    |           |                               |     |     |          |       |
| Driver Output Enable Time to Low Level                       | $t_{PZL}$ | $R_L = 110\Omega$ , Figure 10 |     | 52  | 100      | ns    |
| Driver Output Enable Time to High Level                      | $t_{PZH}$ | $R_L = 110\Omega$ , Figure 9  |     | 52  | 100      | ns    |
| Driver Output Disable Time from High Level                   | $t_{PHZ}$ | $R_L = 110\Omega$ , Figure 9  |     | 40  | 80       | ns    |
| Driver Output Disable Time from Low Level                    | $t_{PLZ}$ | $R_L = 110\Omega$ , Figure 10 |     | 40  | 80       | ns    |
| Driver Output Enable Time from Shutdown to Low Level         | $t_{PSL}$ | $R_L = 110\Omega$ , Figure 10 |     | 700 | 1000     | ns    |
| Driver Output Enable Time from Shutdown to High Level        | $t_{PSH}$ | $R_L = 110\Omega$ , Figure 9  |     | 700 | 1000     | ns    |

### Driver Switching Characteristics—MAX3483E/MAX3488E

( $V_{CC} = +3.3\text{V}$ ,  $T_A = +25^\circ\text{C}$ .)

| PARAMETER                                                    | SYMBOL    | CONDITIONS                    | MIN | TYP      | MAX  | UNITS         |
|--------------------------------------------------------------|-----------|-------------------------------|-----|----------|------|---------------|
| Maximum Data Rate                                            |           |                               | 250 |          |      | kbps          |
| Driver Differential Output Delay                             | $t_{DD}$  | $R_L = 60\Omega$ , Figure 7   | 600 | 900      | 1400 | ns            |
| Driver Differential Output Transition Time                   | $t_{TD}$  | $R_L = 60\Omega$ , Figure 7   | 400 | 740      | 1200 | ns            |
| Driver Propagation Delay, Low-to-High Level                  | $t_{PLH}$ | $R_L = 27\Omega$ , Figure 8   | 700 | 930      | 1500 | ns            |
| Driver Propagation Delay, High-to-Low Level                  | $t_{PHL}$ | $R_L = 27\Omega$ , Figure 8   | 700 | 930      | 1500 | ns            |
| $ t_{PLH} - t_{PHL} $ Driver Propagation Delay Skew (Note 2) | $t_{PDS}$ | $R_L = 27\Omega$ , Figure 8   |     | $\pm 50$ |      | ns            |
| <b>DRIVER-OUTPUT ENABLE/DISABLE TIMES (MAX3483E only)</b>    |           |                               |     |          |      |               |
| Driver-Output Enable Time to Low Level                       | $t_{PZL}$ | $R_L = 110\Omega$ , Figure 10 |     | 900      | 1300 | ns            |
| Driver-Output Enable Time to High Level                      | $t_{PZH}$ | $R_L = 110\Omega$ , Figure 9  |     | 600      | 800  | ns            |
| Driver-Output Disable Time from High Level                   | $t_{PHZ}$ | $R_L = 110\Omega$ , Figure 9  |     | 50       | 80   | ns            |
| Driver-Output Disable Time from Low Level                    | $t_{PLZ}$ | $R_L = 110\Omega$ , Figure 10 |     | 50       | 80   | ns            |
| Driver-Output Enable Time from Shutdown to Low Level         | $t_{PSL}$ | $R_L = 110\Omega$ , Figure 10 |     | 1.9      | 2.7  | $\mu\text{s}$ |
| Driver-Output Enable Time from Shutdown to High Level        | $t_{PSH}$ | $R_L = 110\Omega$ , Figure 9  |     | 2.2      | 3.0  | $\mu\text{s}$ |

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## Receiver Switching Characteristics

( $V_{CC} = +3.3\text{V}$ ,  $T_A = +25^\circ\text{C}$ )

| PARAMETER                                               | SYMBOL     | CONDITIONS                                                 | MIN | TYP | MAX      | UNITS |
|---------------------------------------------------------|------------|------------------------------------------------------------|-----|-----|----------|-------|
| Time to Shutdown                                        | $t_{SHDN}$ | MAX3483E/MAX3485E/MAX3486E/MAX3491E only (Note 3)          | 80  | 190 | 300      | ns    |
| Receiver Propagation Delay, Low-to-High Level           | $t_{RPLH}$ | $V_{ID} = 0$ to 3.0, $C_L = 15\text{pF}$ , Figure 11       | 25  | 62  | 90       | ns    |
|                                                         |            | MAX3483E/MAX3488E                                          | 25  | 75  | 120      |       |
| Receiver Propagation Delay, High-to-Low Level           | $t_{RPHL}$ | $V_{ID} = 0$ to 3.0, $C_L = 15\text{pF}$ , Figure 11       | 25  | 62  | 90       | ns    |
|                                                         |            | MAX3483E/MAX3488E                                          | 25  | 75  | 120      |       |
| $ t_{PLH} - t_{PHL} $ Receiver Propagation Delay Skew   | $t_{RPDS}$ | $V_{ID} = 0$ to 3.0, $C_L = 15\text{pF}$ , Figure 11       |     | 6   | $\pm 10$ | ns    |
|                                                         |            | MAX3483E/MAX3488E                                          |     | 12  | $\pm 20$ |       |
| Receiver Output Enable Time to Low Level                | $t_{PRZL}$ | $C_L = 15\text{pF}$ , Figure 12, MAX3483E/85E/86E/91E only |     | 25  | 50       | ns    |
| Receiver Output Enable Time to High Level               | $t_{PRZH}$ | $C_L = 15\text{pF}$ , Figure 12, MAX3483E/85E/86E/91E only |     | 25  | 50       | ns    |
| Receiver Output Disable Time from High Level            | $t_{PRHZ}$ | $C_L = 15\text{pF}$ , Figure 12, MAX3483E/85E/86E/91E only |     | 25  | 45       | ns    |
| Receiver Output Disable Time from Low Level             | $t_{PRLZ}$ | $C_L = 15\text{pF}$ , Figure 12, MAX3483E/85E/86E/91E only |     | 25  | 45       | ns    |
| Receiver Output Enable Time from Shutdown to Low Level  | $t_{PRSL}$ | $C_L = 15\text{pF}$ , Figure 12, MAX3483E/85E/86E/91E only |     | 720 | 1400     | ns    |
| Receiver Output Enable Time from Shutdown to High Level | $t_{PRSH}$ | $C_L = 15\text{pF}$ , Figure 12, MAX3483E/85E/86E/91E only |     | 720 | 1400     | ns    |

**Note 1:**  $\Delta V_{OD}$  and  $\Delta V_{OC}$  are the changes in  $V_{OD}$  and  $V_{OC}$ , respectively, when the DI input changes state.

**Note 2:** Measured on  $|t_{PLH}(Y) - t_{PHL}(Y)|$  and  $|t_{PLH}(Z) - t_{PHL}(Z)|$ .

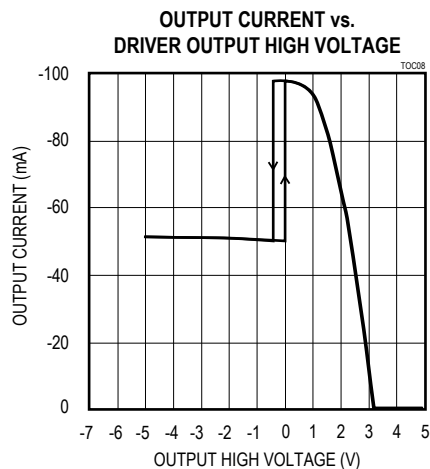
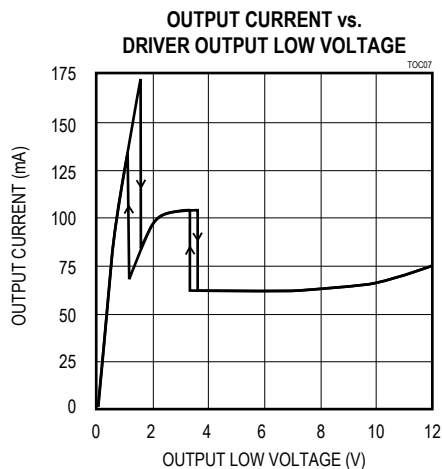
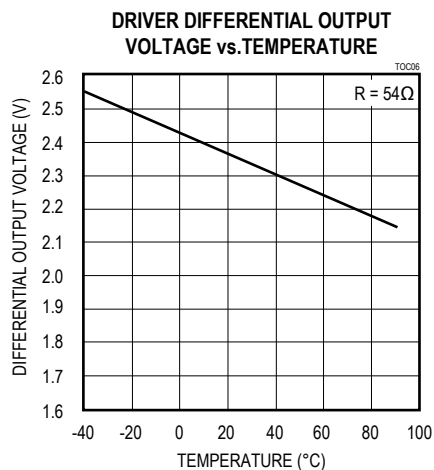
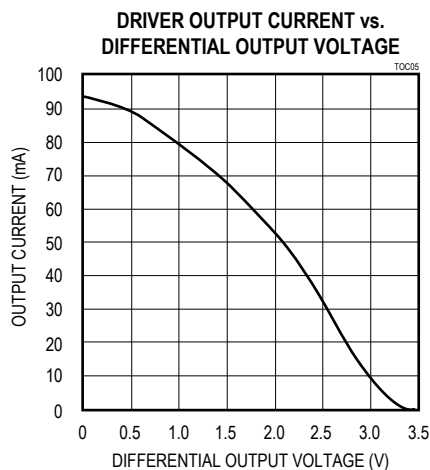
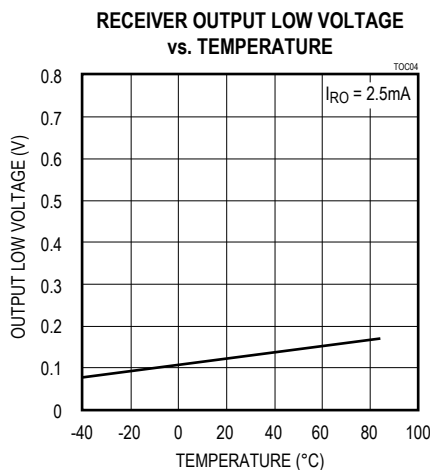
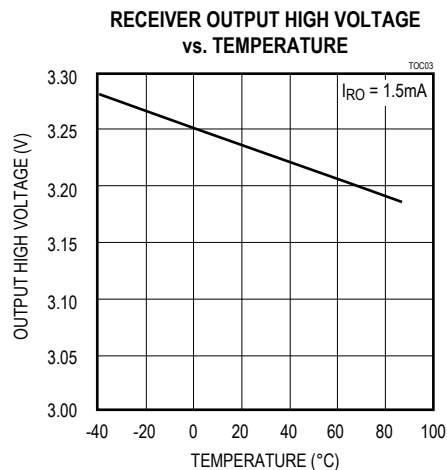
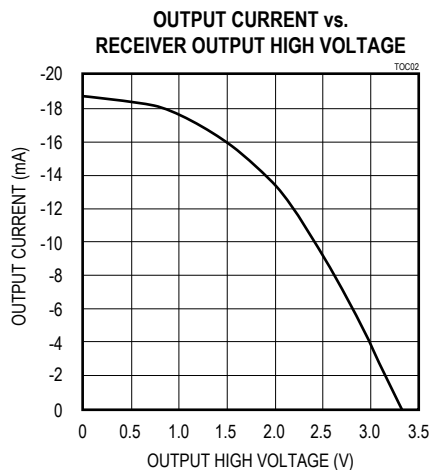
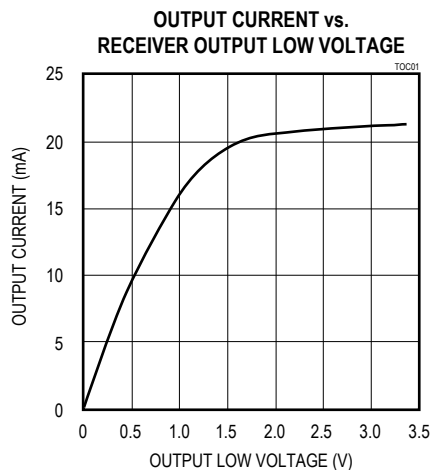
**Note 3:** The transceivers are put into shutdown by bringing  $\overline{RE}$  high and  $\overline{DE}$  low. If the inputs are in this state for less than 80ns, the parts are guaranteed not to enter shutdown. If the inputs are in this state for at least 300ns, the parts are guaranteed to have entered shutdown. See *Low-Power Shutdown Mode* section.

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## Typical Operating Characteristics

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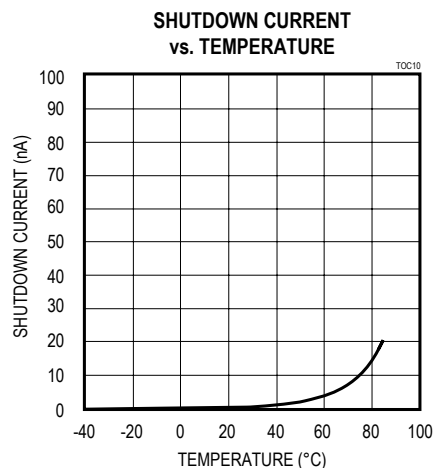
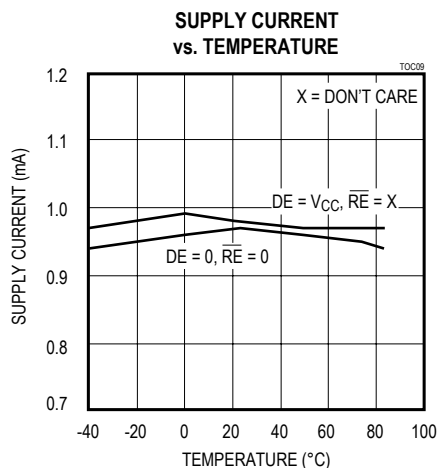


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## Typical Operating Characteristics (continued)

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## Pin Description

| PIN                              |                      |          | NAME                   | FUNCTION                                                                                                                                                                                                                                                                                                                                                                                    |
|----------------------------------|----------------------|----------|------------------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| MAX3483E<br>MAX3485E<br>MAX3486E | MAX3488E<br>MAX3490E | MAX3491E |                        |                                                                                                                                                                                                                                                                                                                                                                                             |
| 1                                | 2                    | 2        | RO                     | Receiver Output. If $A > B$ by 200mV, RO will be high; if $A < B$ by 200mV, RO will be low.                                                                                                                                                                                                                                                                                                 |
| 2                                | —                    | 3        | $\overline{\text{RE}}$ | Receiver Output Enable. RO is enabled when $\overline{\text{RE}}$ is low; RO is high impedance when $\overline{\text{RE}}$ is high. If $\overline{\text{RE}}$ is high and DE is low, the device will enter a low-power shutdown mode.                                                                                                                                                       |
| 3                                | —                    | 4        | DE                     | Driver Output Enable. The driver outputs are enabled by bringing DE high. They are high impedance when DE is low. If $\overline{\text{RE}}$ is high and DE is low, the device will enter a low-power shutdown mode. If the driver outputs are enabled, the parts function as line drivers. While they are high impedance, they function as line receivers if $\overline{\text{RE}}$ is low. |
| 4                                | 3                    | 5        | DI                     | Driver Input. A low on DI forces output Y low and output Z high. Similarly, a high on DI forces output Y high and output Z low.                                                                                                                                                                                                                                                             |
| 5                                | 4                    | 6, 7     | GND                    | Ground                                                                                                                                                                                                                                                                                                                                                                                      |
| —                                | 5                    | 9        | Y                      | Noninverting Driver Output                                                                                                                                                                                                                                                                                                                                                                  |
| —                                | 6                    | 10       | Z                      | Inverting Driver Output                                                                                                                                                                                                                                                                                                                                                                     |
| 6                                | —                    | —        | A                      | Noninverting Receiver Input and Noninverting Driver Output                                                                                                                                                                                                                                                                                                                                  |
| —                                | 8                    | 12       | A                      | Noninverting Receiver Input                                                                                                                                                                                                                                                                                                                                                                 |
| 7                                | —                    | —        | B                      | Inverting Receiver Input and Inverting Driver Output                                                                                                                                                                                                                                                                                                                                        |
| —                                | 7                    | 11       | B                      | Inverting Receiver Input                                                                                                                                                                                                                                                                                                                                                                    |
| 8                                | 1                    | 13, 14   | $V_{CC}$               | Positive Supply: $3.0\text{V} \leq V_{CC} \leq 3.6\text{V}$ . Do not operate device with $V_{CC} > 3.6\text{V}$                                                                                                                                                                                                                                                                             |
| —                                | —                    | 1, 8     | N.C.                   | No Connection. Not internally connected.                                                                                                                                                                                                                                                                                                                                                    |

MAX3483E/MAX3485E/  
MAX3486E/MAX3488E/  
MAX3490E/MAX3491E

3.3V-Powered,  $\pm 15\text{kV}$  ESD-Protected,  
12Mbps and Slew-Rate-Limited  
True RS-485/RS-422 Transceivers

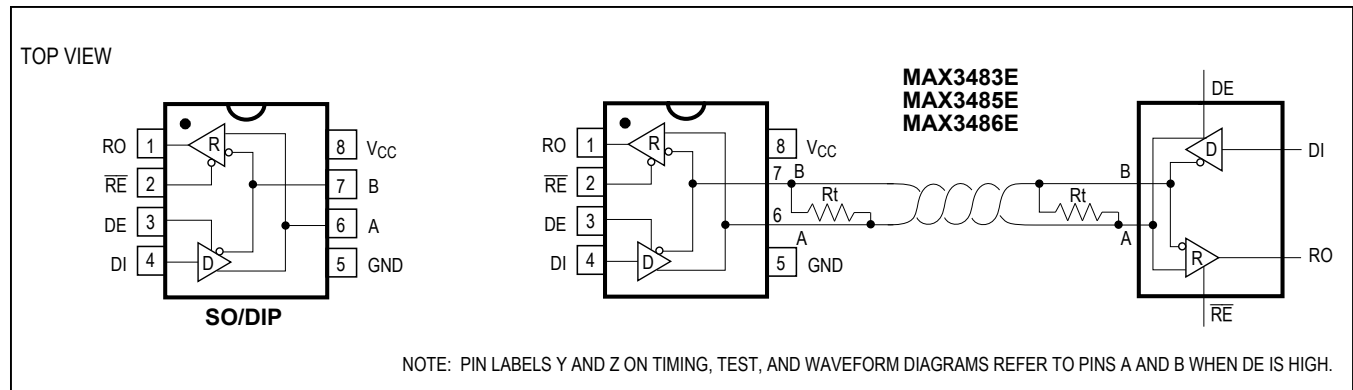


Figure 1. MAX3483E/MAX3485E/MAX3486E Pin Configuration and Typical Operating Circuit

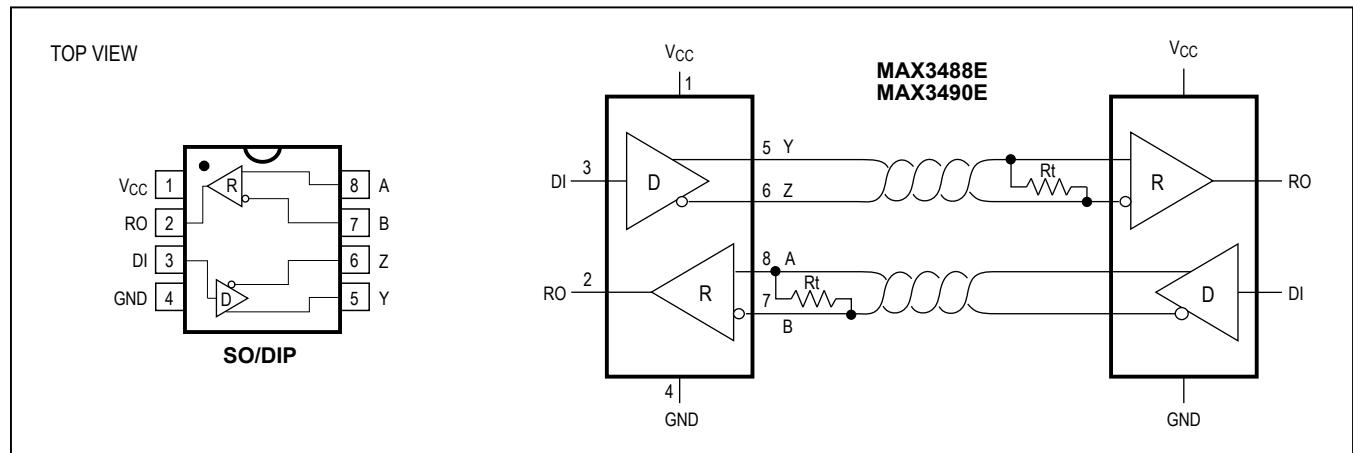


Figure 2. MAX3488E/MAX3490E Pin Configuration and Typical Operating Circuit

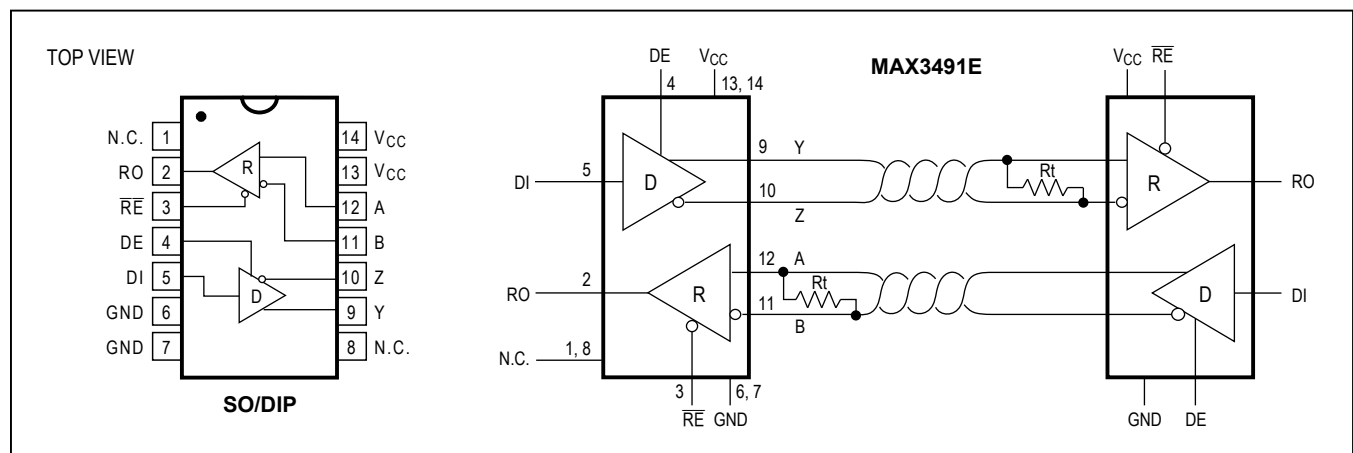


Figure 3. MAX3491E Pin Configuration and Typical Operating Circuit



MAX3483E/MAX3485E/  
MAX3486E/MAX3488E/  
MAX3490E/MAX3491E

3.3V-Powered,  $\pm 15\text{kV}$  ESD-Protected,  
12Mbps and Slew-Rate-Limited  
True RS-485/RS-422 Transceivers

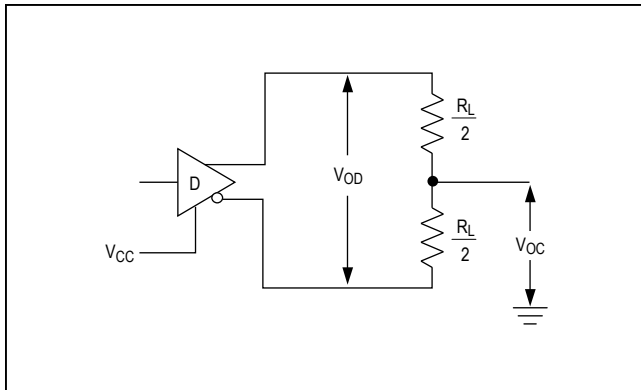


Figure 4. Driver  $V_{OD}$  and  $V_{OC}$

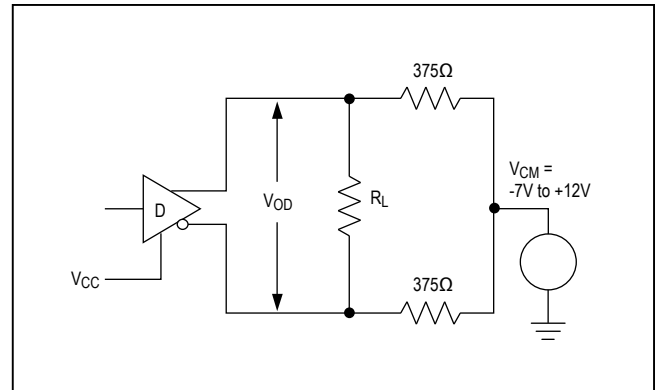


Figure 5. Driver  $V_{OD}$  with Varying Common-Mode Voltage

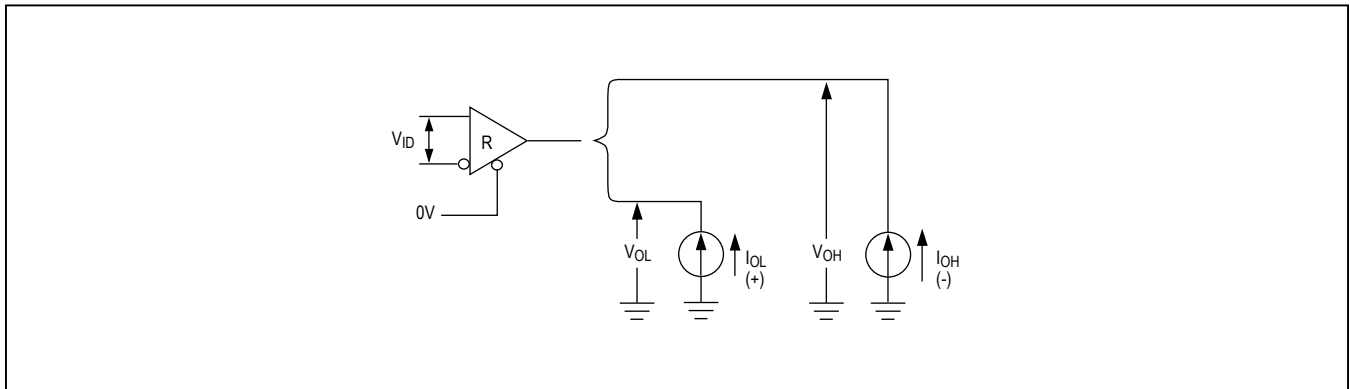


Figure 6. Receiver  $V_{OH}$  and  $V_{OL}$

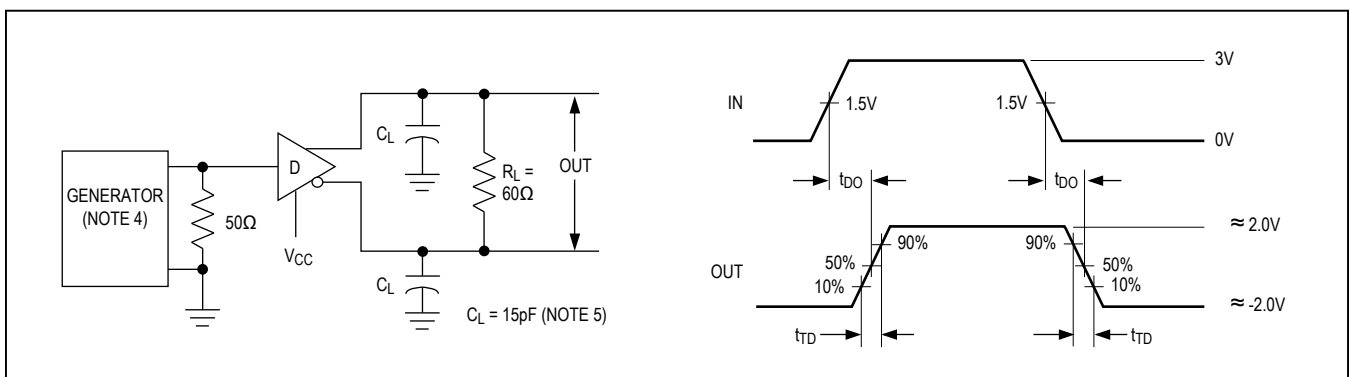


Figure 7. Driver Differential Output Delay and Transition Times

MAX3483E/MAX3485E/  
MAX3486E/MAX3488E/  
MAX3490E/MAX3491E

3.3V-Powered,  $\pm 15\text{kV}$  ESD-Protected,  
12Mbps and Slew-Rate-Limited  
True RS-485/RS-422 Transceivers

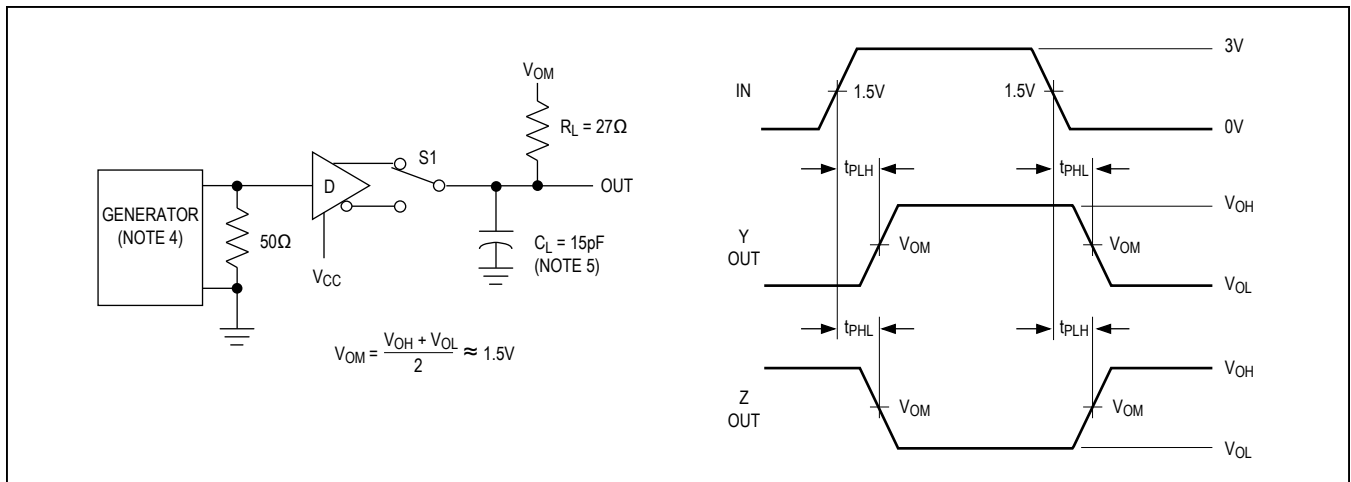


Figure 8. Driver Propagation Times

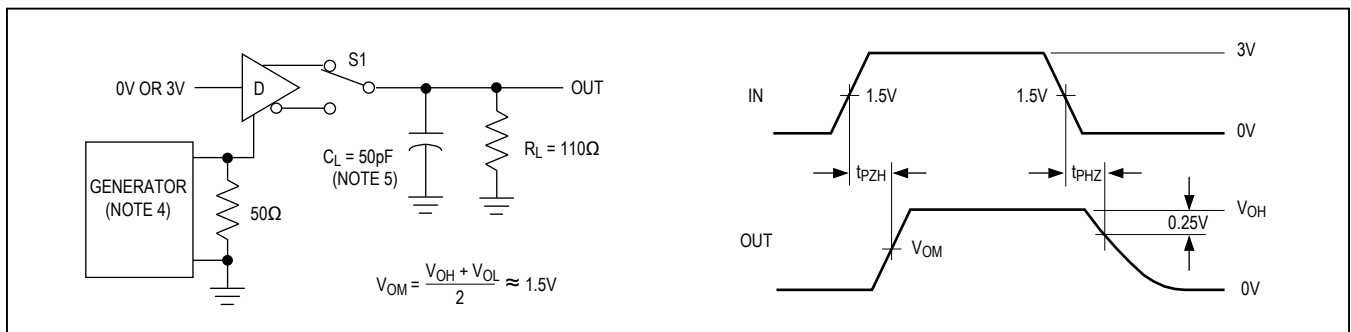


Figure 9. Driver Enable and Disable Times (tPZH, tPSH, tPHZ)

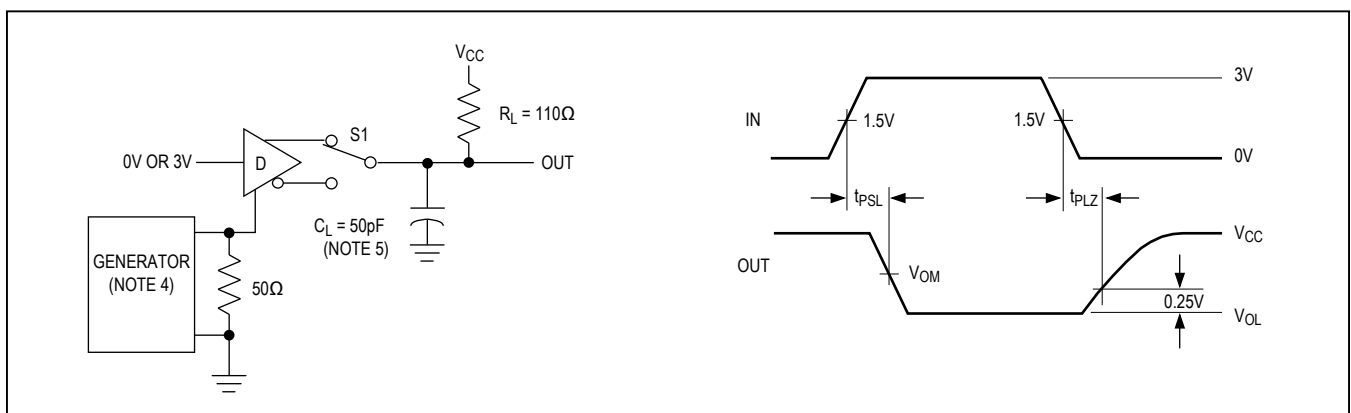


Figure 10. Driver Enable and Disable Times (tPZL, tPSL, tPLZ)

MAX3483E/MAX3485E/  
MAX3486E/MAX3488E/  
MAX3490E/MAX3491E

3.3V-Powered,  $\pm 15\text{kV}$  ESD-Protected,  
12Mbps and Slew-Rate-Limited  
True RS-485/RS-422 Transceivers

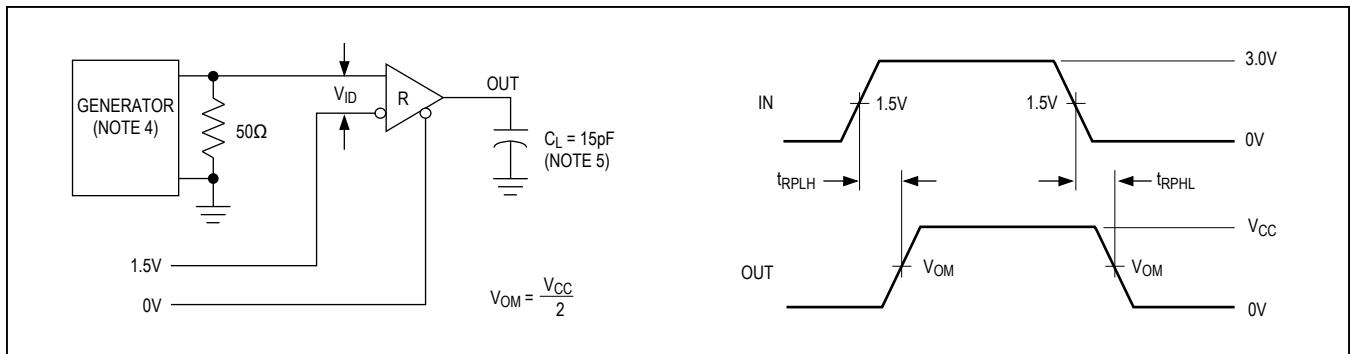


Figure 11. Receiver Propagation Delay

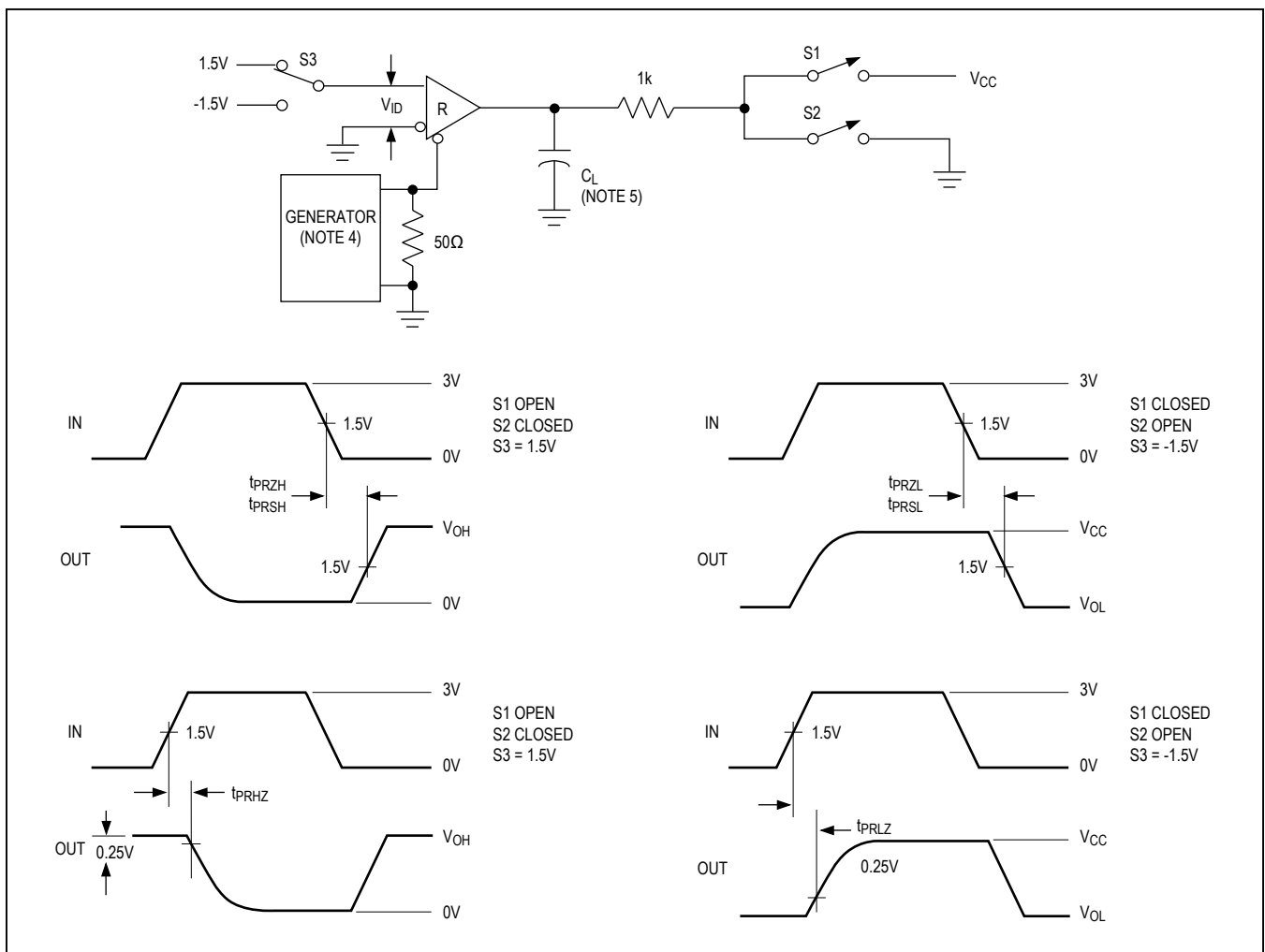


Figure 12. Receiver Enable and Disable Times

**Note 4:** The input pulse is supplied by a generator with the following characteristics:  $f = 250\text{kHz}$ , 50% duty cycle,  $t_r \leq 6.0\text{ns}$ ,  $Z_O = 50\Omega$ .

**Note 5:**  $C_L$  includes probe and stray capacitance.

MAX3483E/MAX3485E/  
MAX3486E/MAX3488E/  
MAX3490E/MAX3491E

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12Mbps and Slew-Rate-Limited  
True RS-485/RS-422 Transceivers

## Function Tables

**Devices with Receiver/Driver Enable  
(MAX3483E/MAX3485E/MAX3486E/MAX3491E)**

**Table 1. Transmitting**

| INPUTS                 |    |    | OUTPUTS |        | MODE     |
|------------------------|----|----|---------|--------|----------|
| $\overline{\text{RE}}$ | DE | DI | B*      | A*     |          |
| X                      | 1  | 1  | 0       | 1      | Normal   |
| X                      | 1  | 0  | 1       | 0      | Normal   |
| 0                      | 0  | X  | High-Z  | High-Z | Normal   |
| 1                      | 0  | X  | High-Z  | High-Z | Shutdown |

\* B and A outputs are Z and Y, respectively, for full-duplex part (MAX3491E).

X = Don't care; High-Z = High impedance

**Table 2. Receiving**

| INPUTS                 |    |                     | OUTPUTS | MODE     |
|------------------------|----|---------------------|---------|----------|
| $\overline{\text{RE}}$ | DE | A, B                | RO      |          |
| 0                      | 0* | $\geq +0.2\text{V}$ | 1       | Normal   |
| 0                      | 0* | $\leq -0.2\text{V}$ | 0       | Normal   |
| 0                      | 0* | Inputs Open         | 1       | Normal   |
| 1                      | 0  | X                   | High-Z  | Shutdown |

\* DE is a "don't care" (x) for the full-duplex part (MAX3491E).

X = Don't care; High-Z = High impedance

**Devices without Receiver/Driver Enable  
(MAX3488E/MAX3490E)**

**Table 3. Transmitting    Table 4. Receiving**

| INPUT | OUTPUTS |   |
|-------|---------|---|
| DI    | Z       | Y |
| 1     | 0       | 1 |
| 0     | 1       | 0 |

| INPUTS              | OUTPUT |
|---------------------|--------|
| A, B                | RO     |
| $\geq +0.2\text{V}$ | 1      |
| $\leq -0.2\text{V}$ | 0      |
| Inputs Open         | 1      |

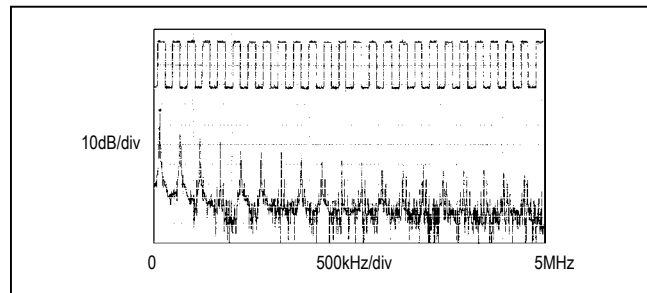


Figure 13. Driver Output Waveform and FFT Plot of MAX3485E/MAX3490E/MAX3491E Transmitting a 125kHz Signal

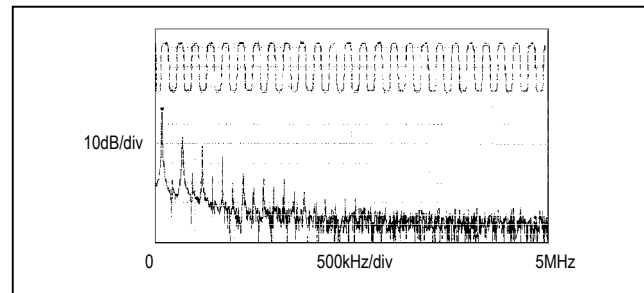


Figure 14. Driver Output Waveform and FFT Plot of MAX3483E/MAX3488E Transmitting a 125kHz Signal

## Applications Information

The MAX3483E/MAX3485E/MAX3486E/MAX3488E/MAX3490E/MAX3491E are low-power transceivers for RS-485 and RS-422 communications. The MAX3483E and MAX3488E can transmit and receive at data rates up to 250kbps, the MAX3486E at up to 2.5Mbps, and the MAX3485E/MAX3490E/MAX3491E at up to 12Mbps. The MAX3488E/MAX3490E/MAX3491E are full-duplex transceivers, while the MAX3483E/MAX3485E/MAX3486E are half-duplex. Driver Enable (DE) and Receiver Enable ( $\overline{\text{RE}}$ ) pins are included on the MAX3483E/MAX3485E/MAX3486E/MAX3491E. When disabled, the driver and receiver outputs are high impedance.

## Reduced EMI and Reflections (MAX3483E/MAX3486E/MAX3488E)

The MAX3483E/MAX3488E are slew-rate limited, minimizing EMI and reducing reflections caused by improperly terminated cables. Figure 13 shows the driver output waveform of a MAX3485E/MAX3490E/MAX3491E transmitting a 125kHz signal, as well as the Fourier analysis of that waveform. High-frequency harmonics with large amplitudes are evident. Figure 14 shows the same information, but for the slew-rate-limited MAX3483E/MAX3488E transmitting the same signal. The high-frequency harmonics have much lower amplitudes, and the potential for EMI is significantly reduced.

## Low-Power Shutdown Mode (MAX3483E/MAX3485E/MAX3486E/MAX3491E)

A low-power shutdown mode is initiated by bringing both  $\overline{\text{RE}}$  high and DE low. The devices will not shut down unless both the driver and receiver are disabled (high impedance). In shutdown, the devices typically draw only 2nA of supply current.

For these devices, the  $t_{\text{PSH}}$  and  $t_{\text{PSL}}$  enable times assume the part was in the low-power shutdown mode; the  $t_{\text{PZH}}$  and  $t_{\text{PZL}}$  enable times assume the receiver or driver was disabled, but the part was not shut down.

MAX3483E/MAX3485E/  
MAX3486E/MAX3488E/  
MAX3490E/MAX3491E

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12Mbps and Slew-Rate-Limited  
True RS-485/RS-422 Transceivers

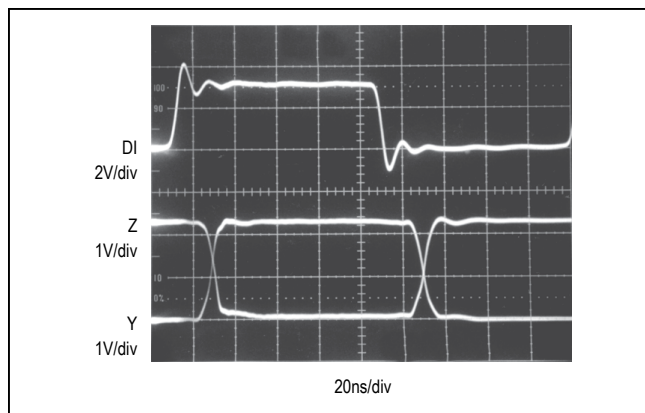


Figure 15. MAX3485E/MAX3490E/MAX3491E Driver Propagation Delay

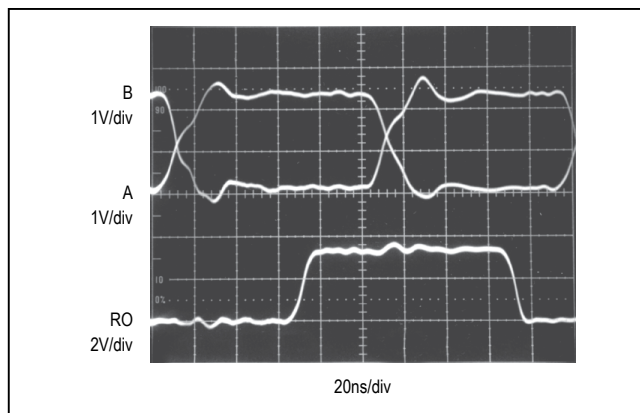


Figure 16. MAX3485E/MAX3490E/MAX3491E Receiver Propagation Delay Driven by External RS-485 Device

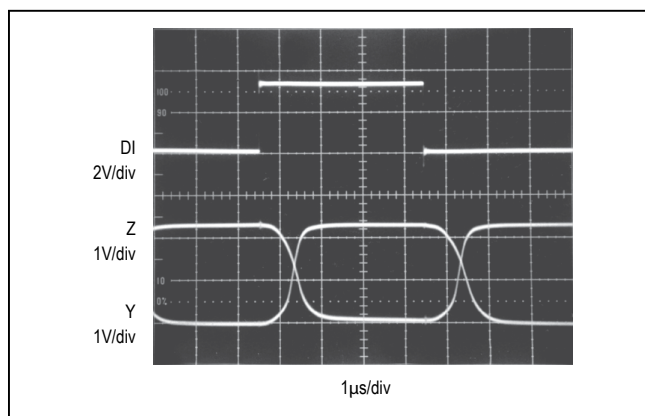


Figure 17. MAX3483E/MAX3488E Driver Propagation Delay

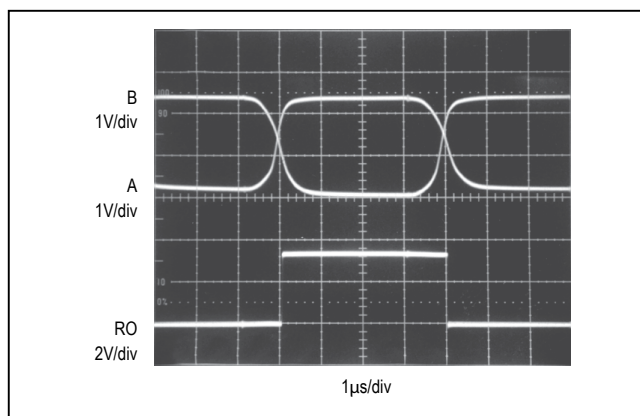


Figure 18. MAX3483E/MAX3488E Receiver Propagation Delay

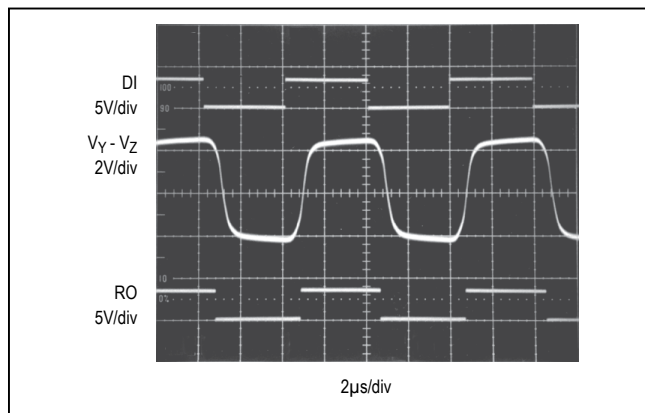


Figure 19. MAX3483E/MAX3488E System Differential Voltage at 125kHz Driving 4000 Feet of Cable

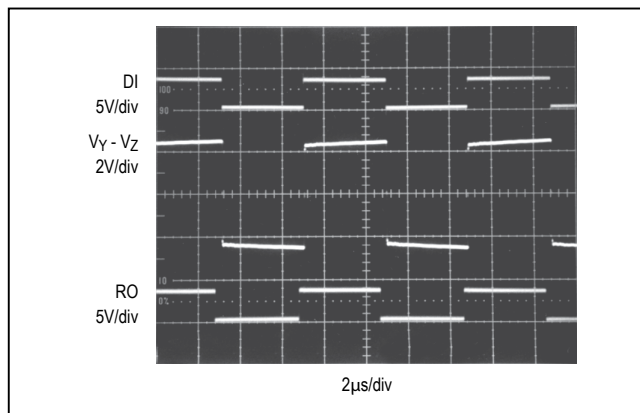


Figure 20. MAX3485E/MAX3490E/MAX3491E System Differential Voltage at 125kHz Driving 4000 Feet of Cable



MAX3483E/MAX3485E/  
MAX3486E/MAX3488E/  
MAX3490E/MAX3491E

3.3V-Powered,  $\pm 15\text{kV}$  ESD-Protected,  
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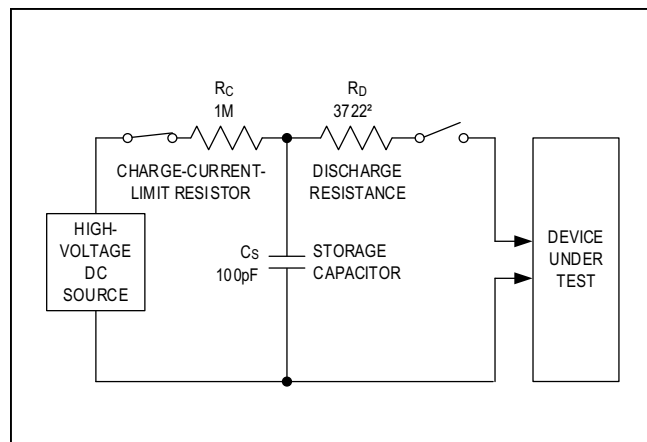


Figure 22a. Human Body ESD Test Model

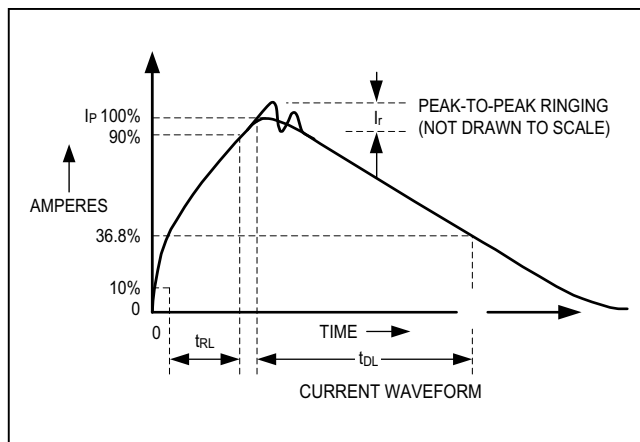


Figure 22b. Human Body Current Waveform

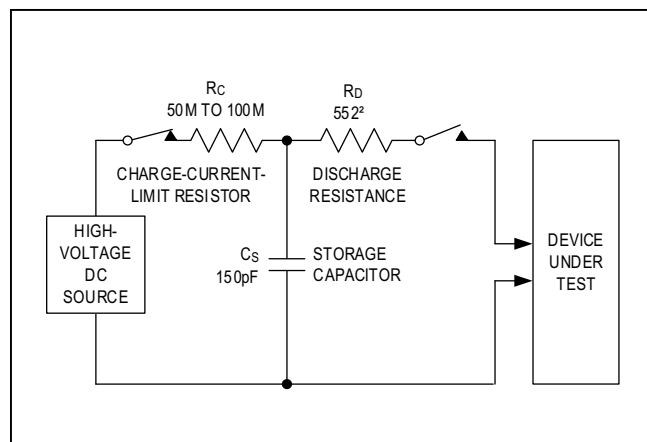


Figure 23a. IEC 1000-4-2 ESD Test Model

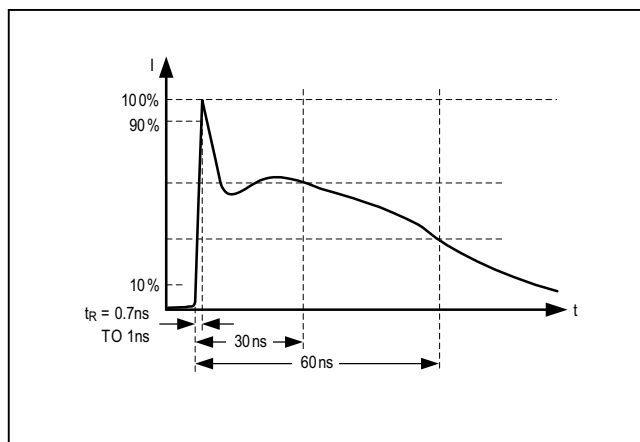


Figure 23b. IEC 1000-4-2 ESD Generator Current Waveform

The air-gap test involves approaching the device with a charged probe. The contact-discharge method connects the probe to the device before the probe is energized.

#### Machine Model

The Machine Model for ESD tests all pins using a 200pF storage capacitor and zero discharge resistance. Its objective is to emulate the stress caused when I/O pins are contacted by handling equipment during test and assembly. Of course, all pins require this protection, not just RS-485 inputs and outputs.

#### Typical Applications

The MAX3483E/MAX3485E/MAX3486E/MAX3488E/MAX3490E/MAX3491E transceivers are designed for bidirectional data communications on multipoint bus transmission lines. Figures 24 and 25 show typical network applications circuits. These parts can also be used as line repeaters, with cable lengths longer than 4000 feet, as shown in Figure 21.

To minimize reflections, the line should be terminated at both ends in its characteristic impedance, and stub lengths off the main line should be kept as short as possible. The slew-rate-limited MAX3483E/MAX3488E and the partially slew-rate-limited MAX3486E are more tolerant of imperfect termination.

MAX3483E/MAX3485E/  
MAX3486E/MAX3488E/  
MAX3490E/MAX3491E

3.3V-Powered,  $\pm 15\text{kV}$  ESD-Protected,  
12Mbps and Slew-Rate-Limited  
True RS-485/RS-422 Transceivers

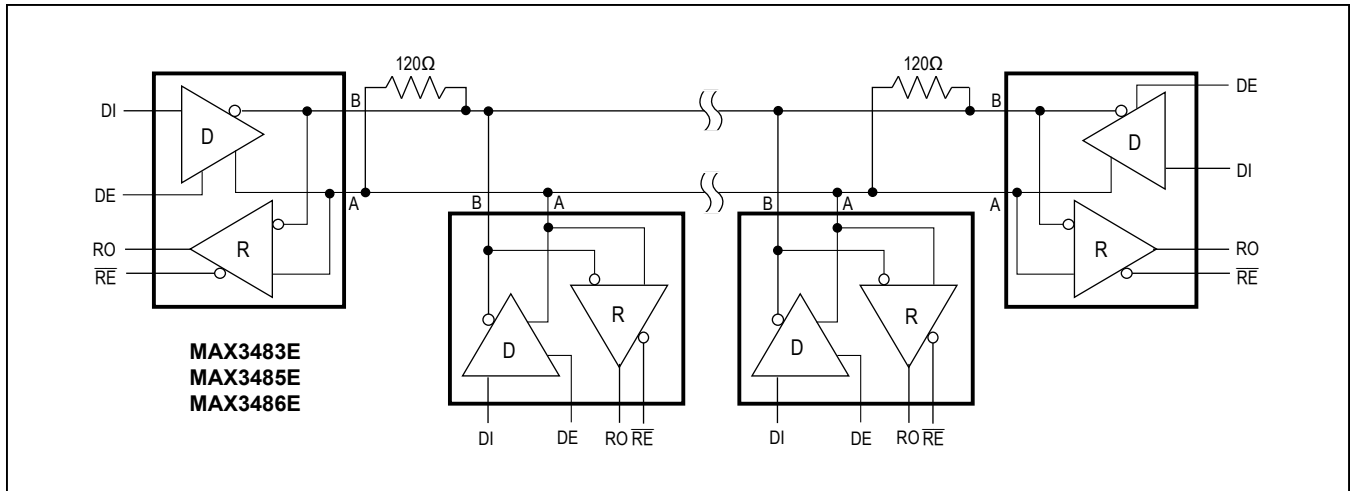


Figure 24. MAX3483E/MAX3485E/MAX3486E Typical RS-485 Network

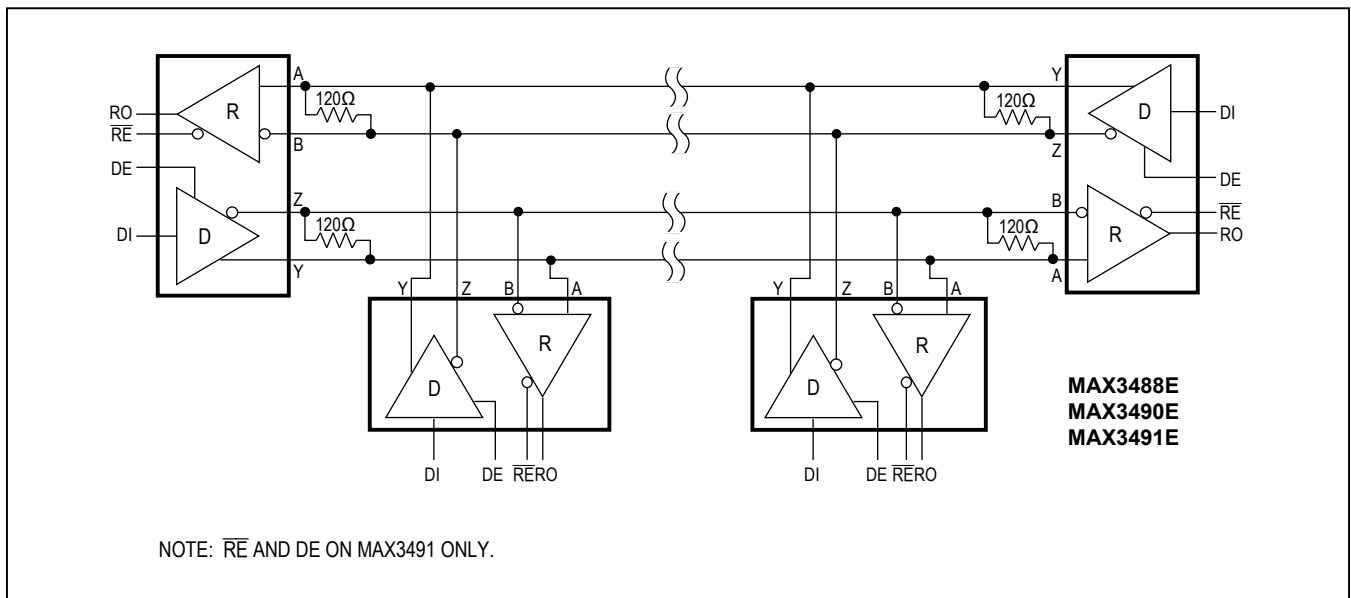


Figure 25. MAX3488E/MAX3490E/MAX3491E Full-Duplex RS-485 Network



MAX3483E/MAX3485E/  
MAX3486E/MAX3488E/  
MAX3490E/MAX3491E

3.3V-Powered,  $\pm 15\text{kV}$  ESD-Protected,  
12Mbps and Slew-Rate-Limited  
True RS-485/RS-422 Transceivers

### Ordering Information (continued)

| PART                | TEMP. RANGE    | PIN-PACKAGE    |
|---------------------|----------------|----------------|
| <b>MAX3485E</b> CSA | 0°C to +70°C   | 8 SO           |
| MAX3485ECPA         | 0°C to +70°C   | 8 Plastic DIP  |
| MAX3485EESA         | -40°C to +85°C | 8 SO           |
| MAX3485EEPA         | -40°C to +85°C | 8 Plastic DIP  |
| <b>MAX3486E</b> CSA | 0°C to +70°C   | 8 SO           |
| MAX3486ECPA         | 0°C to +70°C   | 8 Plastic DIP  |
| MAX3486EESA         | -40°C to +85°C | 8 SO           |
| MAX3486EEPA         | -40°C to +85°C | 8 Plastic DIP  |
| <b>MAX3488E</b> CSA | 0°C to +70°C   | 8 SO           |
| MAX3488ECPA         | 0°C to +70°C   | 8 Plastic DIP  |
| MAX3488EESA         | -40°C to +85°C | 8 SO           |
| MAX3488EEPA         | -40°C to +85°C | 8 Plastic DIP  |
| <b>MAX3490E</b> CSA | 0°C to +70°C   | 8 SO           |
| MAX3490ECPA         | 0°C to +70°C   | 8 Plastic DIP  |
| MAX3490EESA         | -40°C to +85°C | 8 SO           |
| MAX3490EEPA         | -40°C to +85°C | 8 Plastic DIP  |
| <b>MAX3491E</b> CSA | 0°C to +70°C   | 14 SO          |
| MAX3491ECPD         | 0°C to +70°C   | 14 Plastic DIP |
| MAX3491EESD         | -40°C to +85°C | 14 SO          |
| MAX3491EEPD         | -40°C to +85°C | 14 Plastic DIP |

### Chip Information

TRANSISTOR COUNT: 761

MAX3483E/MAX3485E/  
MAX3486E/MAX3488E/  
MAX3490E/MAX3491E

3.3V-Powered,  $\pm 15\text{kV}$  ESD-Protected,  
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True RS-485/RS-422 Transceivers

## Revision History

| REVISION<br>NUMBER | REVISION<br>DATE | DESCRIPTION                                                                    | PAGES<br>CHANGED |
|--------------------|------------------|--------------------------------------------------------------------------------|------------------|
| 0                  | 4/99             | Initial release                                                                | —                |
| 1                  | 5/19             | Updated <i>Absolute Maximum Ratings</i> and <i>Package Information</i> section | 2                |

For pricing, delivery, and ordering information, please visit Maxim Integrated's online storefront at <https://www.maximintegrated.com/en/storefront/storefront.html>.

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