MECHANICAL and PACKAGING

- CASE: Voidless hermetically sealed hard glass.
- TERMINALS: Tin-lead plate with >3% lead. Solder dip is available upon request.
- MARKING: Body painted and alpha numeric.
- POLARITY: Cathode indicated by band.
- Tape & Reel option: Standard per EIA-296. Consult factory for quantities.
- See Package Dimensions on last page.

PART NOMENCLATURE

JAN 1N6638 (e3)

Reliability Level
JAN = JAN Level
JANTX = JANTX Level
JANTXV = JANTXV Level
JANS = JANS Level
Blank = commercial

RoHS Compliance
e3 = RoHS compliant (available in commercial grade only)
Blank = non-RoHS compliant

JEDEC type number
See Electrical Characteristics table

SYMBOLS & DEFINITIONS

Symbol	Definition
V_{BR}	Minimum Breakdown Voltage: The minimum voltage the device will exhibit at a specified current.
V_{RWM}	Working Peak Reverse Voltage: The maximum peak voltage that can be applied over the operating temperature range.
V_F	Maximum Forward Voltage: The maximum forward voltage the device will exhibit at a specified current.
I_F	Forward Current: The forward current dc value, no alternating component.
I_R	Maximum Reverse Current: The maximum reverse (leakage) current that will flow at the specified voltage and temperature.
C	Capacitance: The capacitance in pF at a frequency of 1 MHz and specified voltage.
t_{rr}	Reverse Recovery Time: The time interval between the instant the current passes through zero when changing from the forward direction to the reverse direction and a specified recovery decay point after a peak reverse current is reached.

ELECTRICAL CHARACTERISTICS @ 25°C unless otherwise noted.

TYPE NUMBER	MAXIMUM FORWARD VOLTAGE V_F @ I_F		MAXIMUM DC REVERSE CURRENT				REVERSE RECOVERY TIME t_{rr} (Note 1)	MAXIMUM FORWARD RECOVERY VOLTAGE AND TIME I_F=200mA, t_r=1ns		MAXIMUM JUNCTION CAPACITANCE f = 1 MHz Vsig = 50 mV (p-p)	
			I_{R1}	I_{R2}	I_{R3}	I_{R4}					
			V_R= 20 V	V_R=V_{RWM}	V_R=20 V T_A= +150 °C	V_R=V_{RWM} T_A= +150 °C		V_{FRM}	t_{fr}	V_R=0 V	V_R=1.5 V
	V @ mA	V @ mA	nA	nA	µA	µA	ns	V	ns	pf	pf
1N6638	0.8 V @ 10 mA	1.1 V @ 200 mA	35	500	50	100	4.5	5.0	20	2.5	2.0
1N6642	0.8 V @ 10 mA	1.2 V @ 100 mA	25	500	50	100	5.0	5.0	20	5.0	2.8
1N6643	0.8 V @ 10 mA	1.2 V @ 100 mA	50	500	75	100	6.0	5.0	20	5.0	2.8

NOTE: 1. Reverse Recovery Time Test Conditions – I_F=I_R=10 mA, $I_{R(REC)}$ = 1.0 mA, C=3 pF, R_L = 100 ohms.

GRAPHS

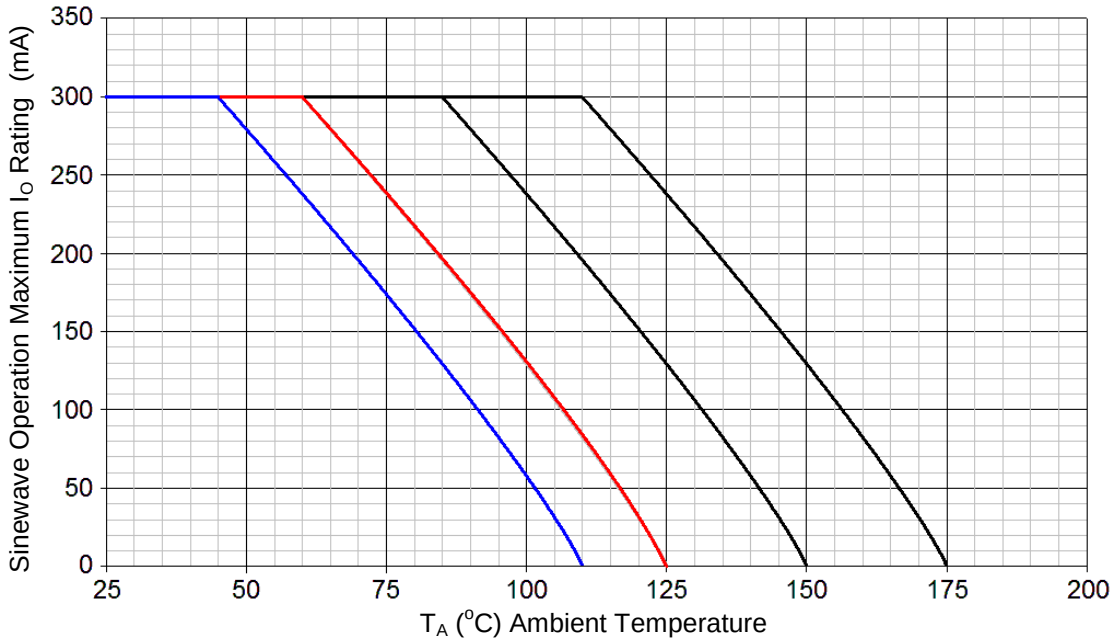


FIGURE 1
Temperature – Current Derating

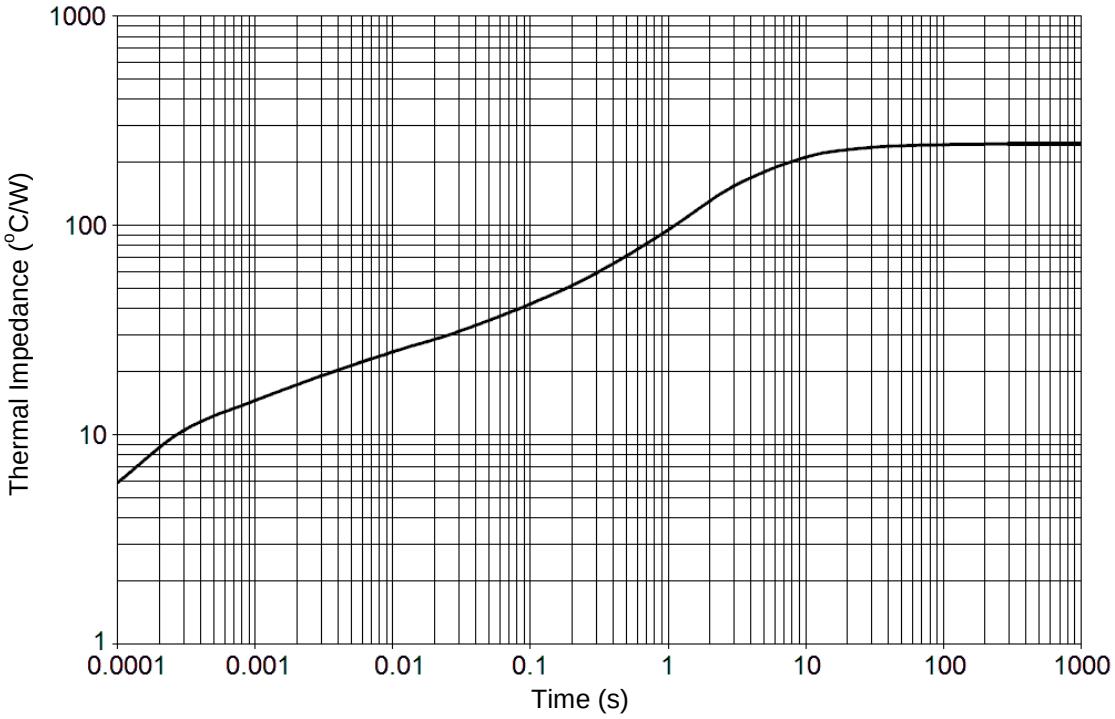


FIGURE 2
Maximum Thermal Impedance at T_A = 55 ^{o}C

GRAPHS (continued)

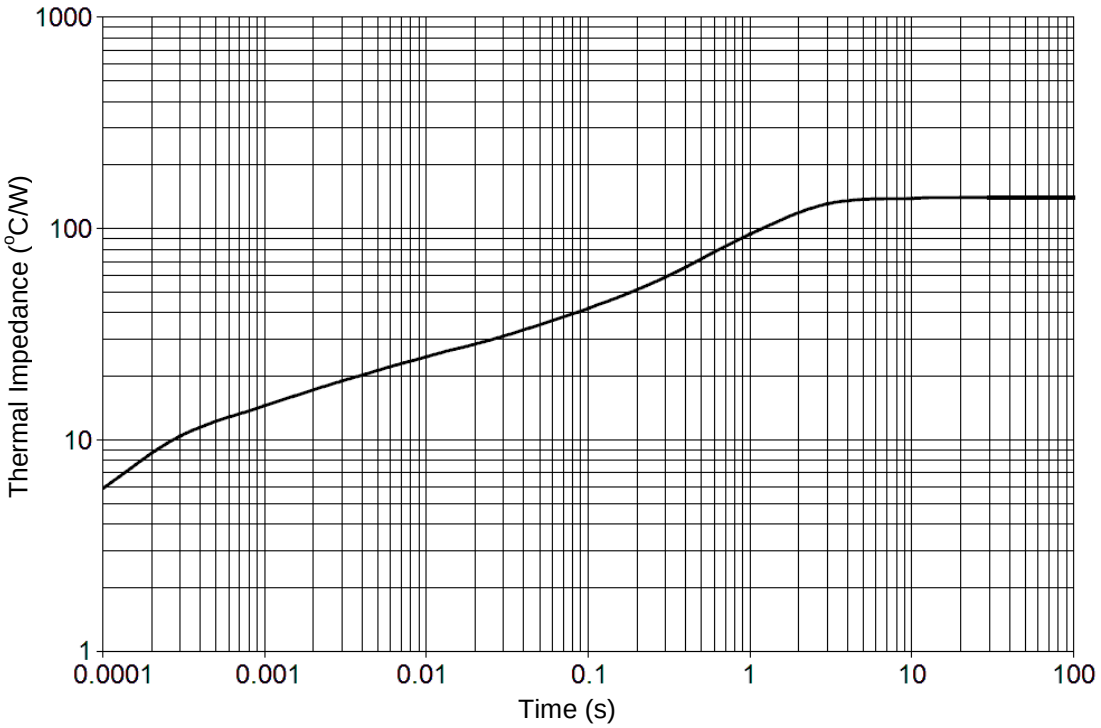
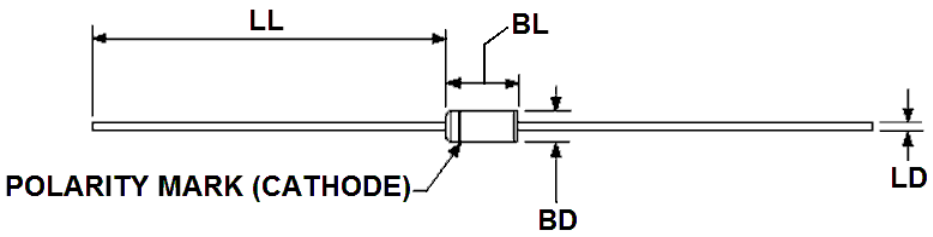


FIGURE 3

Maximum Thermal Impedance at T_L = 25 °C

PACKAGE DIMENSIONS



DIM	INCH		MILLIMETERS		NOTES
	MIN	MAX	MIN	MAX	
BD	0.056	0.080	1.42	2.03	2
BL	0.130	0.180	3.30	4.57	
LD	0.018	0.022	0.46	0.56	3
LL	1.00	1.50	25.40	38.10	

NOTES:

1. Dimensions are in inches. Millimeters are given for general information only.
2. Dimension BD shall be measured at the largest diameter.
3. The specified lead diameter applies in the zone between .050 inch (1.27 mm) from the diode body to the end of the lead. Outside of this zone lead shall not exceed BD.
4. In accordance with ASME Y14.5M, diameters are equivalent to Φx symbology.