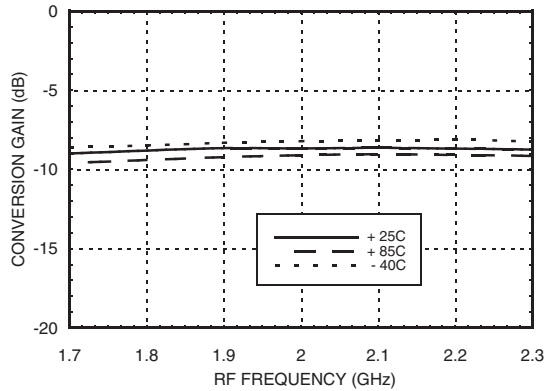
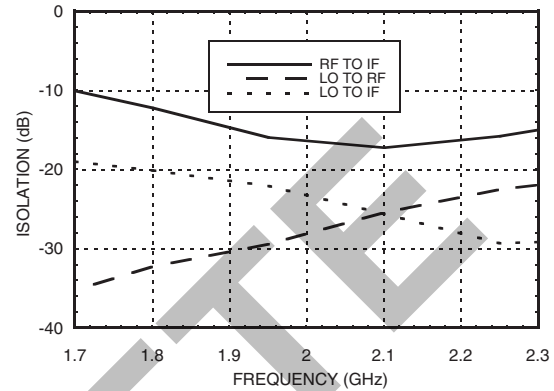




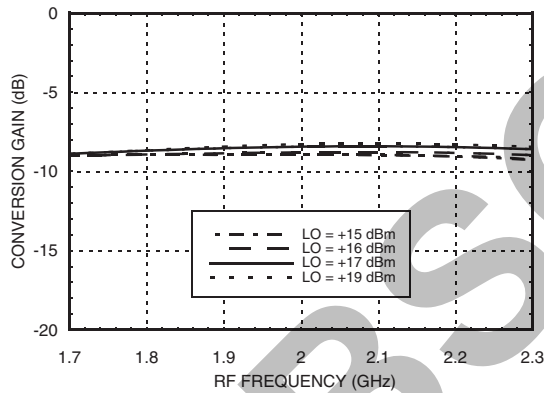
**Conversion Gain vs.  
Temperature @ LO = +17 dBm**



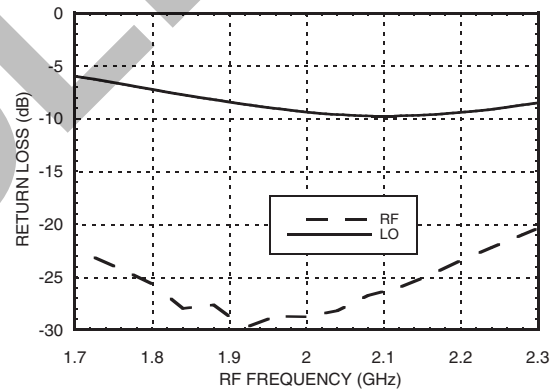
**Isolation @ LO = +17 dBm**



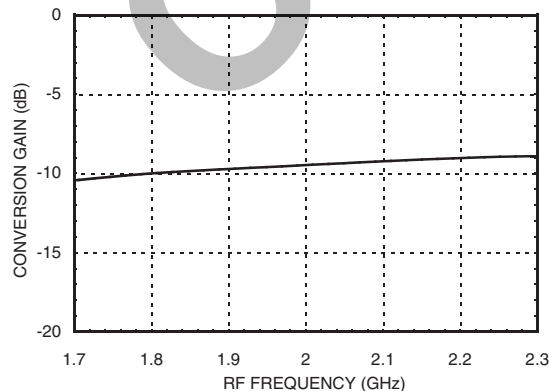
**Conversion Gain vs. LO Drive**



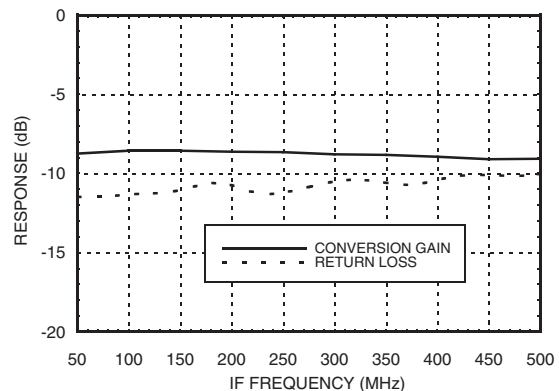
**Return Loss @ LO = +17 dBm**



**Upconverter Performance  
Conversion Gain @ LO = +17 dBm**



**If Bandwidth @ LO = +17 dBm**



\*Unless otherwise noted, all measurements performed as a downconverter, with low side LO & IF = 200 MHz.

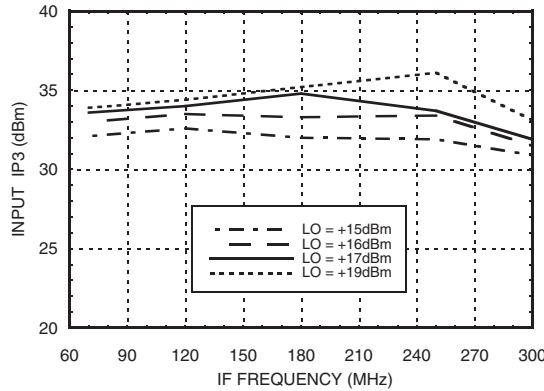
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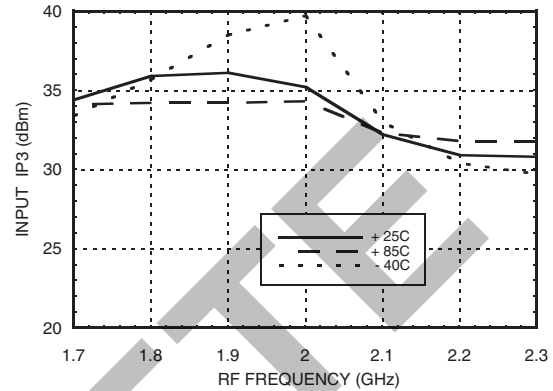


**HIGH IP3 GaAs MMIC  
MIXER, 1.7 - 2.2 GHz**

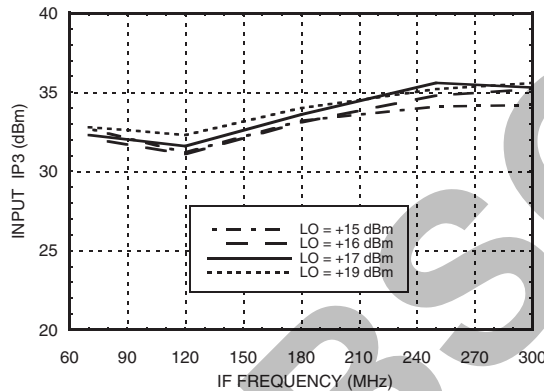
**Input IP3 vs.  
IF Frequency, RF = 1.75 GHz**



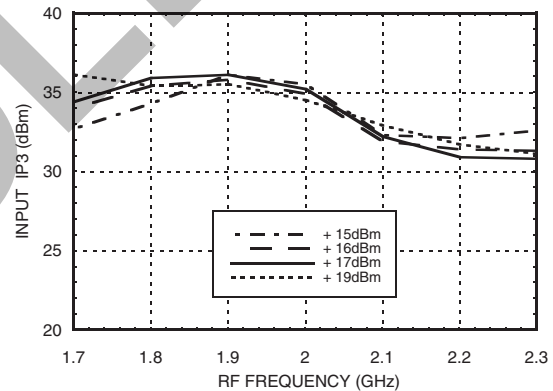
**Input IP3 vs.  
Temperature, LO = +17 dBm**



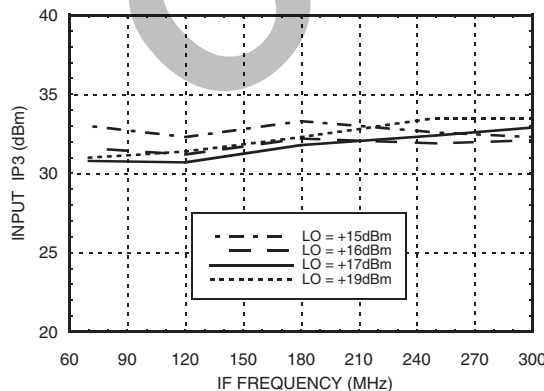
**Input IP3 vs.  
IF Frequency, RF = 1.95 GHz**



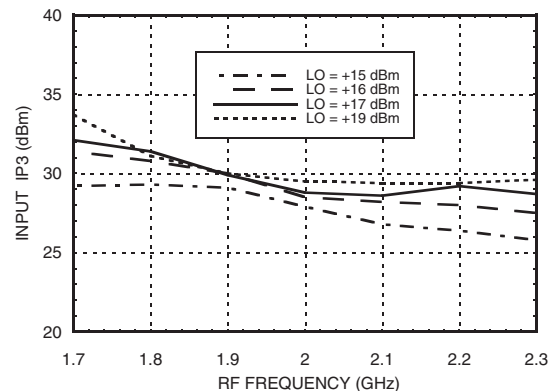
**Input IP3 vs. LO Drive**



**Input IP3 vs.  
IF Frequency, RF = 2.15 GHz**



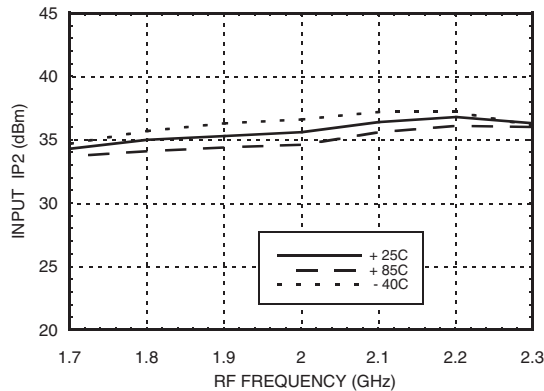
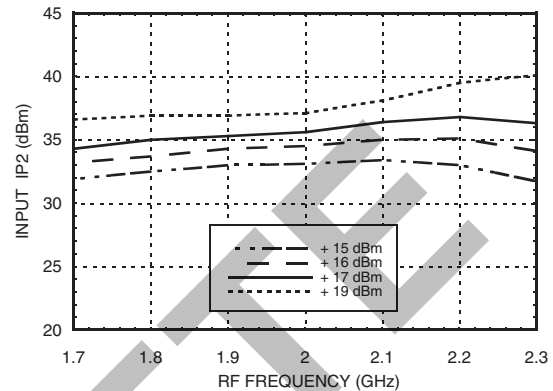
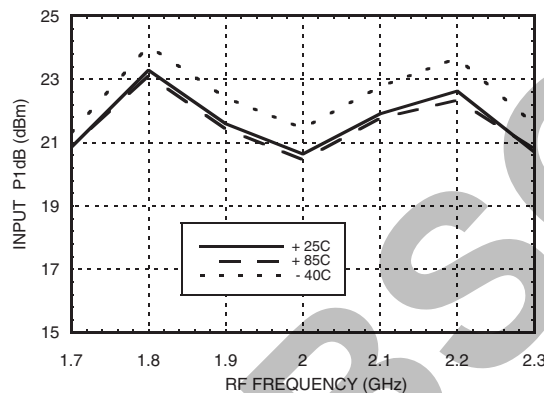
**Upconverter IP3 vs.  
LO Drive, IF = 200 MHz**



\*Unless otherwise noted, all measurements performed as a downconverter, with low side LO & IF = 200 MHz.

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**Input IP2 vs.  
Temperature @ LO = +17 dBm**

**Input IP2 vs. LO Drive @ LO = +17 dBm**

**Input P1dB vs.  
Temperature @ LO = +17 dBm**

**MxN Spurious Outputs**

mRF	nLO				
	0	1	2	3	4
0	xx	-11	7	4	8
1	9	0	24	31	27
2	71	70	49	58	64
3	79	80	80	79	77
4	77	80	80	79	80

RF Freq = 2 GHz @ -10 dBm  
LO Freq = 1.8 GHz @ +17 dBm  
All values in dBc relative to the IF output power.

**Harmonics of LO**

LO Freq (GHz)	nLO Spur @ RF Port			
	1	2	3	4
1.4	42	26	56	46
1.55	33	25	56	53
1.7	29	29	49	50
1.85	26	31	44	53
2	24	36	44	48
2.15	21	38	43	49

LO = +17 dBm  
All values are in dBc below input LO level @ RF port.

**Absolute Maximum Ratings**

RF/IF Input	+27 dBm
LO Drive	+27 dBm
Storage Temperature	-65 to +150 °C
Operating Temperature	-40 to +85 °C
IF DC Current	±40 mA



**ELECTROSTATIC SENSITIVE DEVICE  
OBSERVE HANDLING PRECAUTIONS**

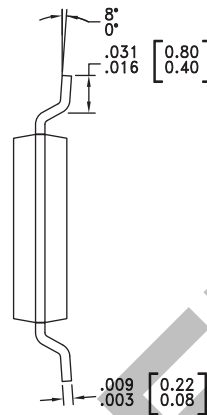
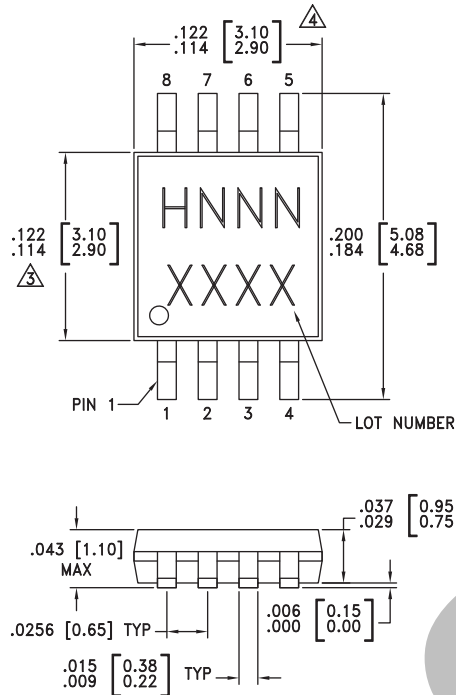
\*Unless otherwise noted, all measurements performed as a downconverter, with low side LO & IF = 200 MHz.

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## Outline Drawing



NOTES:

1. LEADFRAME MATERIAL: COPPER ALLOY
2. DIMENSIONS ARE IN INCHES [MILLIMETERS]
3. DIMENSION DOES NOT INCLUDE MOLDFLASH OF 0.15mm PER SIDE.
4. DIMENSION DOES NOT INCLUDE MOLDFLASH OF 0.25mm PER SIDE.
5. ALL GROUND LEADS MUST BE SOLDERED TO PCB RF GROUND.

## Package Information

Part Number	Package Body Material	Lead Finish	MSL Rating	Package Marking <sup>[3]</sup>
HMC400MS8	Low Stress Injection Molded Plastic	Sn/Pb Solder	MSL1 <sup>[1]</sup>	H400 XXXX
HMC400MS8E	RoHS-compliant Low Stress Injection Molded Plastic	100% matte Sn	MSL1 <sup>[2]</sup>	<u>H400</u> XXXX

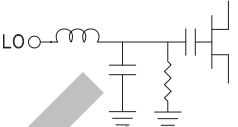

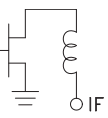
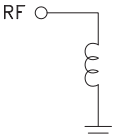
[1] Max peak reflow temperature of 235 °C

[2] Max peak reflow temperature of 260 °C

[3] 4-Digit lot number XXXX

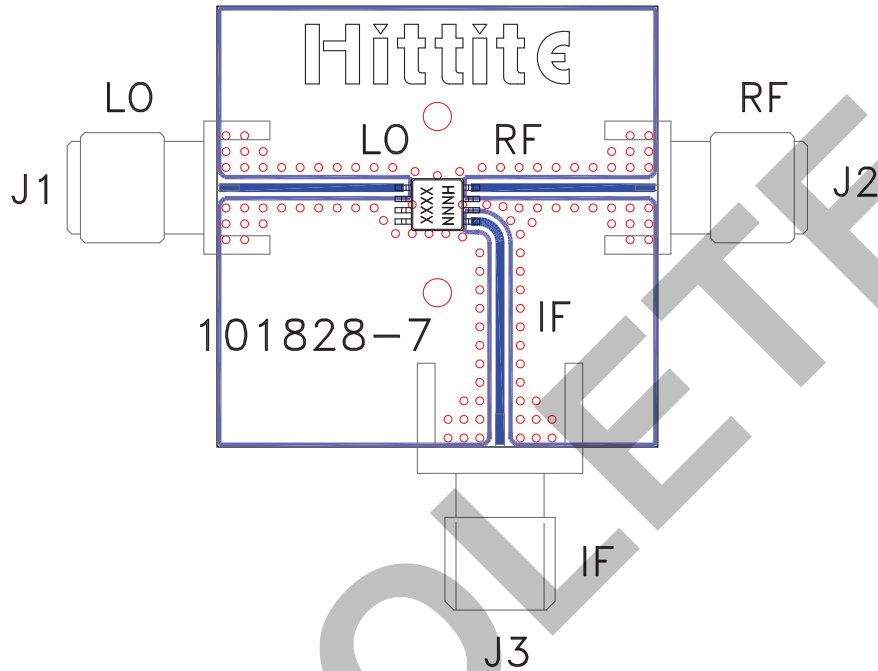


### Pin Descriptions

Pin Number	Function	Description	Interface Schematic
1	LO	This pin is AC coupled & matched to 50 Ohms from 1.4 to 2.2 GHz. Blocking capacitors are required if line potential is not equal to 0V.	
2, 4	N/C	Not connected.	
3, 6, 7	GND	This pin must be connected to RF ground.	
5	IF Port	This pin is DC coupled. For applications not requiring operation to DC this port should be DC blocked externally using a series capacitor. Choose value of capacitor to pass IF frequency desired. For operation to DC, this pin must not sink/source more than 40 mA of current or failure may result.	
8	RF Port	This pin is DC coupled & matched to 50 Ohm from 1.7 to 2.2 GHz	



**Evaluation PCB**



**List of Materials for Evaluation PCB 101830 <sup>[1]</sup>**

Item	Description
J1 - J3	PCB Mount SMA RF Connector
U1	HMC400MS8 / HMC400MS8E Mixer
PCB <sup>[2]</sup>	101828 Eval Board

[1] Reference this number when ordering complete evaluation PCB

[2] Circuit Board Material: Rogers 4350

The circuit board used in the final application should use RF circuit design techniques. Signal lines should have 50 ohm impedance while the package ground leads and exposed paddle should be connected directly to the ground plane similar to that shown. A sufficient number of via holes should be used to connect the top and bottom ground planes. The evaluation circuit board shown is available from Hittite upon request.

**Notes:**

OBSOLETE