HFA16TA60C

Vishay High Power Products

HEXFRED® Ultrafast Soft Recovery Diode, 2 x 8 A



ELECTRICAL SPECIFICATIONS PER LEG (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Cathode to anode breakdown voltage	V _{BR}	Ι _R = 100 μΑ		600	-	-	
Maximum forward voltage		I _F = 8 A	See fig. 1 -	=	1.4	1.7	V
	V _{FM}	I _F = 16 A		=	1.7	2.1	
		I _F = 8 A, T _J = 125 °C		-	1.4	1.7	
Maximum reverse leakage current		$V_R = V_R$ rated	See fig. 2	=	0.3	5.0	- μΑ
	I _{RM}	T _J = 125 °C, V _R = 0.8 x V _R rated		=	100	500	
Junction capacitance	C _T	V _R = 200 V	See fig. 3	=	10	25	pF
Series inductance	L _S	Measured lead to lead 5 mm from package body - 8.0 -		-	nH		

DYNAMIC RECOVERY CHARACTERISTICS PER LEG (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Reverse recovery time See fig. 5 and 10	t _{rr}	$I_F = 1.0 \text{ A}, dI_F/dt = 200 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	18	-	
	t _{rr1}	T _J = 25 °C	I _F = 8.0 A dI _F /dt = 200 A/μs V _R = 200 V	-	37	55	ns
	t _{rr2}	T _J = 125 °C		-	55	90	
Peak recovery current See fig. 6	I _{RRM1}	T _J = 25 °C		-	3.5	5.0	Α
	I _{RRM2}	T _J = 125 °C		-	4.5	8.0	
Reverse recovery charge See fig. 7	Q _{rr1}	T _J = 25 °C		-	65	138	nC
	Q _{rr2}	T _J = 125 °C		-	124	360	
Peak rate of fall recovery current during t_b See fig. 8	dI _{(rec)M} /dt1	T _J = 25 °C		-	240	-	A/μs
	dI _{(rec)M} /dt2	T _J = 125 °C		-	210	-	Ανμδ

THERMAL - MECHANICAL SPECIFICATIONS PER LEG							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Lead temperature	T _{lead}	0.063" from case (1.6 mm) for 10 s	-	-	300	°C	
Junction to case, single leg conducting			-	-	3.5		
Junction to case, both legs conducting	R _{thJC}		-	-	1.75	K/W	
Thermal resistance, junction to ambient	R _{thJA}	Typical socket mount	-	-	80		
Thermal resistance, case to heatsink	R _{thCS}	Mounting surface, flat, smooth and greased	-	0.5	-		
Weight			-	2.0	-	g	
			-	0.07	-	oz.	
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)	
Marking device		Case style TO-220AB	HFA16TA60C				

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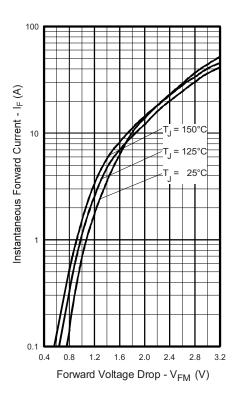


Fig. 1 - Maximum Forward Voltage Drop vs. Instantaneous Forward Current (Per Leg)

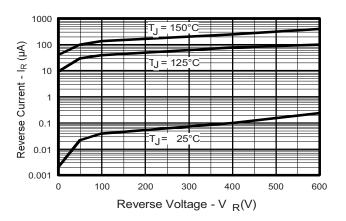


Fig. 2 - Typical Reverse Current vs. Reverse Voltage (Per Leg)

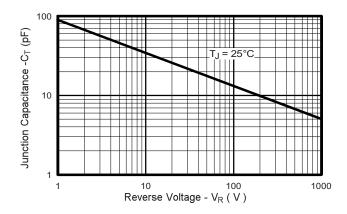


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

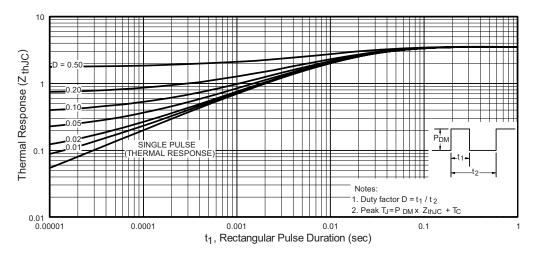


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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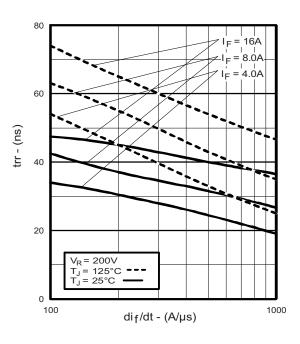


Fig. 5 - Typical Reverse Recovery Time vs. dI_F/dt (Per Leg)

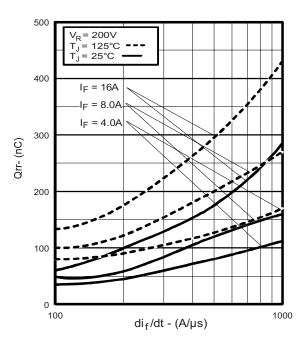


Fig. 7 - Typical Stored Charge vs. dl_F/dt (Per Leg)

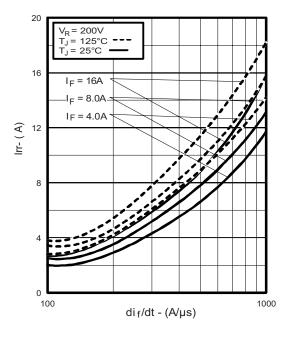


Fig. 6 - Typical Recovery Current vs. dl_F/dt (Per Leg)

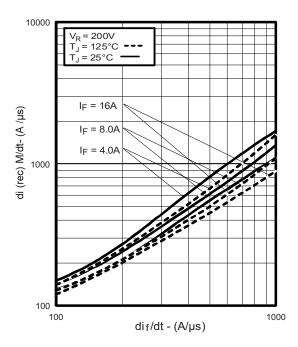


Fig. 8 - Typical $dI_{(rec)M}/dt$ vs. dI_F/dt (Per Leg)

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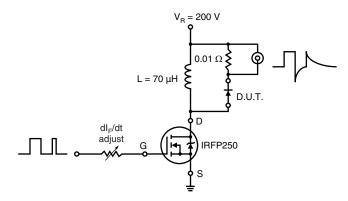
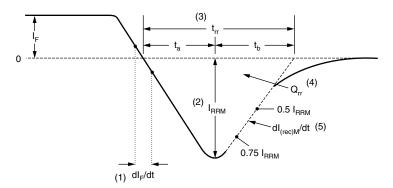


Fig. 9 - Reverse Recovery Parameter Test Circuit



- (1) dl_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) $\rm t_{rr}$ reverse recovery time measured from zero crossing point of negative going $\rm I_F$ to point where a line passing through 0.75 $\rm I_{RRM}$ and 0.50 $\rm I_{RRM}$ extrapolated to zero current.
- (4) \mathbf{Q}_{rr} area under curve defined by \mathbf{t}_{rr} and \mathbf{I}_{RRM}

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5) $dI_{(rec)M}/dt$ - peak rate of change of current during t_b portion of t_{rr}

Fig. 10 - Reverse Recovery Waveform and Definitions

LINKS TO RELATED DOCUMENTS				
Dimensions	http://www.vishay.com/doc?95222			
Part marking information	http://www.vishay.com/doc?95225			

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